Saurabh Shrikant Labde

COMPUTER ARCHITECTURE · DIGITAL DESIGN · VERIFICATION · SYSTEM SOFTWARE

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Education

North Carolina State University   *Raleigh, NC*

M.S. IN ELECTRICAL AND COMPUTER ENGINEERING   *Aug. 2017 - Present*

- **Relevant Courses:** Microprocessor Architecture, Architecture of Parallel Computers , Advanced Architecture with Data Parallel Processors, ASIC Design with Verilog, ASIC Verification with System Verilog, Advanced Verification with UVM, Operating Systems Design, Compiler Design and Optimization

University Of Mumbai   *Mumbai, India*

B.S. IN ELECTRONICS AND TELECOMMUNICATIONS ENGINEERING   *Aug. 2013 - May.2017*

- **Relevant Courses:** Digital Electronics, VLSI System Design, Artificial Neural Networks , Analog Electronics, Microprocessors and Microcontroller

Skills

Languages	C, C++, Verilog, System Verilog, Python, Ruby, Perl, Assembly, Shell , Java, Ruby on Rails, SQL, CUDA
Tools	ModelSim, QuestaSim, Synopsys Design Vision,MATLAB, Xilinx ISE, Git, Make Utility, GDB, Valgrind, LLDB
OS and Packages	Linux/Unix, Windows, XINU-OS, GPGPU-sim, SNIPER-sim, GEM5-sim
Design	Logic Design, RTL Design, Synthesis, Clock Domain Crossing, Static Timing Analysis,FSM, Low Power Design
Verification	UVM Framework, Functional Verification , Assertions, Coverage, Constrained Random testing, Test Bench Design
CPU/GPU Architecture	Multilevel Cache, Branch Prediction,Superscalar processors, vector processors, SIMD, VLIW, Cache Coherence

Work Experience

ARPERS Research Group - NC State University   *Raleigh, NC*

RESEARCH STUDENT   *Jun. 2017 - Aug. 2018*

- Ported the Whisper data structures micro benchmark suite to work with SNIPER multicore simulator.
- Co created and debugged wrapper classes for all the benchmarks.
- Analysied the varying performance of the benchmarks through visualizations generated with scripts.