

COMPUTER ARCHITECTURE · DESIGN VERIFICATION

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Education

North Carolina State University

Raleigh, NC

M.S. IN ELECTRICAL AND COMPUTER ENGINEERING

Aug. 2017 - Present

• Relevant Courses: Microprocessor Architecture, Architecture of Parallel Computers, Advanced Architecture with Data Parallel Processors, ASIC Design with Verilog, ASIC Verification with System Verilog, Advanced Verification with UVM, Operating Systems

University Of Mumbai Mumbai, India

B.S. IN ELECTRONICS AND TELECOMMUNICATIONS ENGINEERING

Aug. 2013 - May.2017

• Relevant Courses: Digital Electronics, VLSI System Design, Artificial Neural Networks , Analog Electronics, Microprocessors and Microcontroller architectures

JANUARY 3, 2019 SAURABH LABDE · RÉSUMÉ 1