

## Expansion Connector (superset of mbed pinning)

mbed	LPCXpresso		
GND	GND	100	GNDX
VIN (4.5-14V)	VIN (4.5-5.5V)		EXT_PC
VB (battery supply)	VB (batt	ery supply)	VB
nR (reset)	RESET_	N	RESET
SPI1-MOSI	P0.9	MOSI1	_P0[9]
SPI1-MISO	P0.8	MISO1	_P0[8]
SPI1-SCK	P0.7	SCK1	_P0[7]
GPIO	P0.6	SSEL1	P0[6]
UART1-TX / I2C1-SDA	P0.0	TXD3/SDA1	P0[0]
UART1-RX / I2C1-SCL	P0.1	RXD3/SCL1	P0[1]
SPI2-MOSI	P0.18	MOSI0	P0[18]
SPI2-MISO	P0.17	MISO0	_P0[17]
SPI2-SCL / UART2-TX	P0.15	TXD1/SCK0	P0[15]
UART2-RX	P0.16	RXD1/SSEL0	_P0[16]
AINO	P0.23	AD0.0	P0[23]
AIN1	P0.24	AD0.1	P0[24]
AIN2	P0.25	AD0.2	P0[25]
AIN3 / AOUT	P0.26	AD0.3/AOUT	P0[26]
AIN4	P1.30	AD0.4	P1[30]
AIN5	P1.31	AD0.5	P1[31]
	P0.2		P0[2]
	P0.3		P0[3]
	P0.21		_P0[21]
	P0.22		P0[22]
	P0.27		_P0[27]
	P0.28		_P0[28]
	P2.13		P2[13]

	Dual row ho	eles (2x27), 100 mil sp	acing					
GNDX	<b>—C</b> J6-1						J6-28 >	VIO_3V3X
_EXT_POWX	<b>—(</b> J6-2						J6-29 <b>)</b> —	
_VB	<b>—C</b> J6-3						J6-30 <b>&gt;</b> —	
RESET_N	<b>—(</b> J6-4						J6-31 <b>)</b> —	
P0[9]	<b>(</b> J6-5						J6-32 <b>)</b> ——	RD-
P0[8]	<b></b> J6-6						J6-33 <b>)</b>	RD+
P0[7]	<b>(</b> J6-7						J6-34 <b>)</b> —	TD-
P0[6]	<b></b> J6-8						J6-35 <b>)</b>	TD+
P0[0]	<b></b> J6-9						J6-36 <b>)</b> —	USB-D-
P0[1]	<b></b> J6-10						J6-37 <b>)</b>	USB-D+
P0[18]	<b></b> J6-11						J6-38 <b>)</b> ——	P0[4]
P0[17]	<b></b> J6-12						J6-39 <b>)</b>	P0[5]
P0[15]	<b>(</b> J6-13						J6-40 <b>)</b>	P0[10]
P0[16]	<b></b> J6-14						J6-41 <b>)</b> ——	P0[11]
P0[23]	<b>(</b> J6-15						J6-42 <b>)</b> ——	P2[0]
P0[24]	<b>C</b> J6-16						J6-43 <b>)</b> ——	P2[1]
P0[25]	<b>C</b> J6-17						J6-44 <b>)</b>	P2[2]
P0[26]	<b>(</b> J6-18						J6-45 <b>)</b> —	P2[3]
P1[30]	<b>(</b> J6-19						J6-46 <b>&gt;</b>	P2[4]
P1[31]	<b>(</b> J6-20						J6-47 <b>)</b> —	P2[5]
P0[2]	<b>(</b> J6-21						J6-48 <b>)</b>	P2[6]
P0[3]	<b>(</b> J6-22						J6-49 <b>&gt;</b>	P2[7]
P0[21]	<b>(</b> J6-23						J6-50 <b>)</b>	P2[8]
P0[22]	<b>C</b> J6-24	<b>⊘</b> PAD19					J6-51 <b>)</b>	P2[10]
P0[27]	<b>(</b> J6-25	PAD18 □	■ PAD15 r	■PAD12 F	<b>⊘</b> PAD9	■ PAD6 -	■PAD3 J6-52 >	P2[11]
P0[28]	<b>C</b> J6-26	PAD17	PAD14	PAD11	PAD8	PAD5	■ PAD2 J6-53 >	P2[12]
P2[13]	<b>(</b> J6-27	PAD16	PAD13	PAD10	PAD7	PAD4	PAD1 J6-54 >	GNDX
ļ	P2[9] P0[20] P0[19]	P4[28] P3[26] P3[25]	P1[29] P1[28] P1[27]	P1[26] P1[25] P1[24]	P1[23] P1[22] P1[21]	P1[20] P1[19]		

LPCX	(presso	mbed		
VOUT (+3.3	V out) if self se +3.3V input	VOUT (3.3V out)		
not used	Se +3.3V Iliput	VU (5.0V USB out)		
not used		IF+		
not used		IF-		
RD-		RD- (Ethernet)		
RD+		RD+ (Ethernet)		
TD-		TD- (Ethernet)		
TD+		TD+ (Ethernet)		
USB-D-		D- (USB)		
USB-D+		D+ (USB)		
P0.4	CAN_RX2	CAN-RD		
P0.5	CAN_TX2	CAN-TD		
P0.10	TXD2/SDA2	UART3-TX / I2C2-SDA		
P0.11	RXD2/SCL2	UART3-RX / I2C2-SCL		
P2.0	PWM1.1	PWMOUT0		
P2.1	PWM1.2	PWMOUT1		
P2.2	PWM1.3	PWMOUT2		
P2.3	PWM1.4	PWMOUT3		
P2.4	PWM1.5	PWMOUT4		
P2.5	PWM1.6	PWMOUT5		
P2.6				
P2.7				
P2.8				
P2.10				
P2.11				
P2.12				
GND				



(C) Embedded Artists AB

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## LPC1769 DISPLAY UNIT



LPC 1769

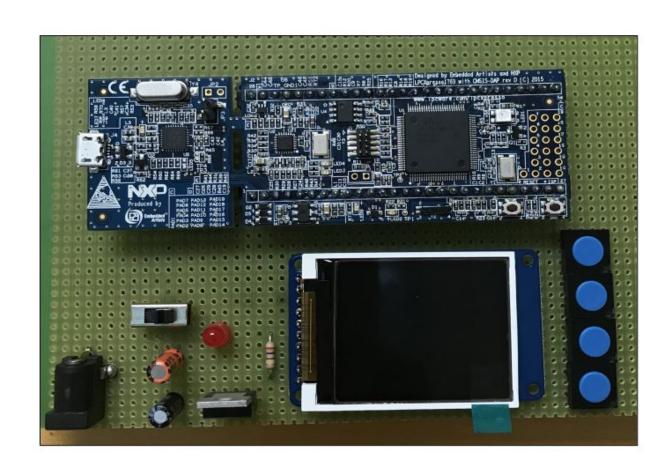
Color TFT LCD display

Resolution: 128x160,

Pixel Depth: 18-bit (262,144) colors

Controller: ST7735 Interface: SPI interface

LCD Pins	LPC1769 Pins
Gnd	Gnd
VCC	VCC (3.3V)
RST	GPIO output (P0.22)
RS/DC	GPIO output (P0.21)
CARD_CS	X
TFT_CS	SSEL0 (P0.16)
MOSI	SSP0 MOSI (P0.18)
SCK	SCK0 (P0.15)
MISO	SSP0 MISO (P0.17)
LITE	VCC (3.3V)



Note from Harry Li: for CMPE127 class, ignore 4 button switch, just connect SPI LCD.

Reference: CTI One Corporation