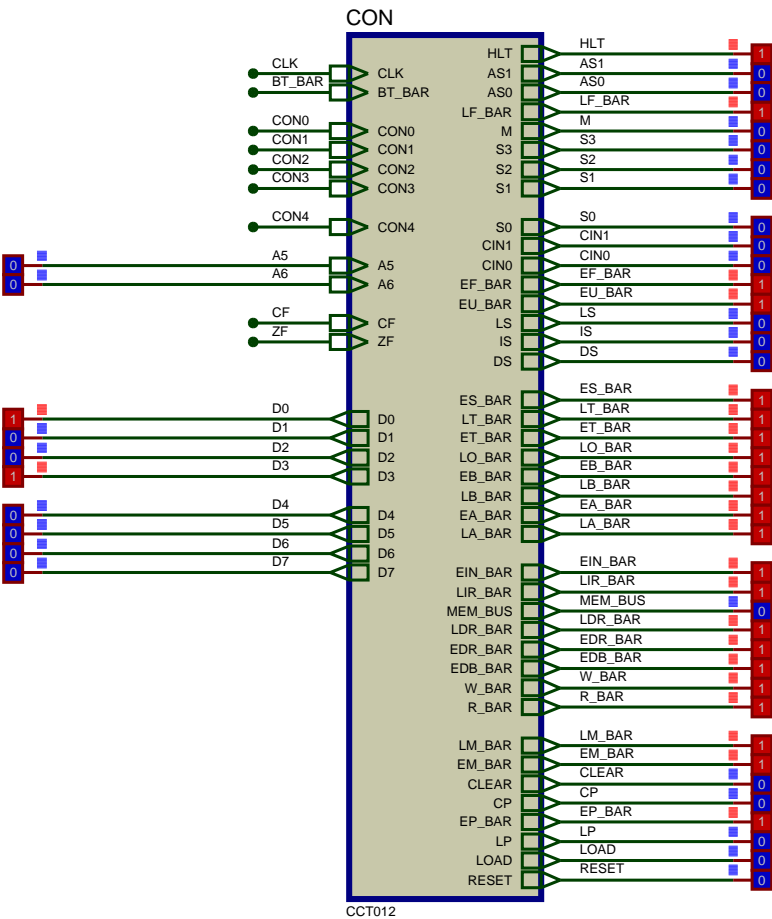
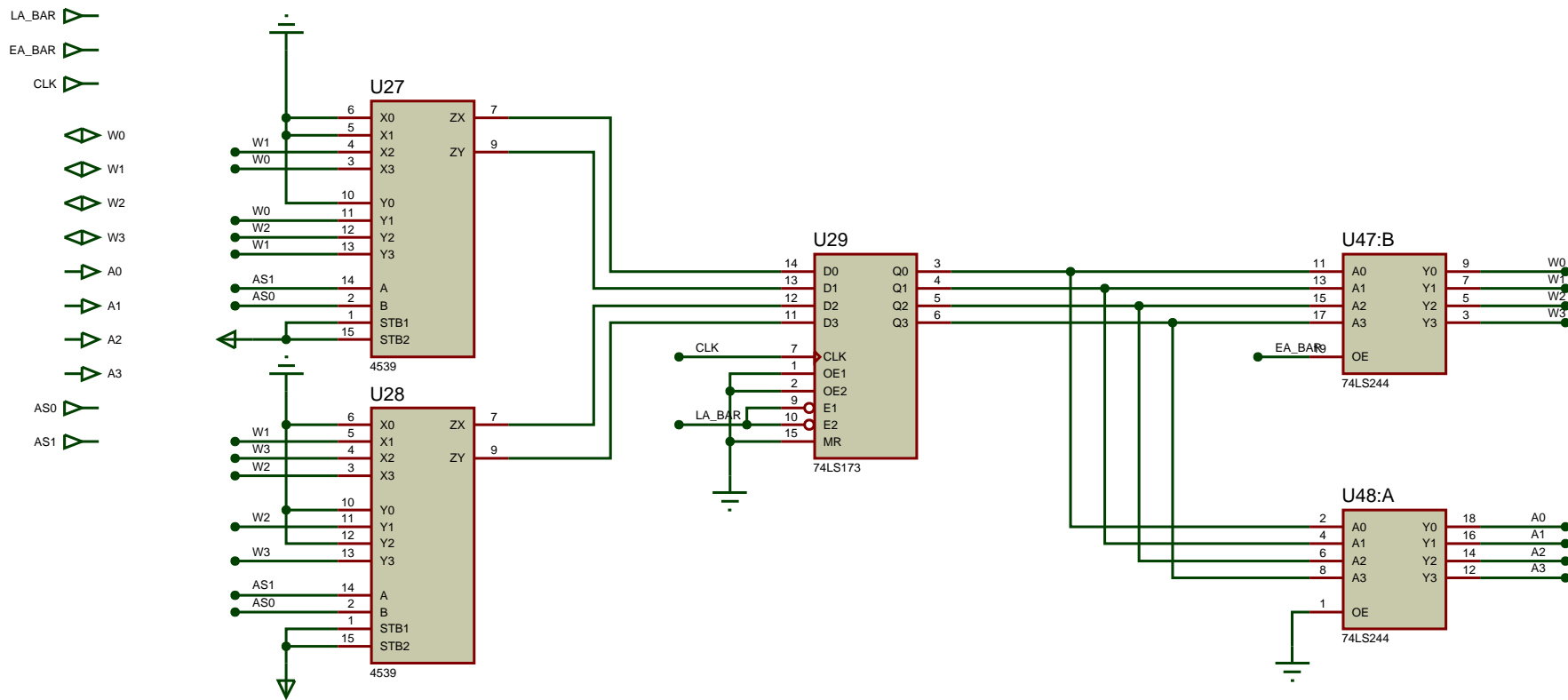


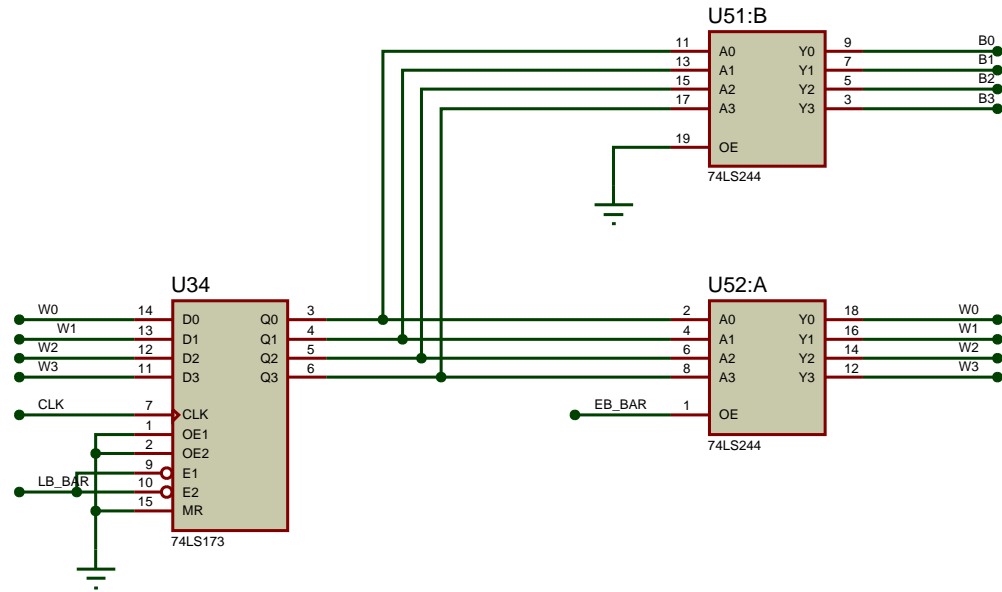
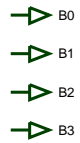
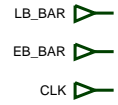
CLK



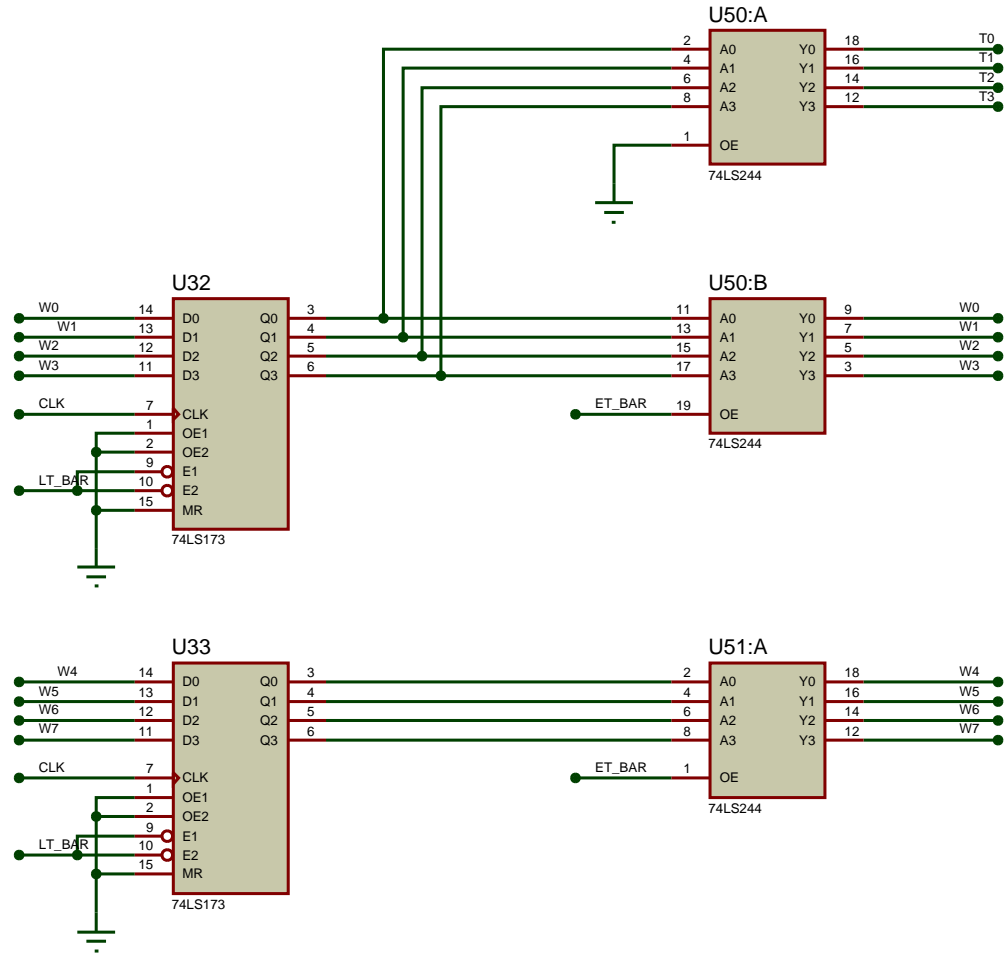
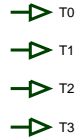
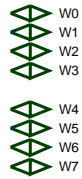
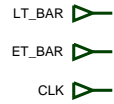
4 BIT PC



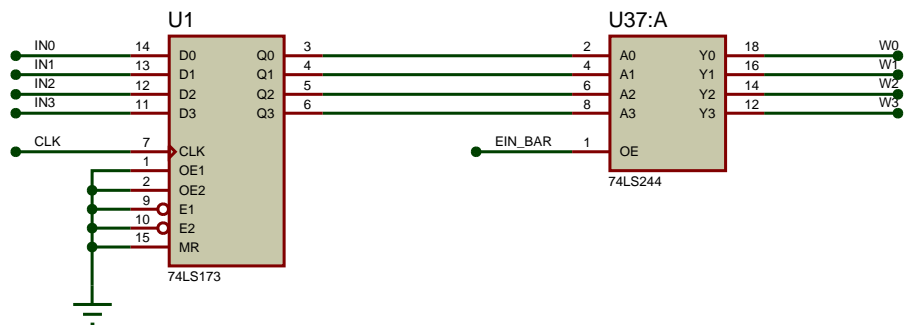
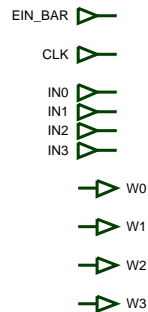
ACCUMULATOR



B_REG



TEMP



INPUT

LO_BAR

CLK

W0

W1

W2

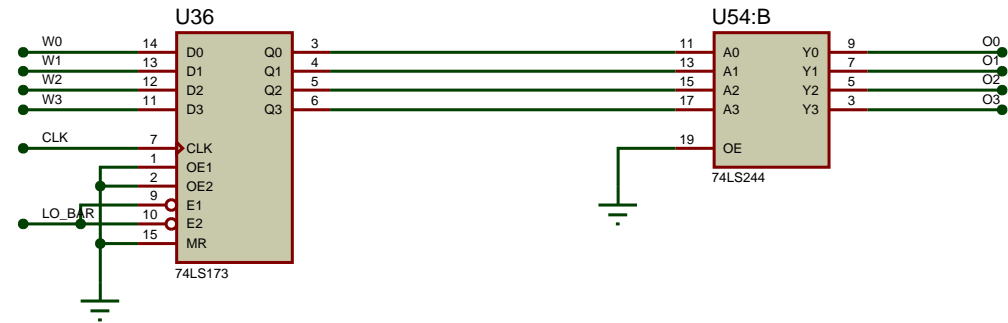
W3

O0

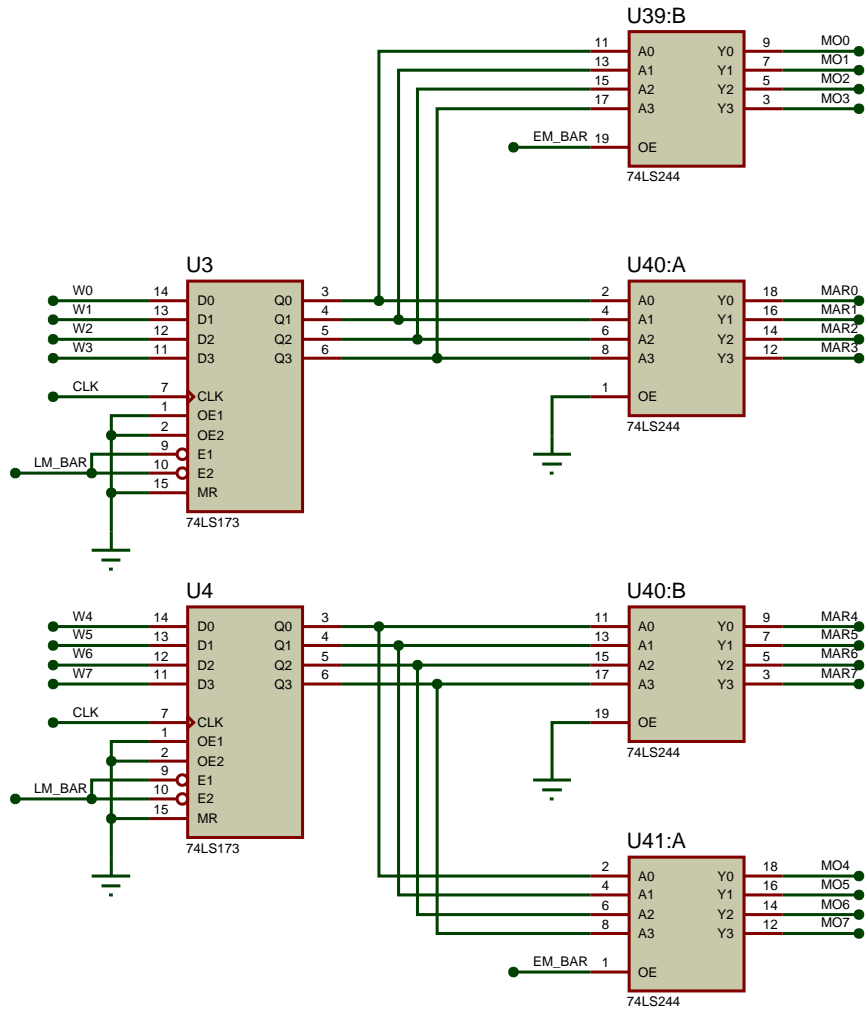
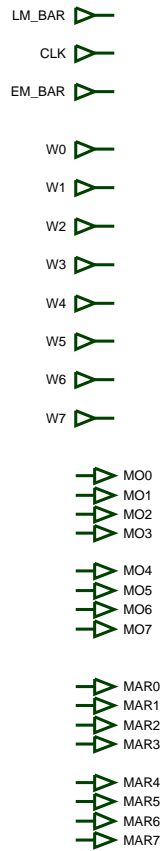
O1

O2

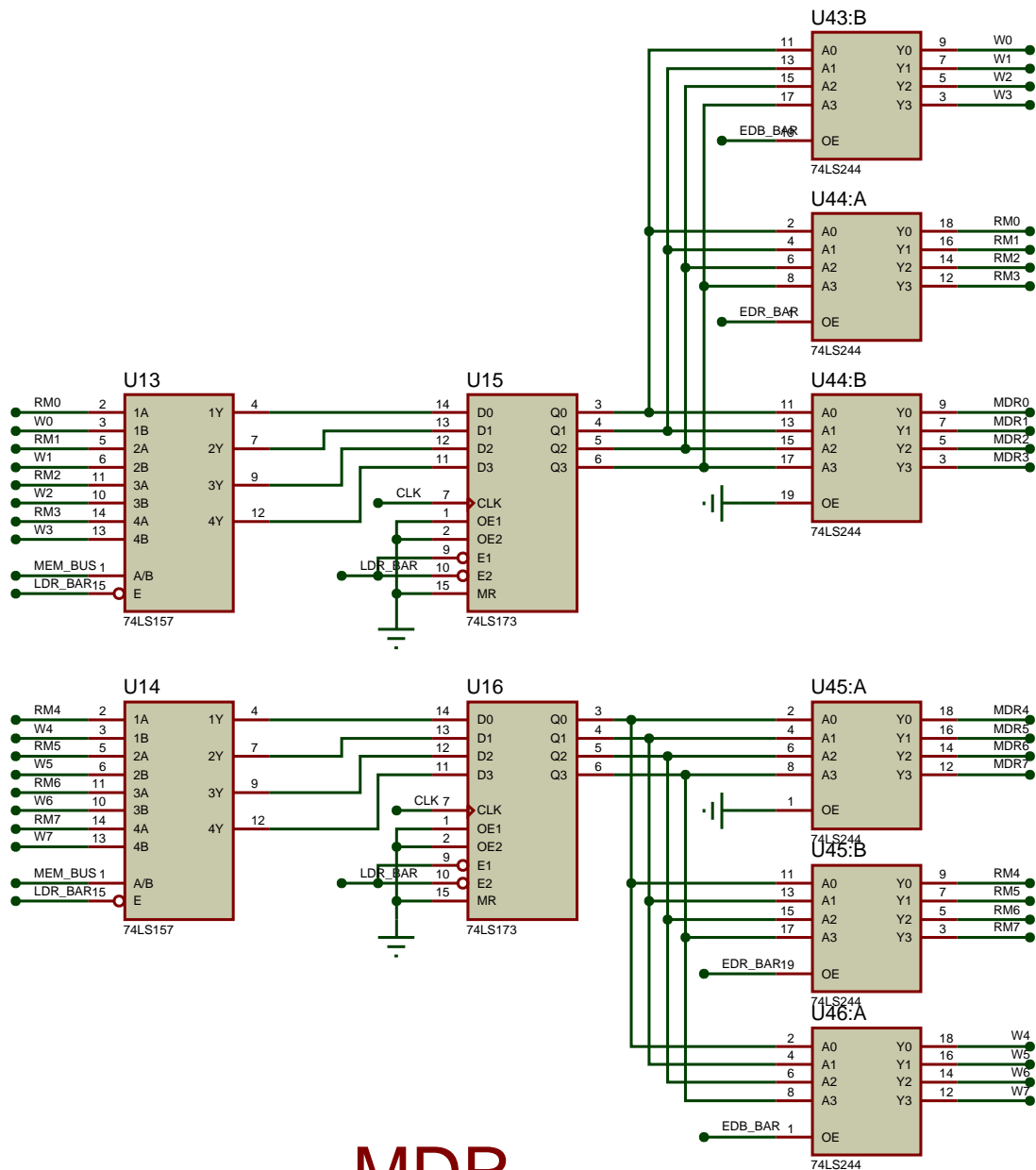
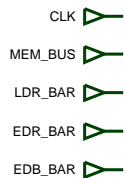
O3



OUT



MAR



MDR

LIR_BAR

CLK

W0

W1

W2

W3

W4

W5

W6

W7

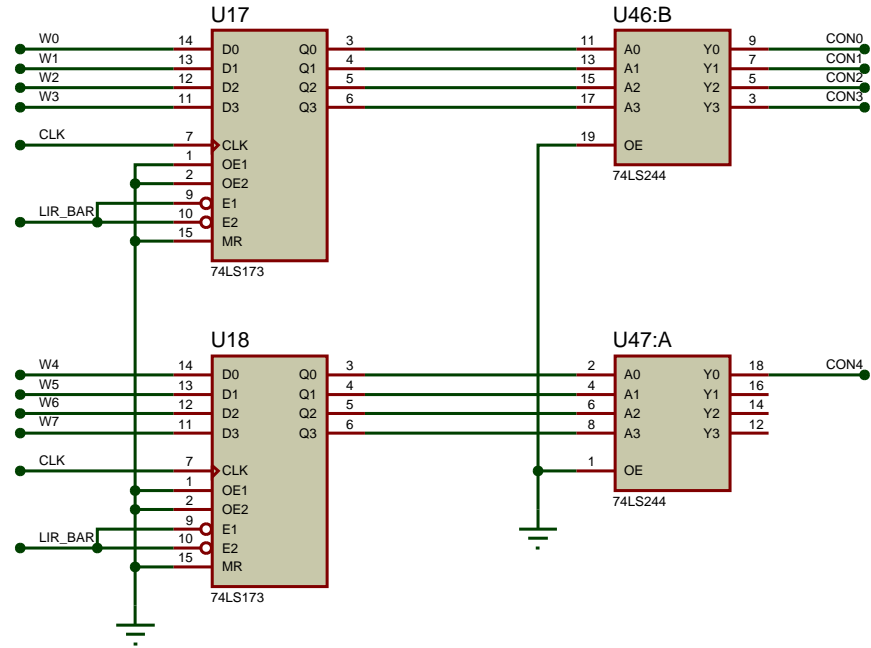
CON0

CON1

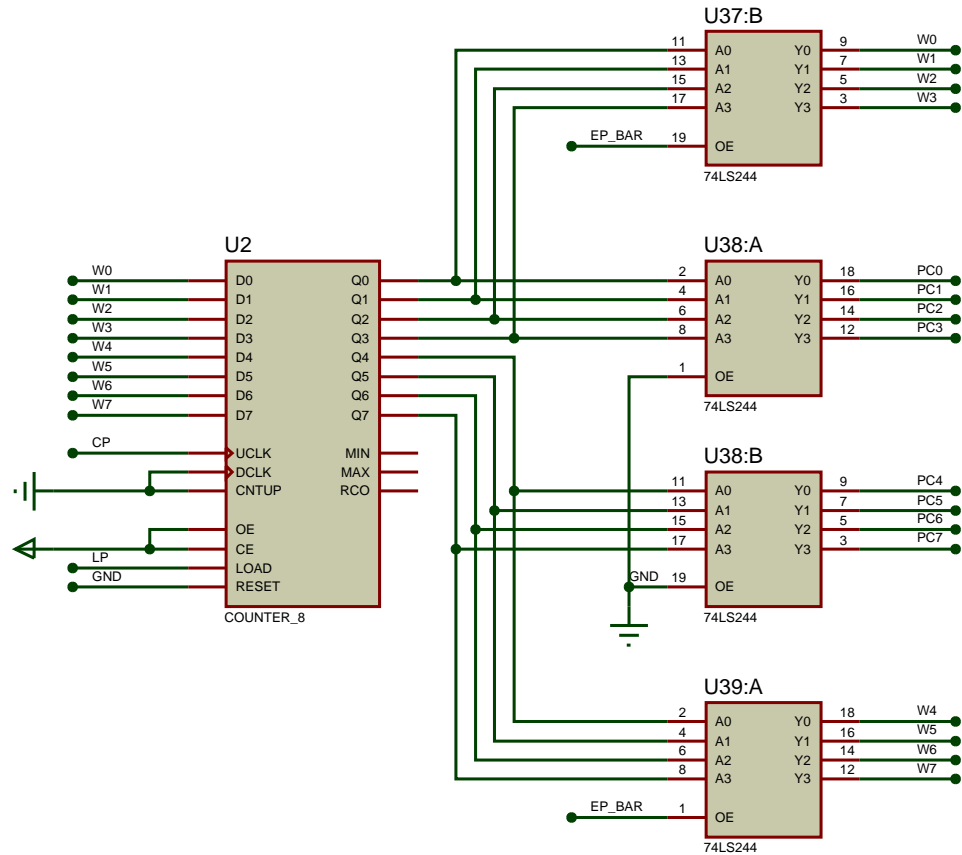
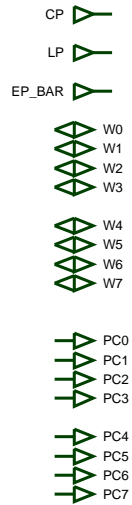
CON2

CON3

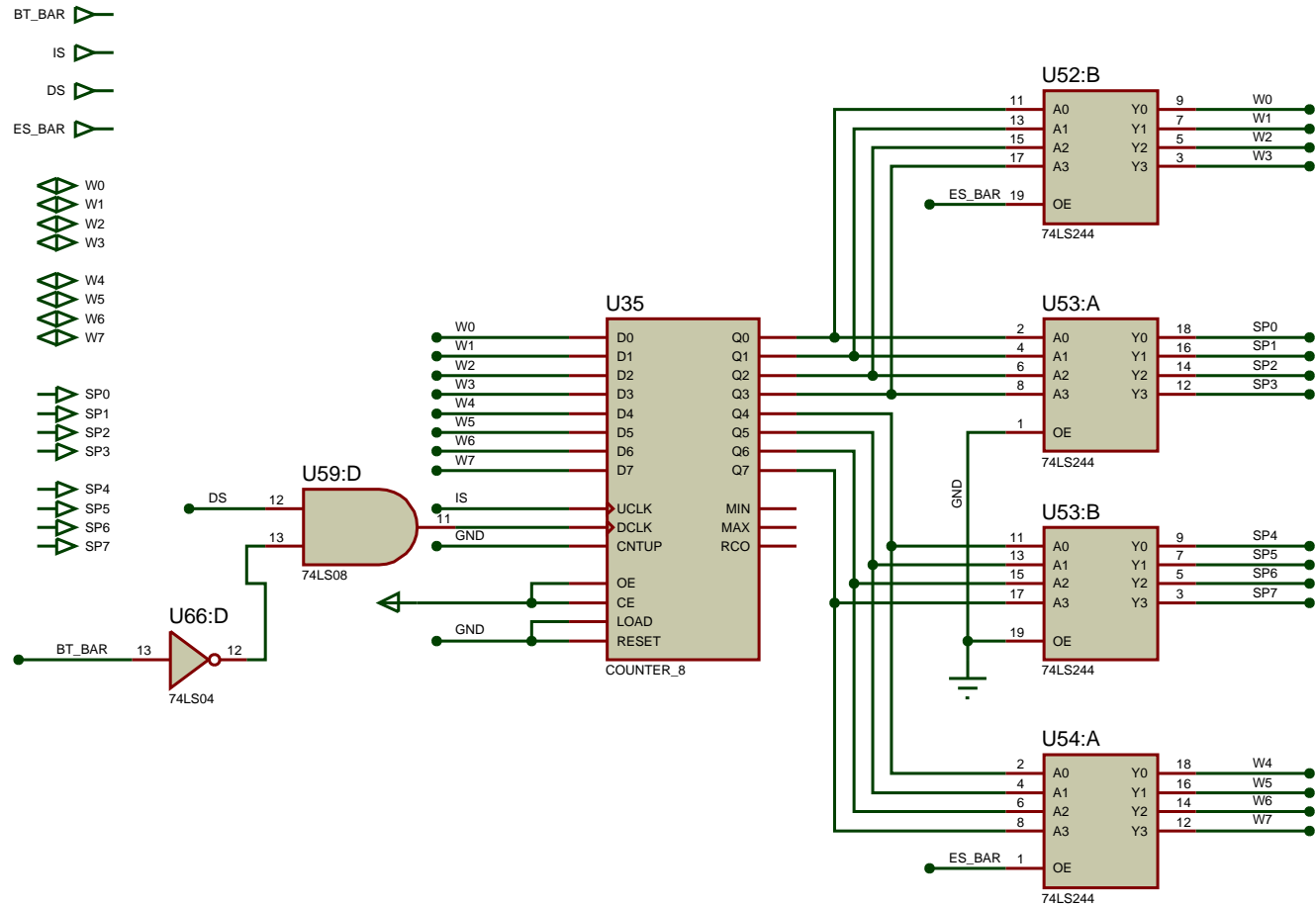
CON4



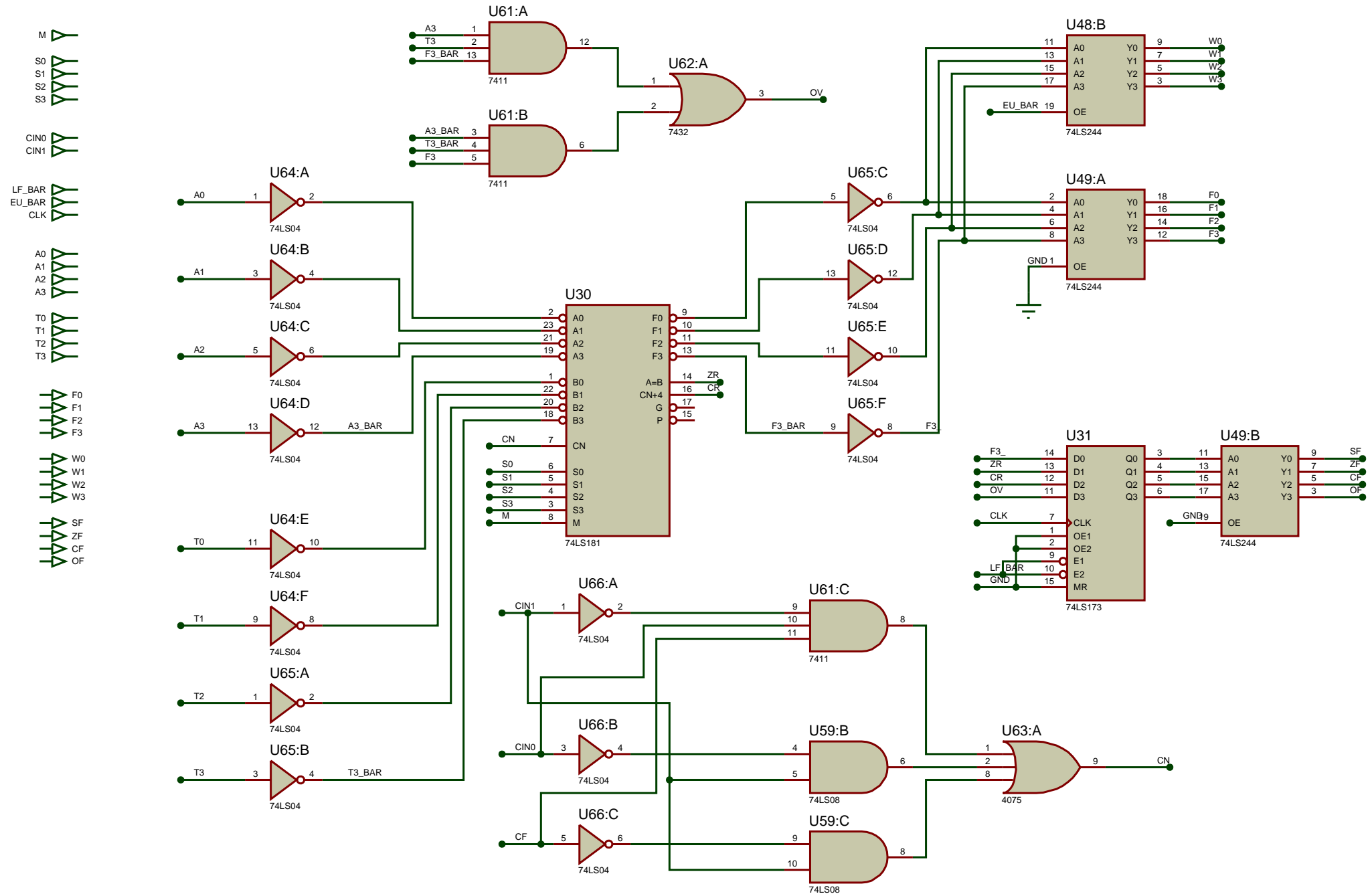
IR



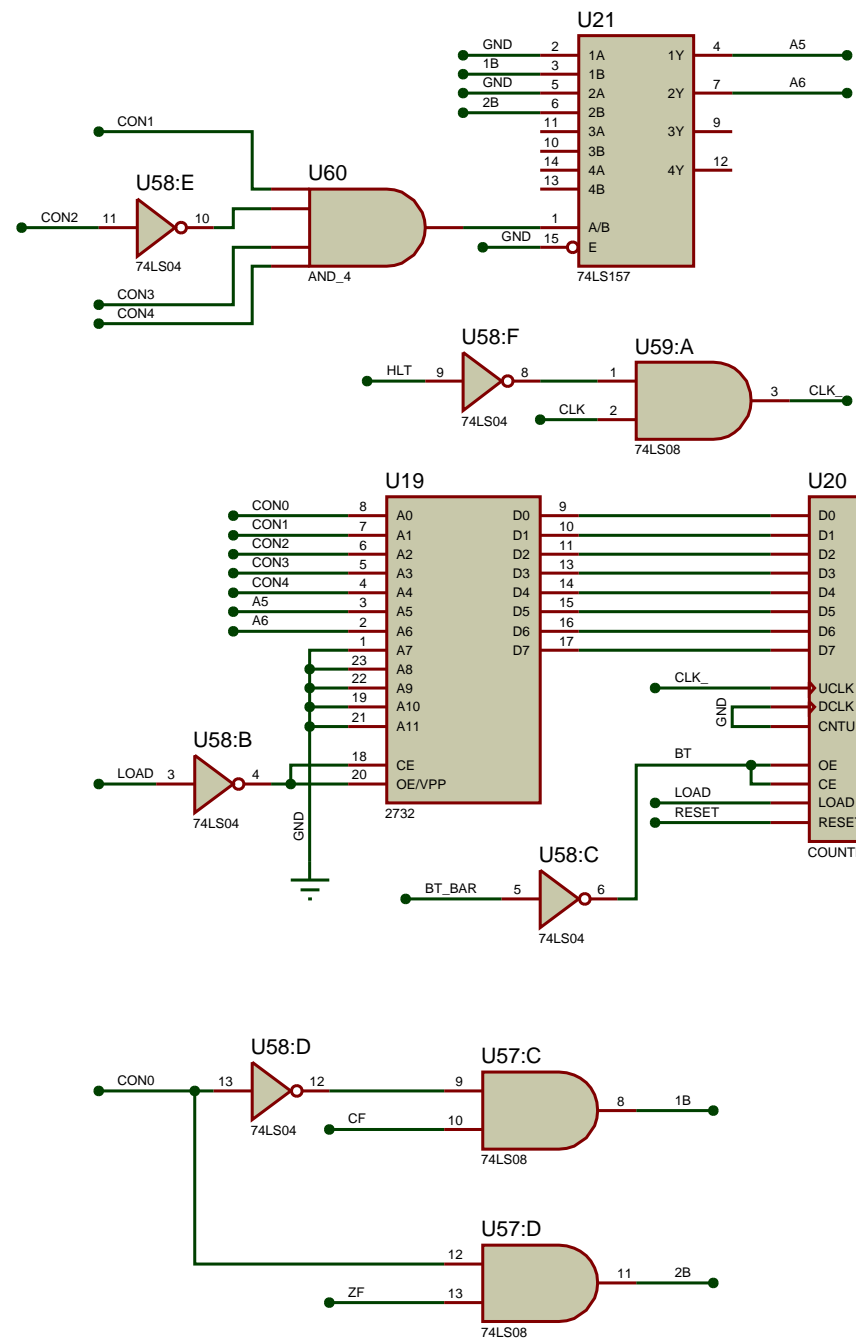
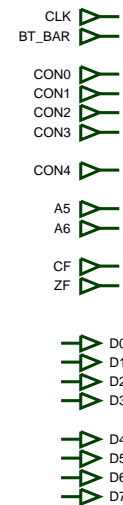
PC



SP



ALU



CONTROLLER

