SAVAN MANDORA

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🪪 CAREER OBJECTIVE

Seeking a dynamic role in Physical Design as a fresher trainee, where I apply my knowledge of Linux, Perl, and TCL scripting in order to learn and explore the ASIC flow of physical design.

💼 PROFESSIONAL EXPERIENCE

ASIC - Physical Design Intern, Einfochips   01/2023 – present
- Explore VLSI Domain, ASIC flow & it's Different domains.
- Familiar Linux, TCL, Perl, Physical Design flow
- Learn the Tools : DC, ICC, ICC2, ICV, PT.
- Understand Corporate work life Environment.

Antenna Design and Simulation Intern, Entuple Tech, Pvt. Ltd.   06/2022 – 07/2022
- Design Microstrip Patch antenna(Multiband).
- Explore the whole process Design From Simulation(Tuned) to Fabrication and Test the Antenna.
- Hanson Experience on Ansys HFSS tool.

📁 PROJECTS

Chip Block (28nm)   05/2023 – present
- Performed Physical Design Flow on Real Logic Block.
- Analyze log files, PnR scripts, Reports.
- Performed StarRC, PrimeTime analysis.
- Tool used : ICC, ICC2, PrimeTime, ICV

CHIP90 (90nm), ORCA (28nm)   02/2023 – 04/2023
- Performed these two design to learn Basic practical approach of Physical Design.
- Tool used : ICC, ICC2
- Flow : Chip Planning(Floor planning, Power planning), Placement, CTS, Routing, Sign Off (DRC, LVS, Timing).
- Analyzing reports also solve issue regarding PnR flow.

Design Multiband Microstrip Patch Antenna   01/2022 – 10/2022
- Center Frequencies [1.5GHz, 2GHz, 2.5GHz, 5GHz, 7GHz]
- Ansys HFSS tool used [Tuned many times to achieve center frequency]
- Used EM fundamentals
- Also design Power Dividers, Porter Hole antenna

🎓 EDUCATION

B.E.(E.C.), *Government Engineering College, Rajkot*   07/2019 – present
8.66 CGPA

H.S.C.(Science), *S. V. Virani High School, Rajkot*   06/2017 – 04/2019
61.89%

🧠 SKILLS

Linux

MOSFET

Digital System Design Fundamentals

TCL

Analyze the scripts and Reports

Python Programming

Tools
- Design Compiler
- ICC1 Tool
- ICC2 Tool