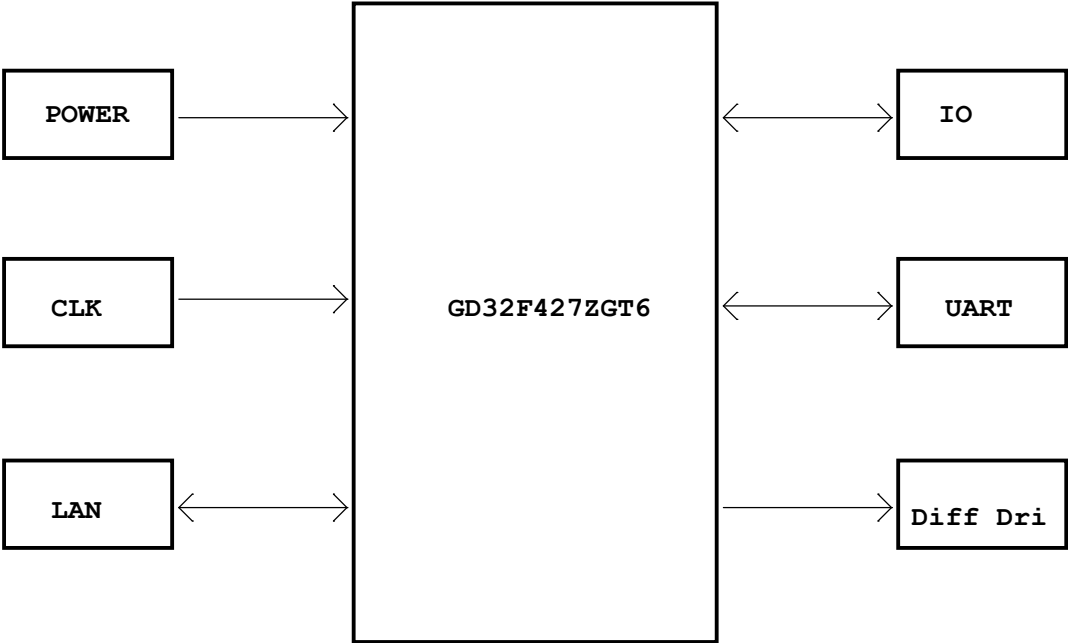
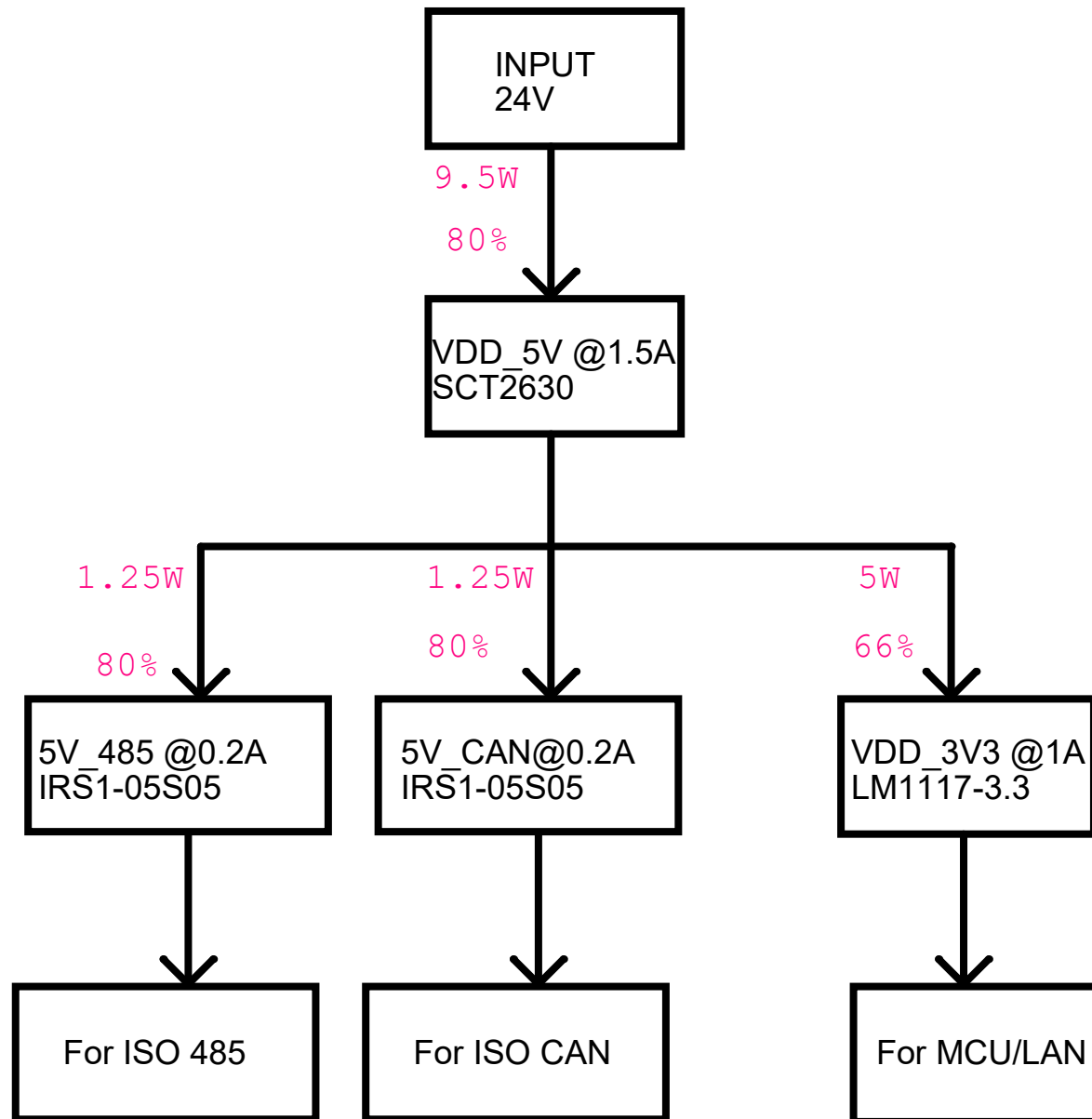


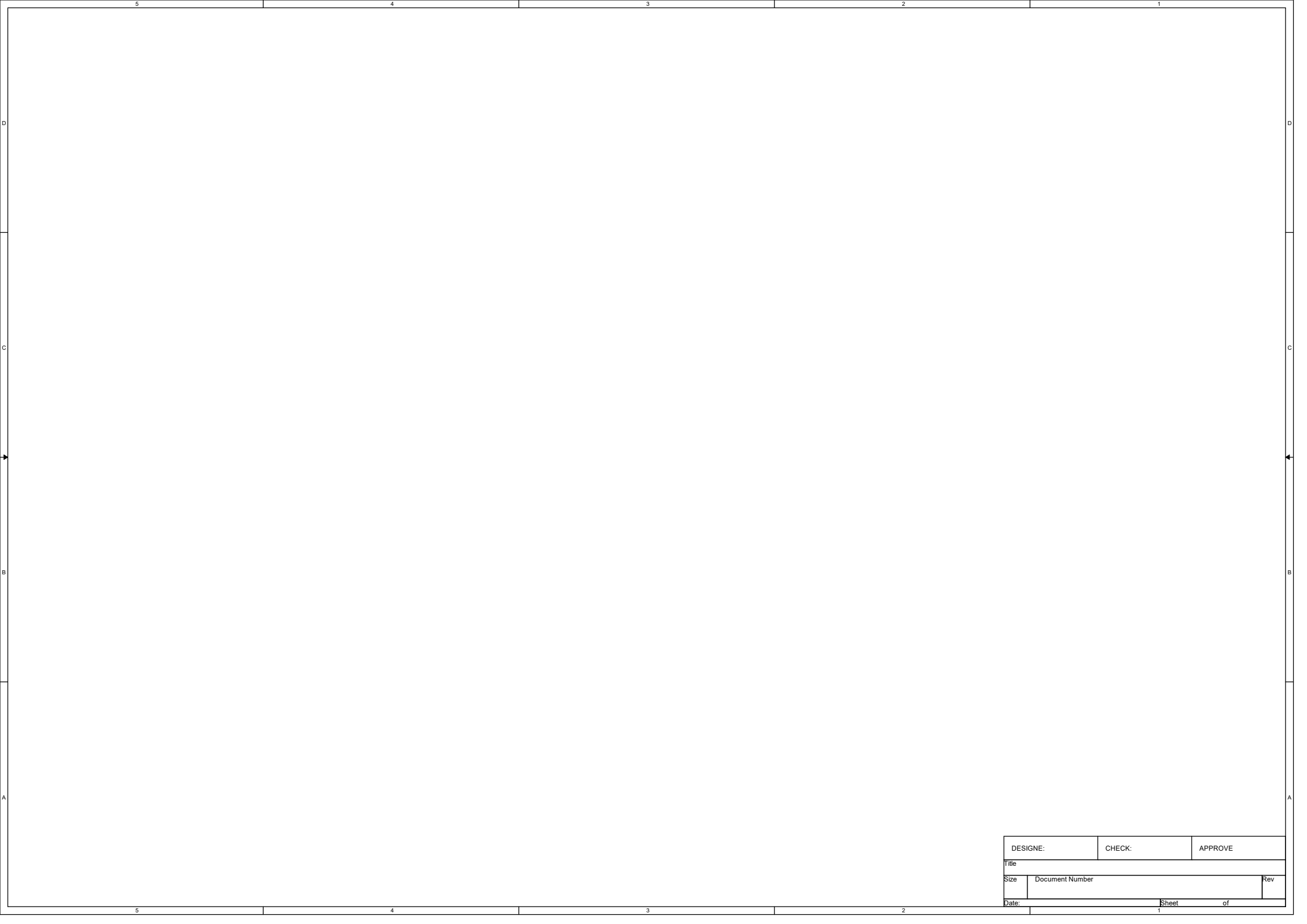
Revision History		
R1.0		Initial release
R2.0	2022/12/15	Reduce the cost: 1 delete some isolation power 2 power chip\MCU\PHY localization

Change List

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Title			
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Date:	Sheet 1 of 1		

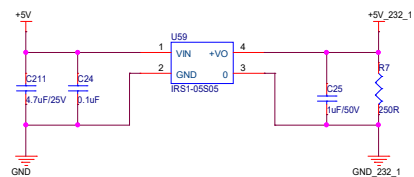
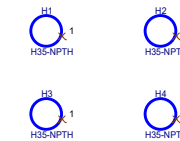
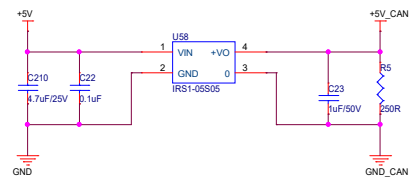
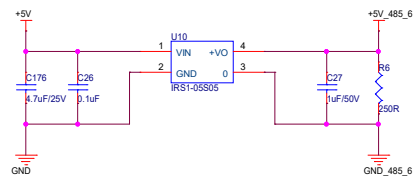
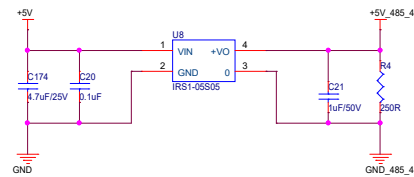
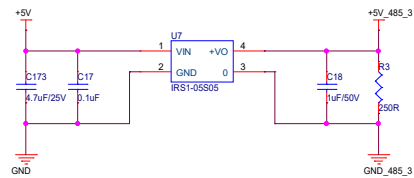
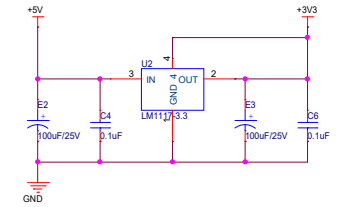
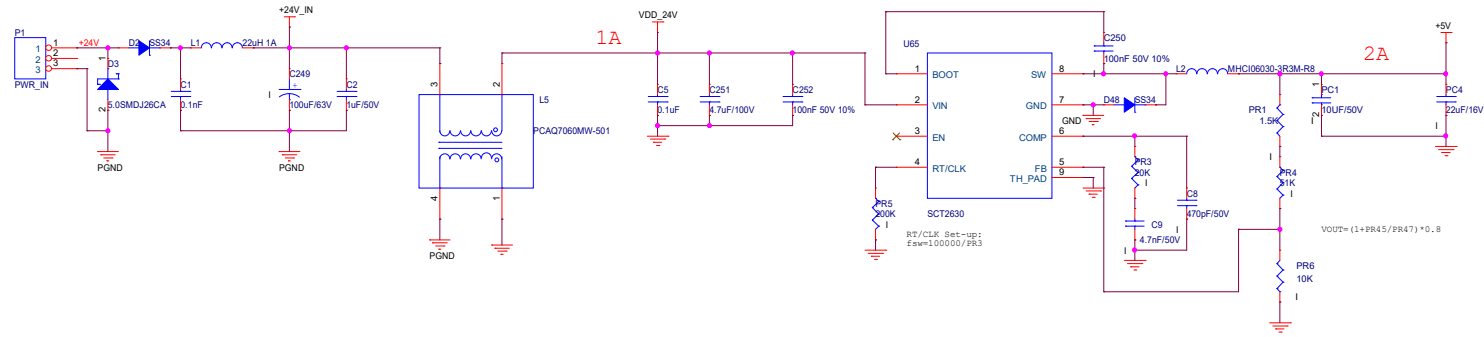






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POWER INPUT



PWM1----->TIM10 CH1 PF6
PWM2----->TIM11 CH1 PB9
PWM4----->TIM1 CH4 PE14
PWM3----->TIM9 CH1 PE5
A2,B2----->TIM4 CH1,CH2 PD12,PD13
A1,B1----->TIM3 CH1,CH2 PB4,PB5
A3,B3----->TIM2 CH1,CH2 PA15,PB3
3个485改为232(1,2,5)
新增can1

输入34路,方式不变
输出16路,8路PnP, 8路nnp,输出MCU 1, OUT为高

[S] MII_MDO <<

[S] MII_MDC <<

[S] MII_MDC <<

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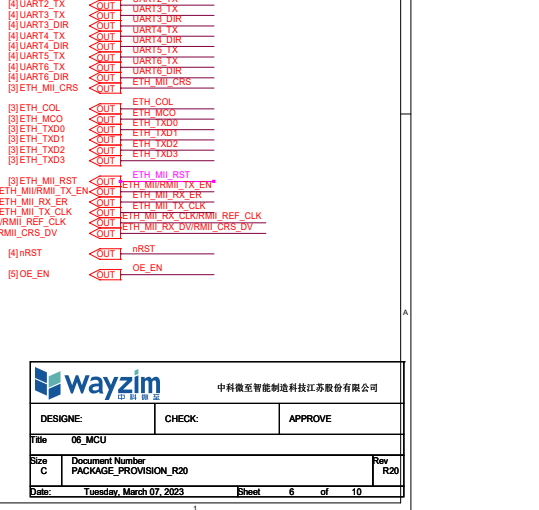
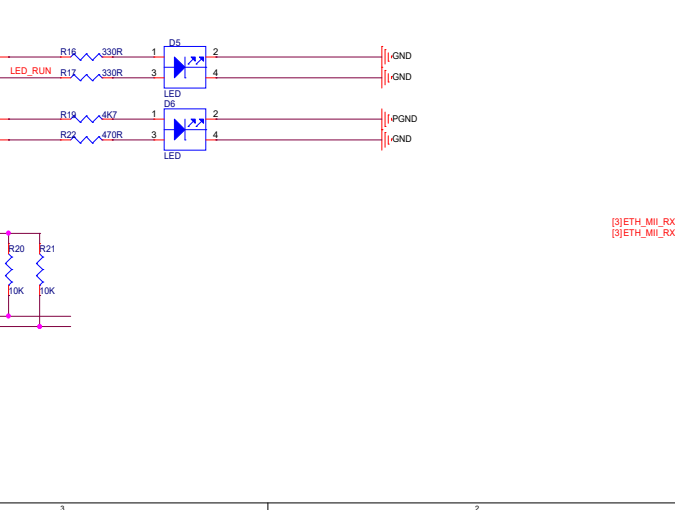
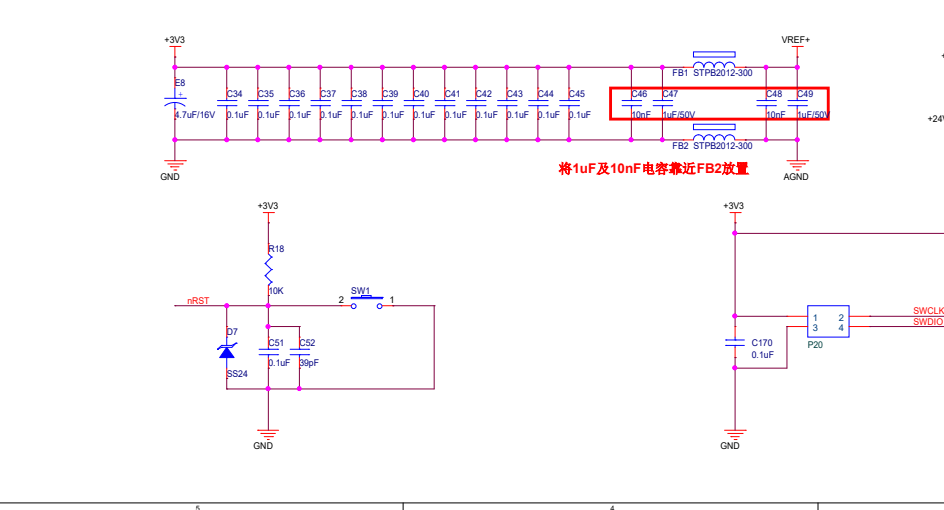
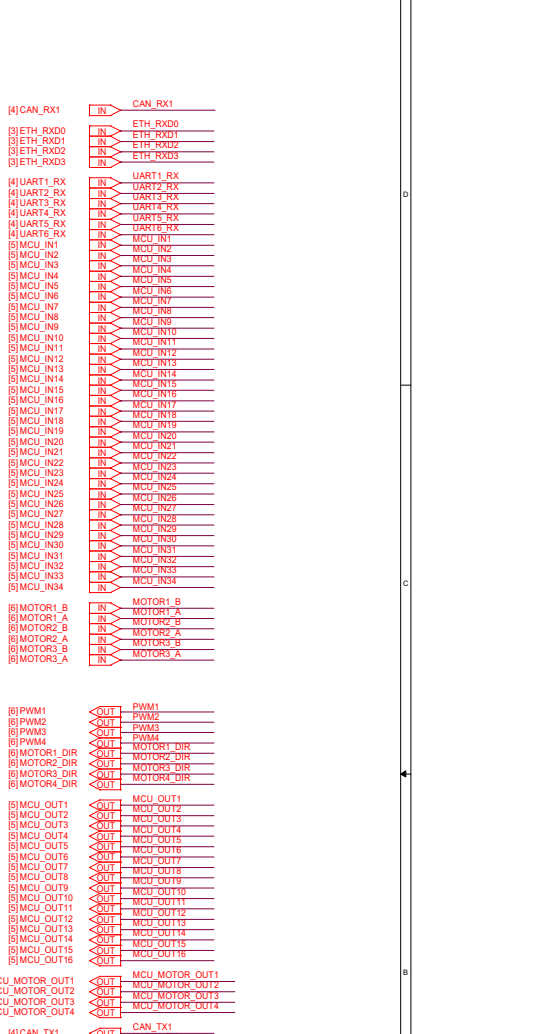
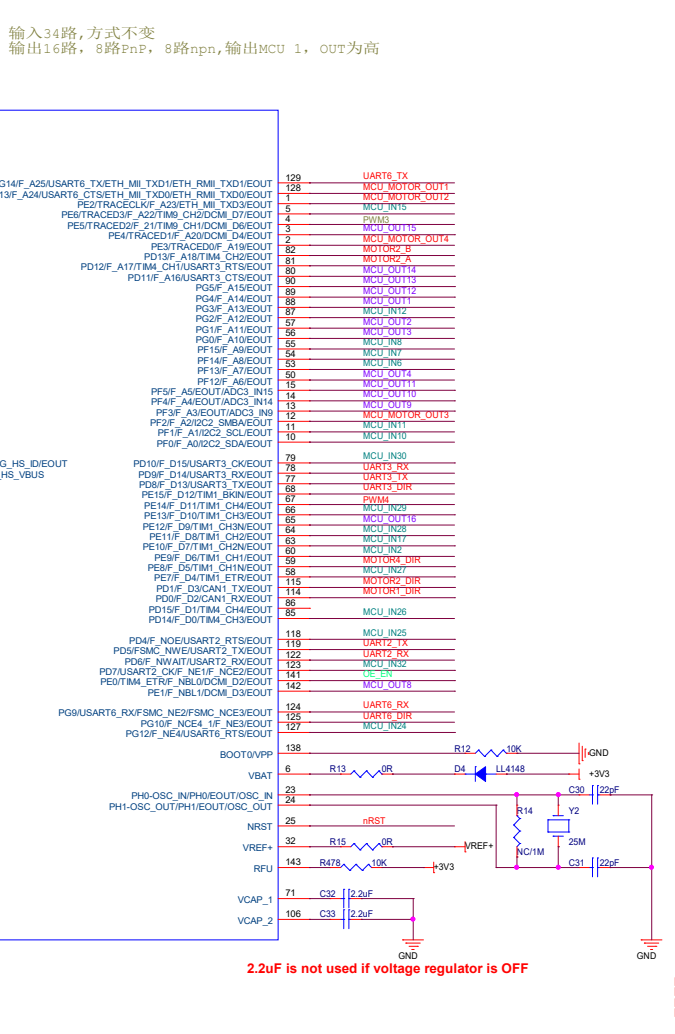
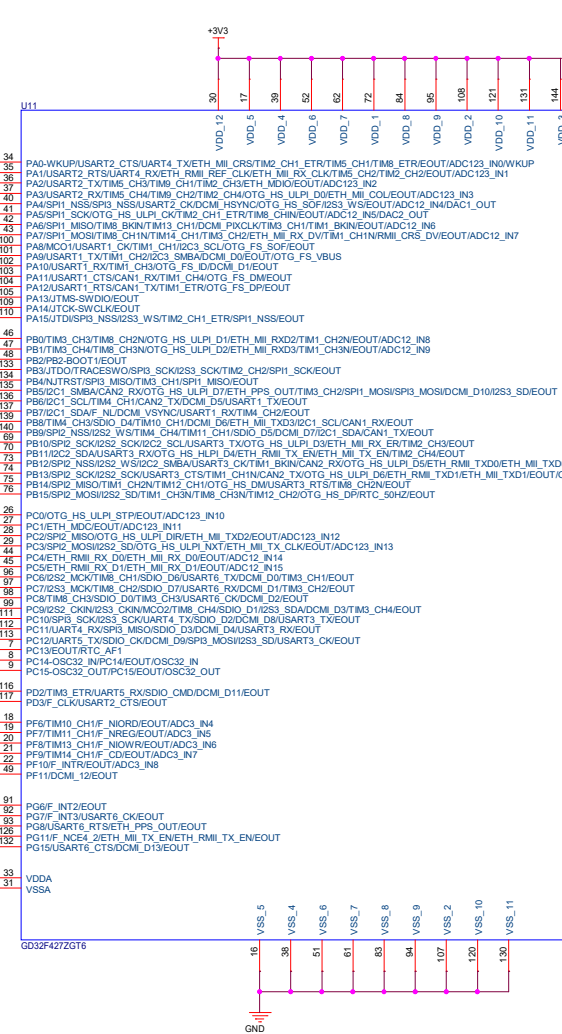
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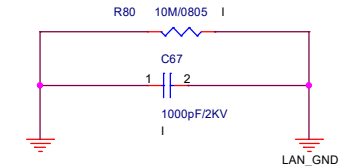
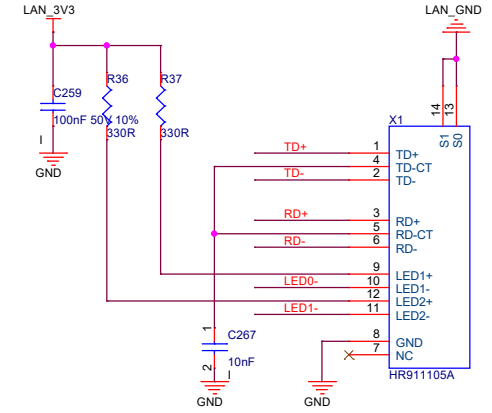
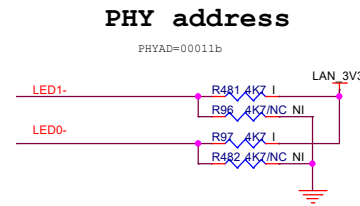
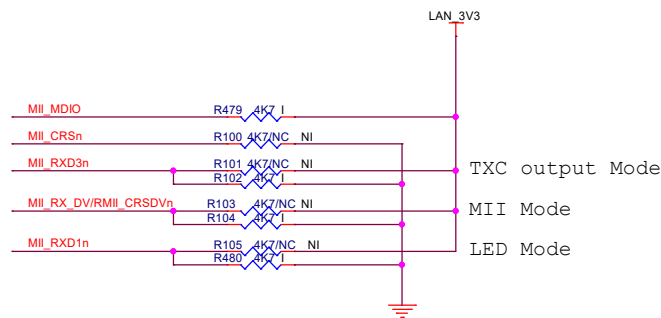
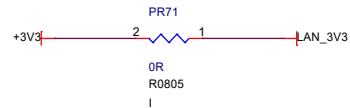
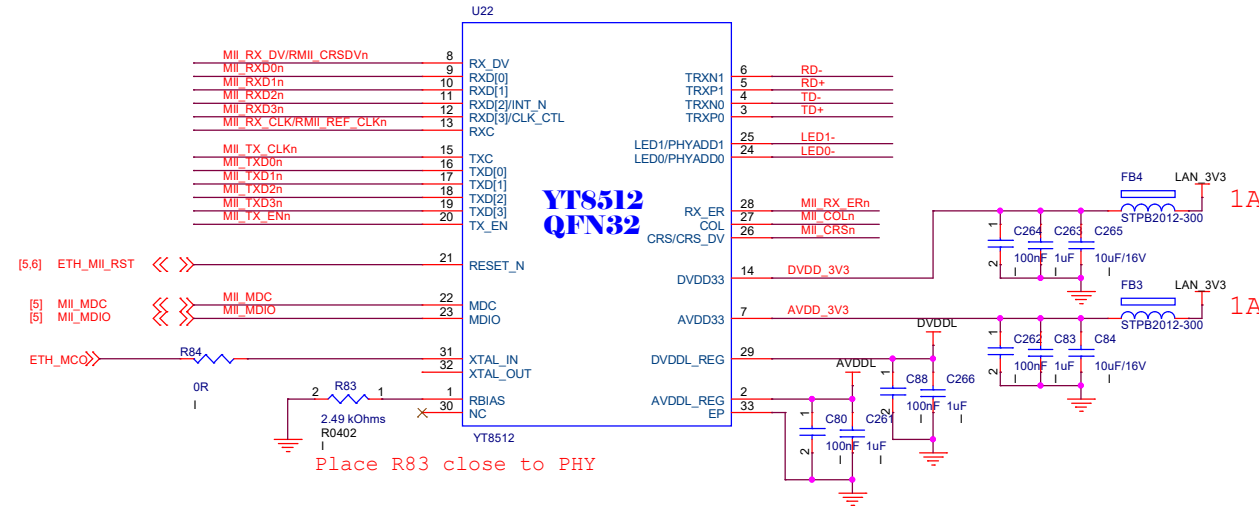
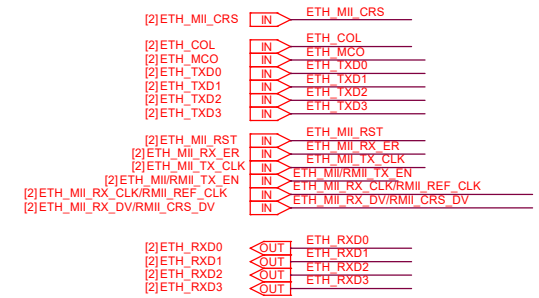
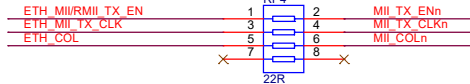
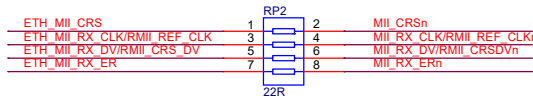
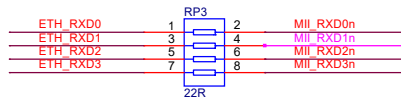
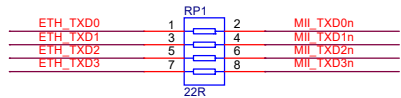
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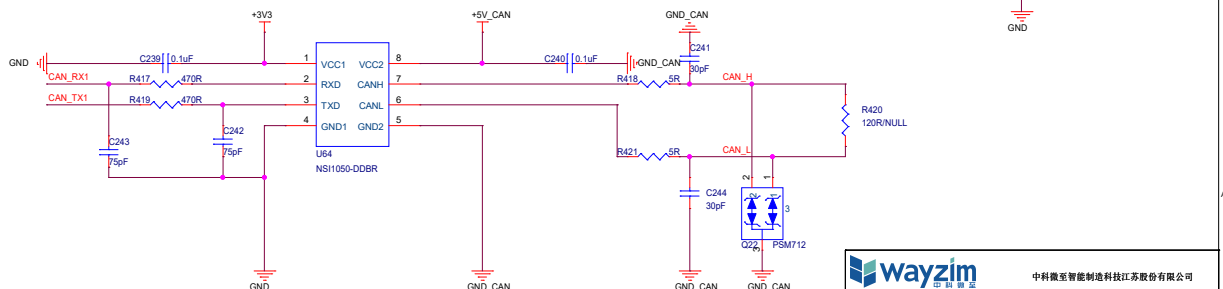
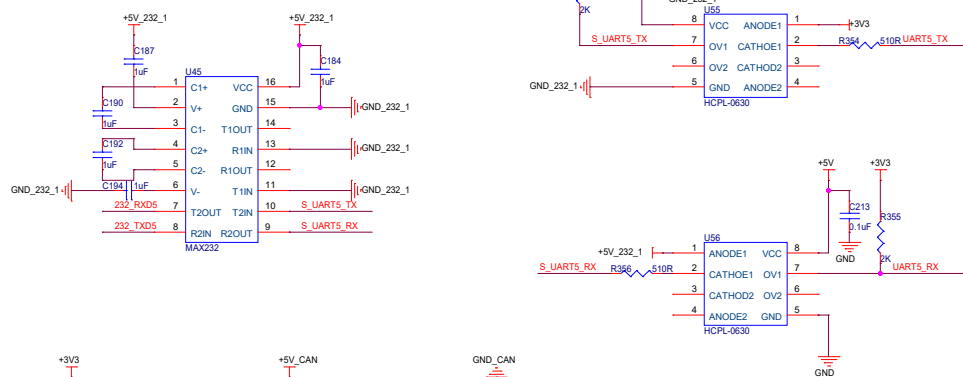
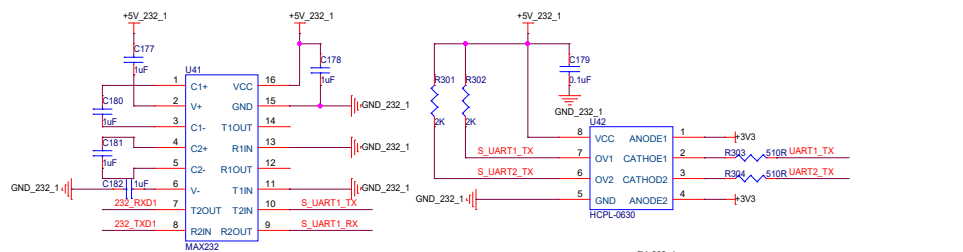
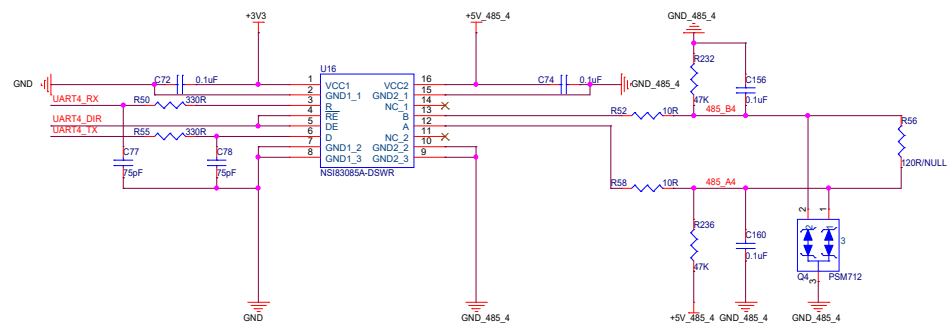


将1uF及10nF电容靠近FB2放置

2.2uF is not used if voltage regulator is OFF



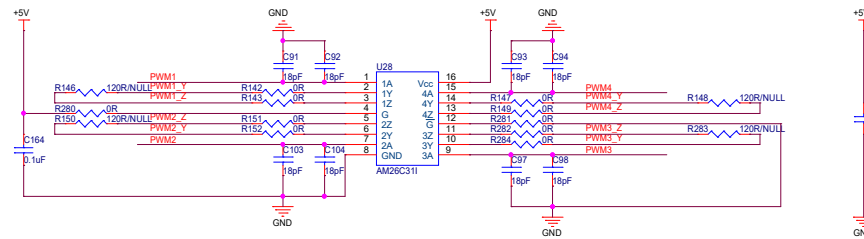
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Size A3	Document Number PACKAGE_PROVISION_R20		Rev R20
Date:	Tuesday, March 07, 2023	Sheet 7	of 10



输入34路,方式不变
输出16路,8路PnP,8路npn,输出MCU 1, OUT为高



差分驱动方式



目前使用的是两个24pF电容，占空比可以满足50%。考虑改变电容值，看看不同的效果

