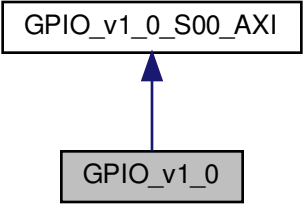


GPIO_v1_0_S00_AXI



```
graph BT; GPIO_v1_0[GPIO_v1_0] --> GPIO_v1_0_S00_AXI[GPIO_v1_0_S00_AXI]
```

GPIO_v1_0