

GPIO_v1_0_S00_AXI



```
graph BT; GPIO_v1_0[GPIO_v1_0] --> GPIO_v1_0_S00_AXI[GPIO_v1_0_S00_AXI]
```

The diagram illustrates a hierarchical relationship between two components. At the bottom is a white rectangular box labeled 'GPIO_v1_0'. A blue arrow points vertically upwards from the top center of this box to the bottom center of a gray rectangular box labeled 'GPIO_v1_0_S00_AXI' positioned above it.

GPIO_v1_0