

```
graph BT; A["/media/saverio/OS/Users<br/>/Saverio/Desktop/SE/git<br/>/Andrea/FPGA/MyIntGPIO/MyIntGPIO.sdk<br/>/provaINTRGpio/src/myIntGPIO2.h"] <--> B["/media/saverio/OS/Users<br/>/Saverio/Desktop/SE/git<br/>/Andrea/FPGA/MyIntGPIO/MyIntGPIO.sdk<br/>/provaINTRGpio/src/mainTest.c"]; A <--> C["/media/saverio/OS/Users<br/>/Saverio/Desktop/SE/git<br/>/Andrea/FPGA/MyIntGPIO/MyIntGPIO.sdk<br/>/provaINTRGpio/src/myIntGPIO2.c"];
```

/media/saverio/OS/Users
/Saverio/Desktop/SE/git
/Andrea/FPGA/MyIntGPIO/MyIntGPIO.sdk
/provaINTRGpio/src/myIntGPIO2.h

/media/saverio/OS/Users
/Saverio/Desktop/SE/git
/Andrea/FPGA/MyIntGPIO/MyIntGPIO.sdk
/provaINTRGpio/src/mainTest.c

/media/saverio/OS/Users
/Saverio/Desktop/SE/git
/Andrea/FPGA/MyIntGPIO/MyIntGPIO.sdk
/provaINTRGpio/src/myIntGPIO2.c