CPE301 – SPRING 2019

Design Assignment DA3A

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Primary Github address: <https://github.com/sawar1/UNLV301>

Directory: DA3A

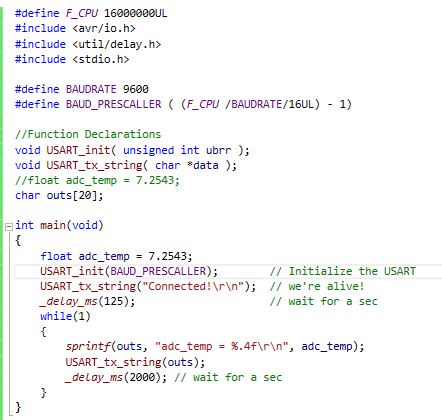
1. **COMPONENTS LIST AND CONNECTION BLOCK DIAGRAM w/ PINS**

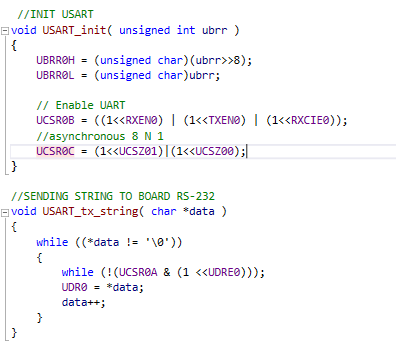
Atmel Studio 7

ATmega328p Xplained mini

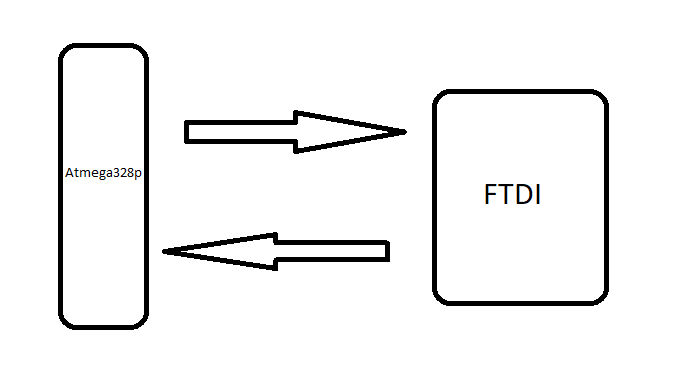
FTDI chip

1. **INITIAL/MODIFIED/DEVELOPED CODE OF TASK 1/A**

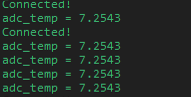


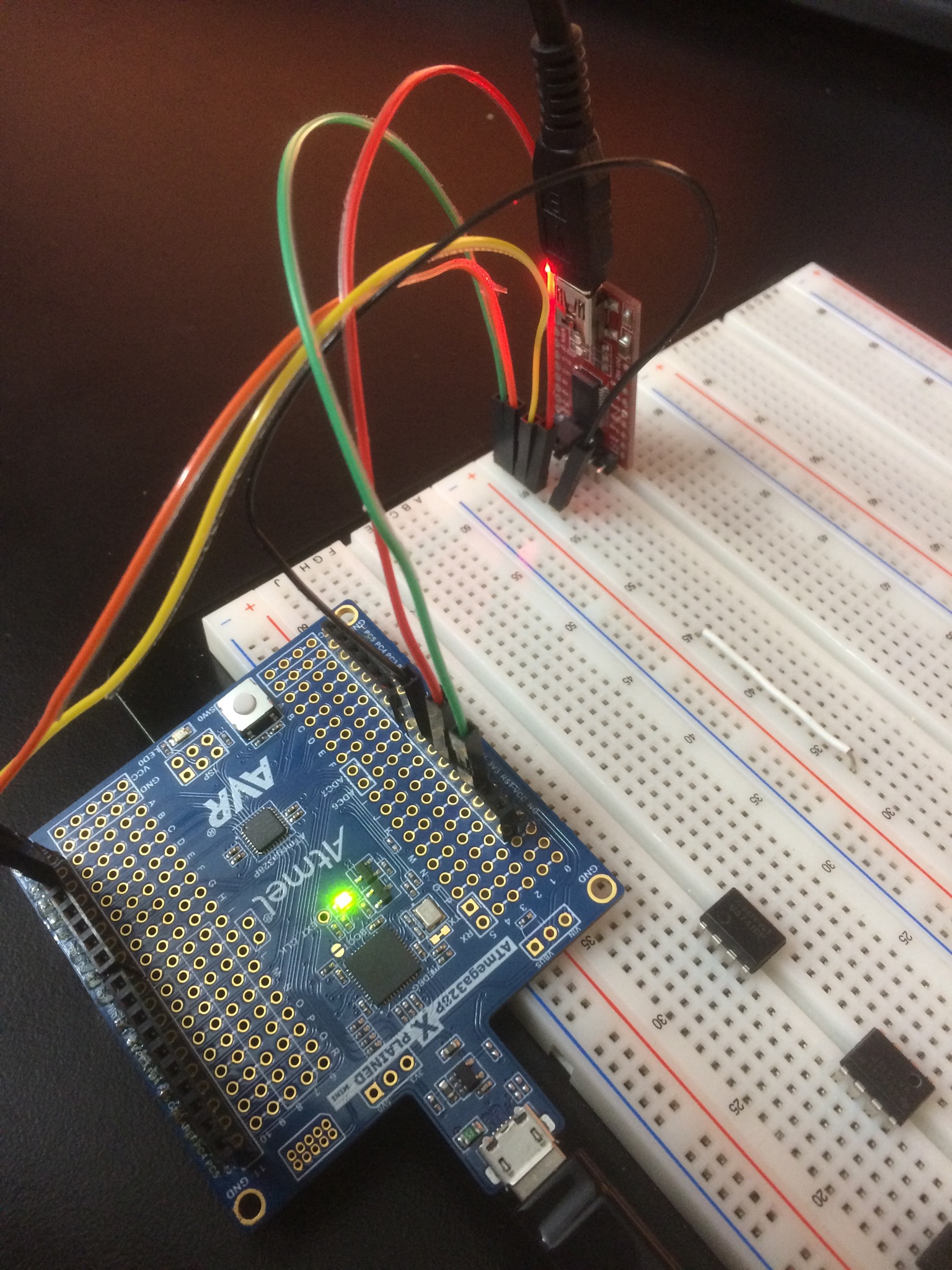


1. **schematic**

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1. **SCREENSHOTS OF EACH TASK OUTPUT (ATMEL STUDIO OUTPUT)**

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1. **VIDEO LINKS OF EACH DEMO**

<https://www.youtube.com/watch?v=gJzn0ml2T6k>

1. **GITHUB LINK OF THIS DA**

<https://github.com/sawar1/UNLV301>

**Student Academic Misconduct Policy**

<http://studentconduct.unlv.edu/misconduct/policy.html>

“This assignment submission is my own, original work”.

RIMON SAWA