EE 320 L ELECTRONICS I

LABORATORY 4: DIODES AND RECTIFIERS

DEPARTMENT OF ELECTRICAL AND COMPUTER ENGINEERING UNIVERSITY OF NEVADA, LAS VEGAS

1. OBJECTIVE

Get familiar with diodes, and apply knowledge learned in lecture to practical applications. Understand how regular diodes, rectifiers and Zener diodes work, and their functions in the circuits.

2. COMPONENTS & EQUIPMENT

Power Supply Breadboard

Function Generator Jump wires

Multimeter **Resistors & Capacitors**

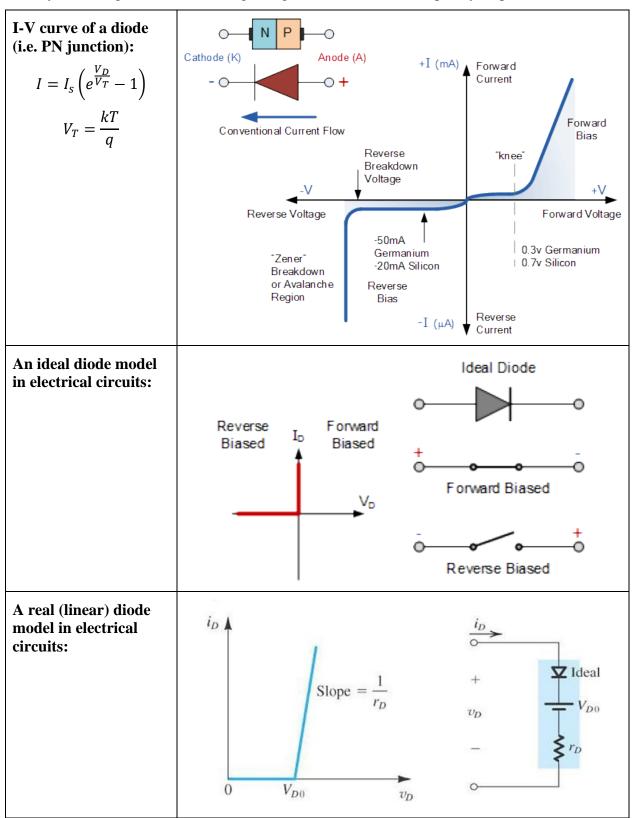
Diodes (LED, 1N4001, 1N4148, 1N4375) Oscilloscope

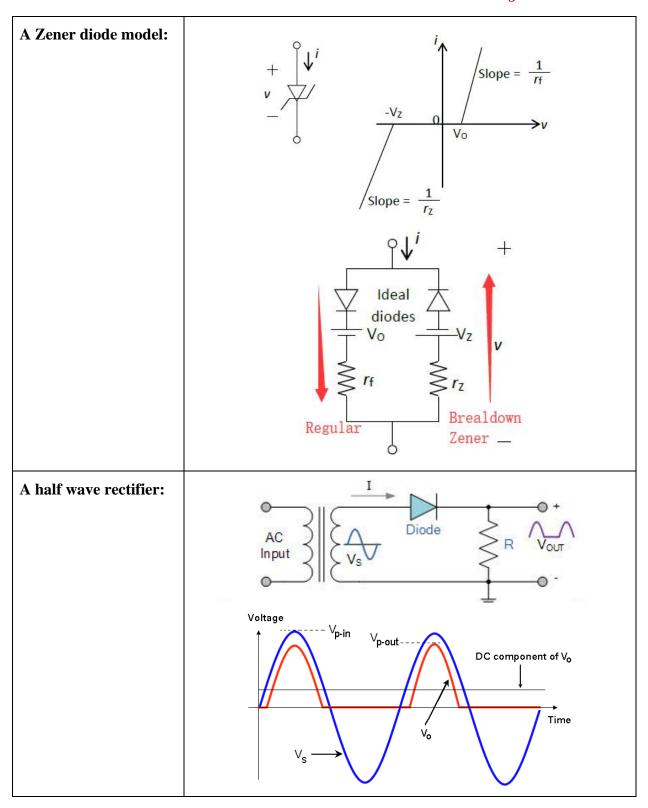
3. BACKGROUND

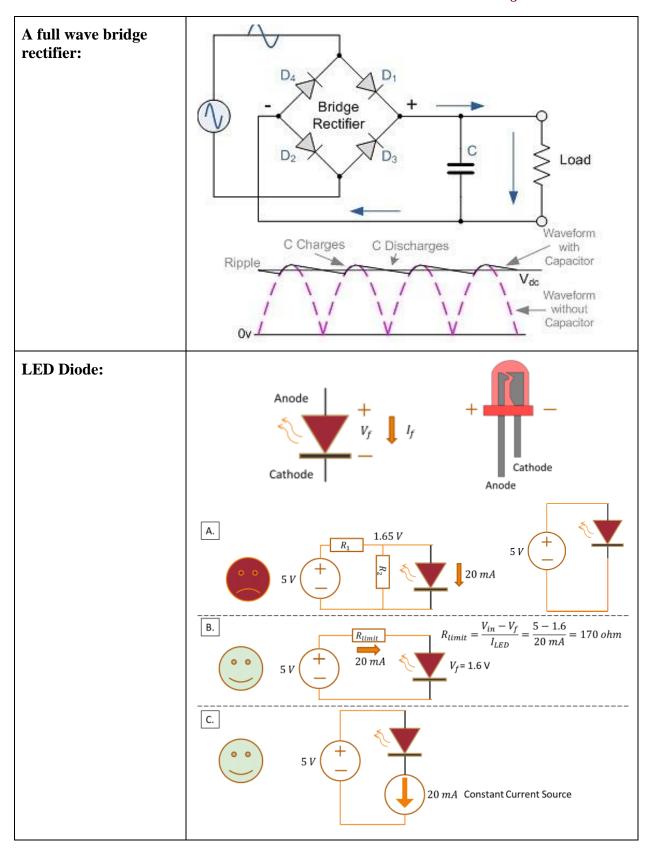
A diode is a two-terminal electronic component that conducts current primarily in one direction (asymmetric conductance); it has low (ideally zero) resistance in one direction, and high (ideally infinite) resistance in the other. This unidirectional behavior is called **rectification**, and is used to convert alternating current (AC) to direct current (DC). Forms of rectifiers, diodes can be used for such tasks as extracting modulation from radio signals in radio receivers.

Special-purpose diodes can perform many different functions. For example, diodes are used to regulate voltage (Zener diodes), to protect circuits from high voltage surges (avalanche diodes), to electronically tune radio and TV receivers (varactor diodes), to produce light (light-emitting diodes), and etc.

Key knowledges and formulas regarding to RC circuits and frequency response.







4. LAB DELIVERIES

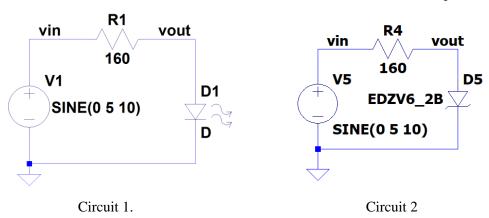
PRELAB:

- 1. Review the knowledge of regular diodes, rectifiers and Zener diodes, part of which are listed in the previous section.
- 2. Overview the key character of diodes in their datasheets.

LED:	https://www.vishay.com/docs/83171/tlur640.pdf
1N4001:	https://www.vishay.com/docs/88503/1n4001.pdf
1N4148:	http://www.onsemi.com/pub/Collateral/1N914-D.PDF
1N4735:	https://www.mouser.com/datasheet/2/427/1n4728a-104110.pdf

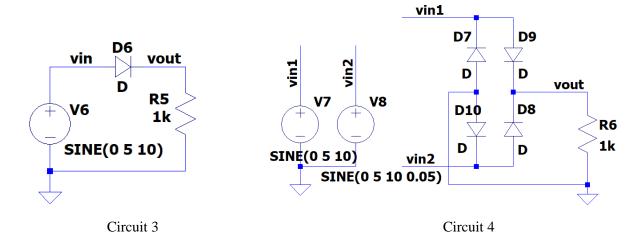
3. Use LTspice to simulate Circuit 1 & Circuit 2.

- 1) Observe the voltage at "vin" and "vout". Write down the peak voltage of "vout".
- 2) Repeat 1) but replace the LED diode with a regular diode.
- 3) Repeat 1) but replace the diode with a Zener diode (Circuit 2).
- 4) Repeat 3) but increase the input voltage V_p to 10V. Observe the waveform change and why?
- 5) What if reverse the Anode and Cathode of the Zener diode in Circuit 2. Explain the changes.



4. Use LTspice to simulate half-wave rectifier (Circuit 3).

- 1) Observe the voltage at "vin" and "vout". Write down the peak voltage of "vout".
- 2) Repeat 1) but replace the regular diode with a Zener diode (forward bias). Observe "vout".
- 3) Reverse the orientation of Zener diode in 2), i.e. reversed bias. Observe "vout".
- 4) Increase the input voltage V_p to 10V. Observe "vout". Explain the changes.
- 5) Add a 1μ F capacitor in parallel with the load resistor R5. Observe "vout" again and explain.



- 5. Use LTspice to simulate full-wave rectifier (Circuit 4).
 - 1) Observe the voltages and waveforms of "vin1-vin2" and "vout". "vin1" and "vin2" should share the same amplitude and frequency, but with a 180 ° phase difference (i.e. "vin1" = -"vin2"), and the same common ground of the circuit. Explain.
 - 2) Repeat 1) with a 0.22μ F capacitor connected at load in parallel. Explain the changes.
 - 3) Increase the resistance to $100k\Omega$ and capacitance to $10 \mu F$ (or the largest you can find in lab).
 - 4) Repeat 2) but use Zener diode in reverse orientation, with $R = 10k\Omega$, $C = 0.22\mu F$.
 - 5) Repeat 4) with $R = 100k\Omega$, $C = 10\mu F$.

LAB EXPERIMENTS:

- 1. Implement and measure Circuit 1 & 2 in Prelab Experiment 3 on breadboard, and compare the hand-calculation and LTspice results.
- 2. Implement and measure Circuit 3 in Prelab Experiment 4 on breadboard, and compare the hand-calculation and LTspice results.
- 3. Implement and measure Circuit 4 in Prelab Experiment 5 on breadboard, and compare the hand-calculation and LTspice results. (Careful with the ground setup.)

POSTLAB REPORT:

Include the following elements in the report document:

	the following elements in the report document.			
Section	Element			
1	Theory of operation			
1	Include a brief description of every element and phenomenon that appear during the experiments.			
2	Prelab report			
	1. Hand calculation results of Prelab Experiment 3~5.			
	2. LTspic	ce schematics and simulation results of Prelab Experiment 3~5.		
	Results of the	experiments		
3	Experiments	Experiment Results		
	1	Screenshots of LTspice simulations and oscilloscope waveforms, and V _p , V _{pp} values.		
	2	Screenshots of LTspice simulations and oscilloscope waveforms, and V _p , V _{pp} values.		
	3	Screenshots of LTspice simulations and oscilloscope waveforms, and V _p , V _{pp} values.		
4	Answer the questions			
	0	0 4:		
4	Questions	Questions		
4	Questions 1	Why add a capacitor at load in Experiment 2 and 3?		
	1 Conclusions			
5	1 Conclusions			
	1 Conclusions	Why add a capacitor at load in Experiment 2 and 3?		
	Conclusions Write down your etc. Images	Why add a capacitor at load in Experiment 2 and 3? r conclusions, things learned, problems encountered during the lab and how they were solved,		
	1 Conclusions Write down your etc. Images Paste images (e.	Why add a capacitor at load in Experiment 2 and 3? r conclusions, things learned, problems encountered during the lab and how they were solved, g. scratches, drafts, screenshots, photos, etc.) in Postlab report document (only .docx, .doc or		
	1 Conclusions Write down your etc. Images Paste images (epdf format is ac	Why add a capacitor at load in Experiment 2 and 3? r conclusions, things learned, problems encountered during the lab and how they were solved, g. scratches, drafts, screenshots, photos, etc.) in Postlab report document (only .docx, .doc or recepted). If the sizes of images are too large, convert them to jpg/jpeg format first, and then		
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5. REFERENCES & ACKNOWLEDGEMENT

- 1. Adel S. Sedra & Kenneth C. Smith, "Microelectronic Circuit", 6^{th} Ed.
- 2. https://en.wikipedia.org/wiki/Diode
- 3. Related diode datasheets.

I appreciate the help from faculty members and TAs during the composing of this instruction manual. I would also thank students who provide valuable feedback so that we can offer better higher education to the students.