23SDEC02R EMBEDDED SYSTEM AUTOMATION LAB WORKBOOK

Session 01: Changing the clock rate of ESP 32 Microcontroller

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PREREQUISITE:

- General idea of clock frequency, ESP32 board
- General idea of basic circuit

PRE-LAB:

- 1) What is the role of the CPU clock speed in a microcontroller like the ESP32?

 Determines the execution speed of instructions appearing the people among.
- 2) What is the default CPU clock speed of the ESP32, and how can it be configured? 240 Mm3, configurable to lower frequency was software sering.

OBJECTIVE:

To design and implement a serial communication port, check the CPU clock rate & XTAL Freq & APB bus clock.

COMPONENTS REQUIRED:

- ESP32
- Breadboard
- Connecting wires
- Micro USB cable

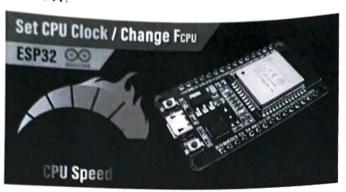
THEORY:

ESP32 CPU Speed (Frequency):

The ESP32 is a dual-core system with two Harvard Architecture Xtensa LX6 CPUs. All embedded memory, external memory, and peripherals are located on the data bus and/or the instruction bus of these CPUs.

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CIRCUIT:



ESP32:



CODE:

```
Jefine Op10-PWS

Unitara Forq=0;

Uo in serverit

primode (GP10-Pin, OURDIT);

Senal Segm (1152007),

Senal Point ("Defaut (PU frag")),

Senal Point (get CPU Forquery mrg()),

Uoid 100P() &

digital Wate (GP10-PIN, 0);
```

POST LAB: Take the snapshot of Tinker CAD simulation and paste here with your REG NO on it.

INTERFERENCE & ANALYSIS

The clock speed was shown

RESULT

The experiment was executed successfully