KLH | PBL

Project Based Learning

DEPARTMENT OF ECE

Review Date: 24/7/25

Course Code: 23VLS3101A , 23EC2230F , 23SDEC10A

Course Title: ANALOG VLSI , ASIC and FPGA , INTEGRATED SYSTEM DESIGN AUTOMATION

Course Coordinator/Instructor: DR. VIJAY RAO KUMBHARE /
DR. PHALGUNI SINGH /
DR. PAVAN KUMAR REDDY BOPPIDI

Review: ①/2/3/4

Group No.: 1

Project Title: Low power, fault tolerant ADC for edge IOT

Group Member Details:

Reg. No.	Name
2310040027	Rizwan
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2310040010	Rohan
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Comments Given During Review	Action Taken and Approval
Data Collection & L.R.	
Proposed block diagram	
Circuit Implementation	

NB: This form should be submitted during the next review.

Approved By:

Faculty Name

Signature