



Project Based Learning

DEPARTMENT OF ECE

Review Date: 21/8/25

Course Code: 23VLS31010A, 23EC2230P, 23SDEC10A

Course Title: ANALOG VLSI, ASIC & FPGA, ISDA

Course Coordinator/Instructor: Dr. Vijay Rao Kumbhare,
Dr. Phalguni Singh

Review: 1/2/3/4

Group No.: 1

Project Title: Low power, fault tolerant ADC for edge IoT.

Group Member Details:

Reg. No.	Name
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Comments Given During Review	Action Taken and Approval
Table No. missing	
Citations are missing	
Introduction (provide more data)	
Include the diagrams & results done by the other authors.	
Implement the proposed design	
Explain the diagrams. They are just hanging around.	

NB: This form should be submitted during the next review.

Approved By:
Faculty Name

Signature