

Programming assignment 2

Getting started

This is a continuation of Programming Assignment 1. In Assignment 1, you were given sequential reference implementations and asked to parallelize them with CPU threads. In this assignment, you will solve the same problem set using CUDA on a GPU.

The problems below should be solved using CUDA C/C++. You are expected to use a recent CUDA toolkit version with `nvcc` (CUDA 12 or above recommended).

You are allowed to use any external sources you can find. The code you submit must be written by members of your group, but it may be inspired by online material. All group members must be able to explain all parts of the submitted solution.

Tasks

As in Assignment 1, you will work with two algorithms:

1. Odd-Even Sort
2. Pearson Correlation Coefficient (PCC)

Use the same sequential reference implementations from Assignment 1 (`oddevensort`, `pcc_seq`). Do not modify the reference implementations; instead, add CUDA versions (e.g. `oddevensort_par.cu`, `pcc_par.cu`) and use the sequential programs for validation and performance comparison.

Task 1 – CUDA Odd-Even Sort

Implement a CUDA version of odd-even sort. This is the same algorithmic task as in Assignment 1, but now on the GPU. The algorithm works by alternating phases where independent pairs of elements can be compared and swapped in parallel. Correct synchronization between phases is required. A sorted output is guaranteed after n iterations of the main loop. Note that the input may already be fully sorted in an i th iteration of the loop, where $i < n$. The algorithm could be written to check during every iteration whether the input is fully sorted, but we do not implement that optimization in this task.

You must implement and benchmark **both** of the following CUDA variants:

1. Single-block version (in-kernel synchronization):

- Launch exactly one CUDA block
- Perform all odd/even phases inside one kernel launch
- Synchronize threads after each phase using `__syncthreads()`

2. Multi-block version (kernel-launch synchronization):

- Use multiple CUDA blocks
- Synchronize across the full grid by ending each phase and launching the next phase as a new kernel (global synchronization happens between kernel launches)

Requirements:

- Implement both CUDA odd-even sort variants described above
- Validate correctness for arbitrary input sizes and execution configurations (a simple loop that verifies $v[i] \leq v[i + 1]$ should be enough)
- Compare the two variants and discuss observed performance differences

Task 2 – CUDA Pearson Correlation Coefficient

Your task is to implement a CUDA version of the Pearson correlation coefficient, corresponding to the same PCC task from Assignment 1.

PCC generates the input matrix on the fly from matrix dimensions and an optional seed (no separate input file); both sequential and parallel PCC write correlation output to files that can be compared with the provided verify program.

The Pearson correlation coefficient ([Wikipedia](#)) measures the linear relationship between two datasets. It ranges between -1 and $+1$, where 0 implies no correlation. A correlation of -1 or $+1$ implies an exact linear relationship. A positive correlation means that as x increases, y increases. A negative correlation means that as x increases, y decreases.

The correlation coefficient is calculated as follows:

$$r = \frac{\sum (x - m_x)(y - m_y)}{\sqrt{\sum (x - m_x)^2 \sum (y - m_y)^2}}$$

where m_x is the mean of vector x and m_y is the mean of vector y ([SciPy documentation](#)).

The PCC computation involves multiple reductions that can be parallelized on the GPU.

Requirements:

- Implement a CUDA PCC computation
- Ensure correctness for arbitrary input sizes and CUDA execution configurations within a reasonable floating-point tolerance (use the provided verify program to check for correctness; the benchmark script runs it automatically)

Performance requirements and grading

This assignment is about correctness, parallelization, and performance reasoning.

Benchmarking. For odd–even sort, benchmark scalability using an array of 2^{19} elements; you may also use other sizes. For PCC, benchmark with matrices up to 4096×4096 .

To receive a pass grade, your submission must demonstrate:

- Correct results for both algorithms
- Positive speedup (> 2.0) for both odd–even sort and PCC on at least one non-trivial input size
- A comparison of odd–even sort performance between:
 - the single-block (`__syncthreads()` per phase) implementation, and
 - the multi-block (multiple kernel launches) implementation

Higher grades will be awarded based on:

- Quality of parallel implementation
- Clear and reproducible benchmarking methodology
- Insightful performance analysis
- Discussion of synchronization, memory behavior, kernel launch overhead, and scalability limits

There is no fixed speedup requirement for higher grades, but results should be reasonable given the algorithmic characteristics.

Submission guidelines

The submission consists of two parts: a demo and a code/report submission.

Demo

When your group is ready, you can join the online lab sessions and demonstrate the assignment. During the demo, you should:

- Run your code on a machine you bring
- Demonstrate correctness for both algorithms
- Explain your parallelization approach and performance results

You may benchmark on another machine, but your code must be runnable during the demo.

Code and report

After passing the demo stage, submit your solution via Moodle.

You may work in groups of one to three students. All submitted material must be written by the group. Only one group member should submit to Moodle, but all names must be listed in the report.

Your submission must include:

- Source code (including your CUDA implementations; the provided Makefile has targets for `oddeven_sort_par` and `pcc_par`—add your `.cu` source files and adjust the Makefile if needed)
- A Makefile to compile your programs. You may use the one provided as part of the sequential version.
- A report describing:
 - implementation choices (parallelization strategy, data/task decomposition, synchronization mechanisms, ...)
 - benchmarking setup (hardware and software setup, GPU model, block/grid configuration, input sizes, # of runs, ...)
 - performance results (speedup, execution time, ...)
 - explanation of observed (scaling) behavior

Deadline

Submit your solutions as a single ZIP file via Moodle no later than end of day March 8, 2026.

You must complete the demo before submitting.