

Lab 10: Circuit Diagrams with Design Values

1. Full Experimental Schematic

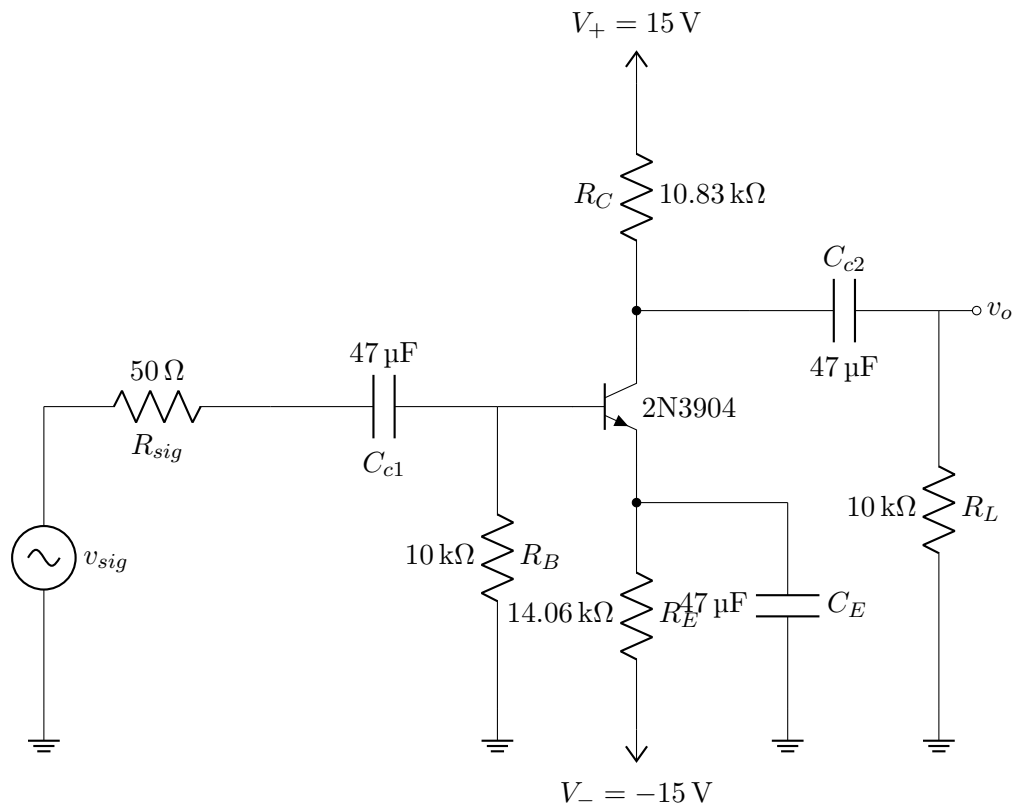


Figure 1: Full Amplifier Schematic (Orthogonal Layout)

2. DC Equivalent Circuit (Bias Point)

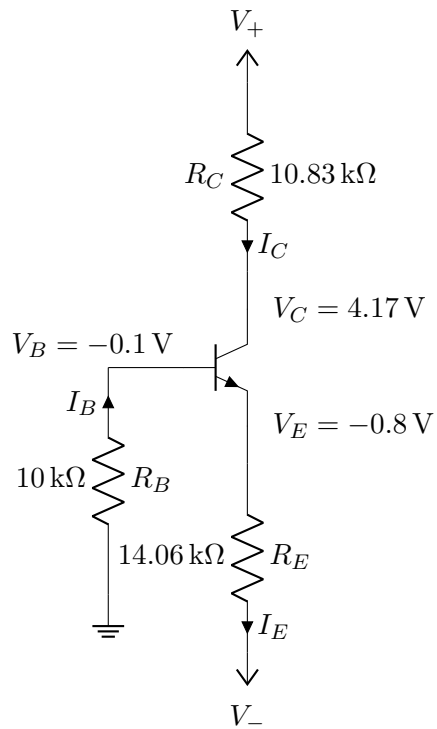


Figure 2: DC Equivalent Circuit

3. AC Small-Signal Model

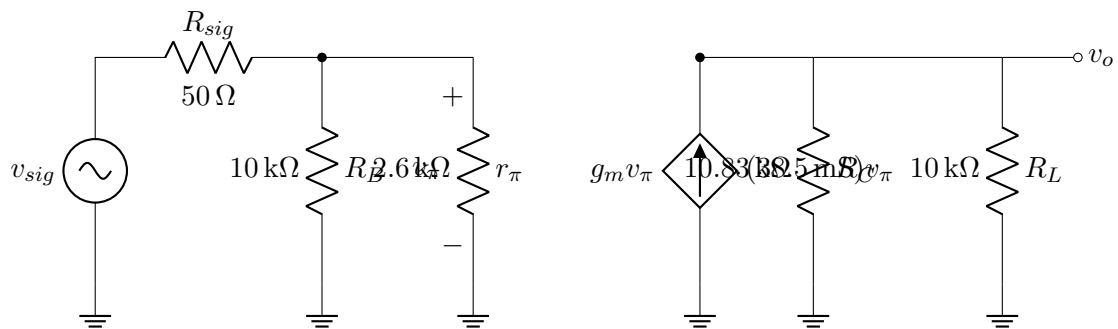


Figure 3: Small-Signal AC Model