

netx90_app_uart

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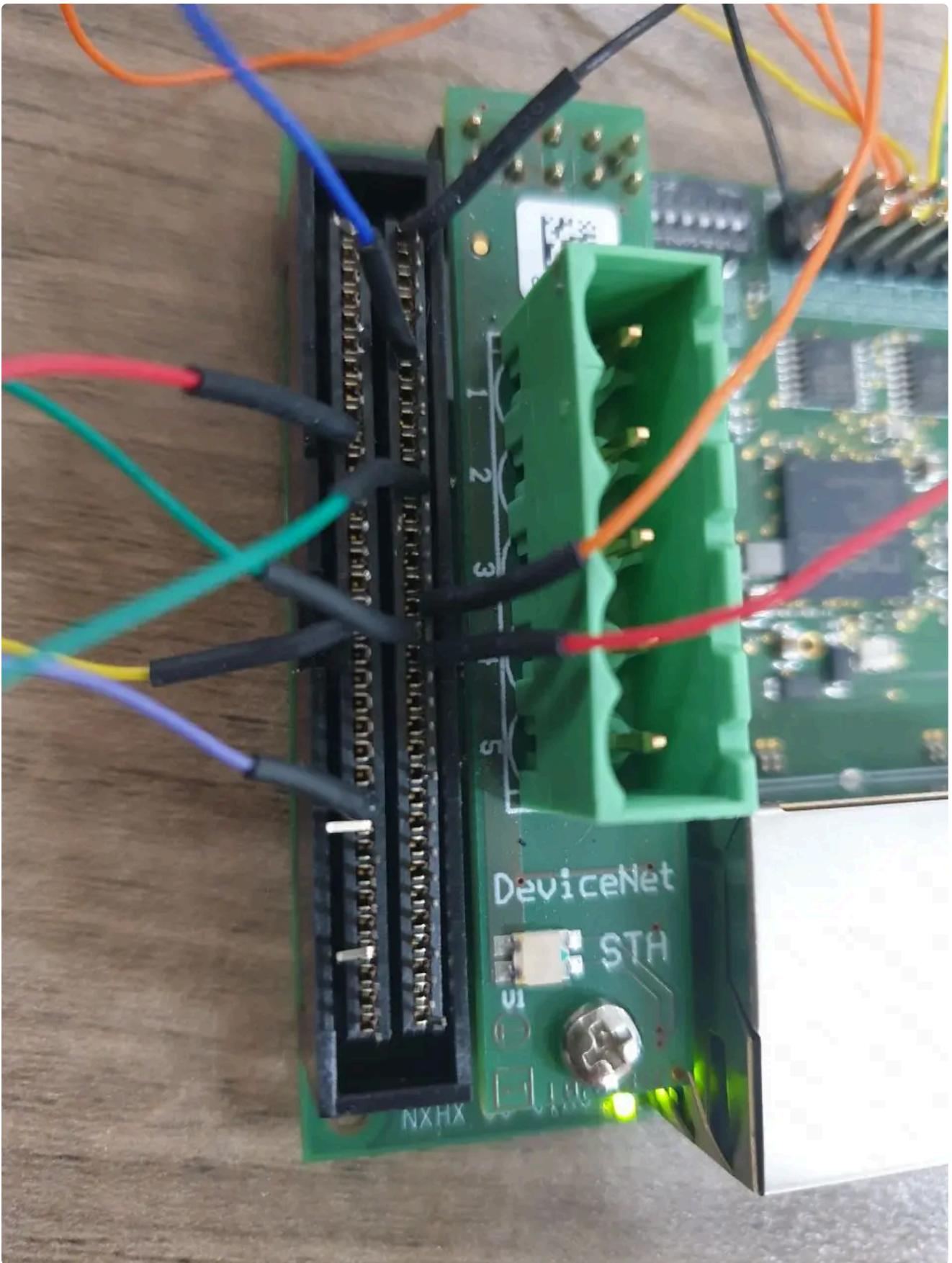
요약

1.

Task

Pin Assignment (netX 90)													
	1	2	3	4	5	6	7	8	9	10	11	12	
A	VSS(GND)	VDDIO	HIF_A8	HIF_A5	HIF_A2	HIF_D15	dpm0_spi_d_irq	dpm0_spi_m_osi	HIF_D6	HIF_D3	VDDIO	VSS(GND)	A
B	VDDC	HIF_A10	HIF_A7	HIF_A4	HIF_A1	HIF_D14	dpm0_spi_c_1k	dpm0_spi_m_iso	HIF_D5	HIF_D2	HIF_D0	VDDC	B
C	HIF_A11	HIF_A9	HIF_A6	HIF_A3	HIF_A0	HIF_D13	dpm0_spi_c_sn	HIF_D7	HIF_D4	HIF_D1	JT_TDI	JT_TDO	C
D	HIF_A12	HIF_A13	HIF_A14	HIF_A15	HIF_A16	HIF_A17	uart_app_txd	uart_app_txd	MII1_RXCLK	JT_RST	JT_TMS	JT_TCK	D
E	HIF_WRN	HIF_RDN	HIF_RDY	MII0_CRS	MII0_COL	PHY1_LED_L_INK_IN	PHY0_LED_L_INK_IN	MII1_COL	MII1_CRS	MII1_RXER	PHY1_TXN	PHY1_TXP	E
F	HIF_SDCLK	HIF_DIRQ	MII0_RXER	MII0_RXCLK	MII0_TXEN	VSS(GND)	VSS(GND)	MII1_RXD3/LVDS1_RXN	MII1_RXD2/LVDS1_RXP	MII1_RXDV	PHY1_RXN	PHY1_RXP	F
G	SQI_CS0N	SQI_MISO	SQI_SIO2	MII0_RXDV	MII0_TXCLK	VSS(GND)	VSS(GND)	MII1_RXD1/LVDS1_TXP	MII1_RXD0/LVDS1_RXN	uart_rxn	uart_txn	PHY_EXTR5	G
H	SQI_MOSI	SQI_CLK	SQI_SIO3	MII0_RXD3	MII0_RXD2	MII0_RXD3/ADC3_IN5	MII0_RXD2/ADC2_IN4	MII1_RXD3/LVDS0_RXP	MII1_RXD2/LVDS0_RXN	MII1_RXD1/LVDS0_RXN	PHY0_RXN	PHY0_RXP	H
J	RDY_N	RUN_N	BOO	xm0_tx	xm0_rx	MII0_RXD1/ADC3_IN4	MII0_RXD0/ADC2_IN7	MII1_RXD1/LVDS0_RXP	MII1_RXD0/LVDS0_RXN	MLED2	PHY0_RXN	PHY0_RXP	J
K	DCDC_LX_OUT	VSS_DCDC(GND)	VSS(GND)	COM_I03/AD_C3_IN7	COM_I02/AD_C3_IN6	COM_I01/AD_C3_IN3	COM_I00/AD_C2_IN2	MII1_RXEN/ADC2_IN3	mled1	MLED3	VDDC		K
L	VDDIO	VREF_ADC	RST_OUT_N	mm107	mm105	MII03/ADC3_IN1	MII01/ADC2_IN1	MII1_RXIO/A_DCC2_IN6	CLK25OUT	mled0	PHY_VDDIO	VDDIO	L
M	VSS_REF(GND)	VDDC	RST_IN_N	mm106	mm104	MII02/ADC3_IN0	MII00/ADC2_IN0	VDD_PLL	OSC_XTI(XT_ALIN)	OSC_XTO(XT_ALOUT)	VDDC	VSS(GND)	M
	1	2	3	4	5	6	7	8	9	10	11	12	

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A	VSS(GND)	VDDIO	HIF_A8	HIF_A5	HIF_A2	HIF_D15	dpm0_spi_d_irq	dpm0_spi_m_osi	HIF_D6
B	VDDC	HIF_A10	HIF_A7	HIF_A4	HIF_A1	HIF_D14	dpm0_spi_c_1k	dpm0_spi_m_iso	HIF_D5
C	HIF_A11	HIF_A9	HIF_A6	HIF_A3	HIF_A0	HIF_D13	dpm0_spi_c_sn	HIF_D7	HIF_D4
D	HIF_A12	HIF_A13	HIF_A14	HIF_A15	HIF_A16	HIF_A17	HIF_BHEN	HIF_CSN	MII1_RXCLK
E	HIF_WRN	HIF_RDN	HIF_RDY	MII0_CRS	MII0_COL	PHY1_LED_L_INK_IN	PHY0_LED_L_INK_IN	MII1_COL	MII1_CRS



(1.27 mm pitch 납땜)

hardware_config.hwc 파일 flash 후 uart_app 동작

