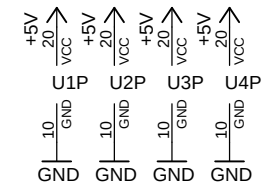
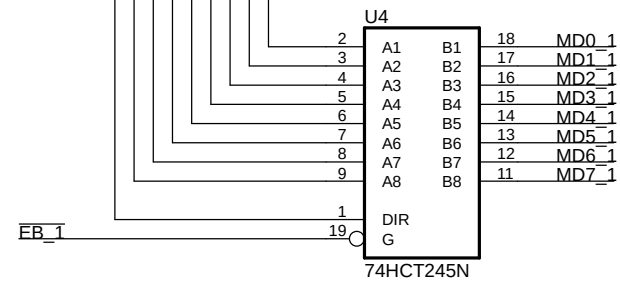
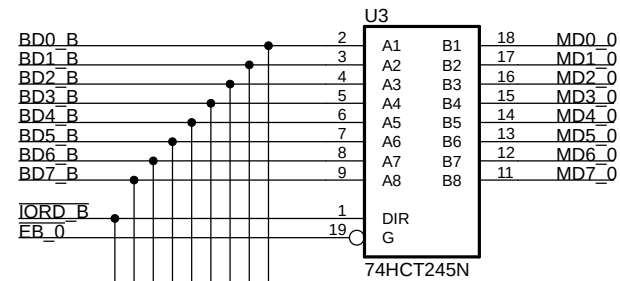
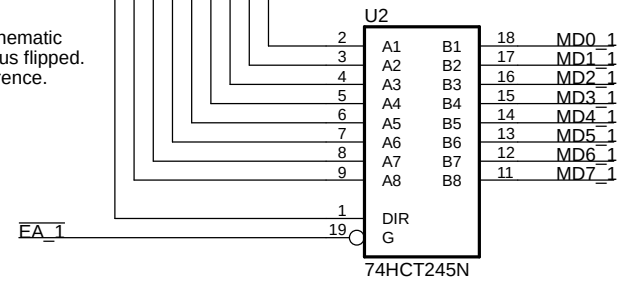
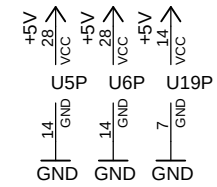
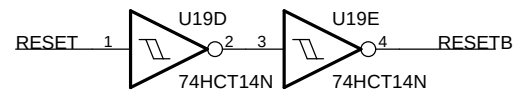
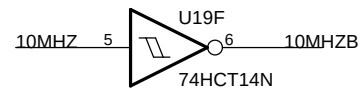
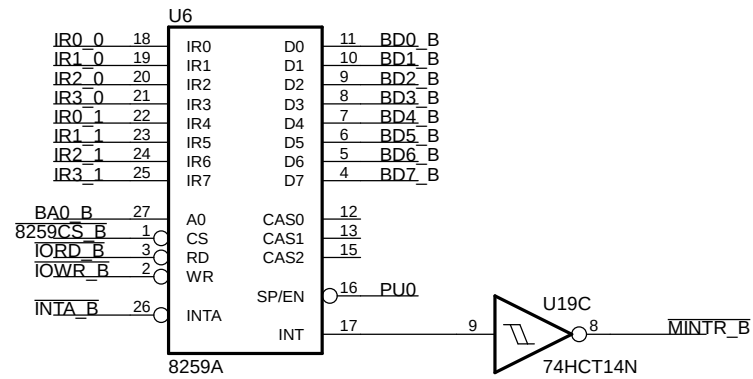
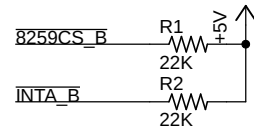
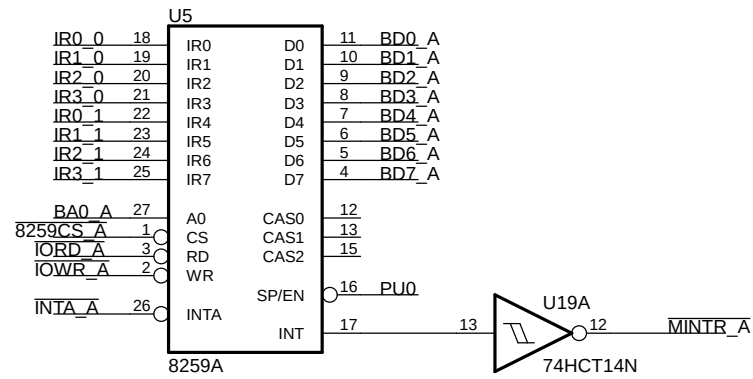


Note pin order in Intel Schematic on U2, U3 has the data bus flipped. Shouldn't make any difference.





Note: Experienced issues here with the iPDS-100 locking up during boot. Removed U16, the 74LS51N. Suspect this may have been due to my using HCT for U18, the '00.

Suggestions

1. Leave U16 off, it's not necessary for SBX bubble memory, or
2. Consider using 74LS00 for U18 rather than 74HCT00, or
3. Consider adding pullups to U18 pins 1,2,4,5

