Stephen R. Bennett

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Objective

Seeking an entry level hardware position to begin in the Spring or Fall of 2014.

Education

University of Colorado at Boulder August 2013 – Present

- **MS:** Electrical Engineering
- **Emphasis:** Power & Analog Electronics
- **Current GPA:** 4.000/4.000

University of Colorado at Boulder Spring 2009 – May 2013

- **BS:** Electrical and Computer Engineering
- **Minor:** Computer Science
- **Emphasis:** Communications
- **Final GPA:** 3.929/4.000
- **Dean's List** eight (8) consecutive semesters
- **ILR 3** Professional working proficiency French

Skills

Proficient in: Altium Electronics Designer (Protel) · MATLAB & Simulink · LTspice · Python · C · C++ · Perl · Git · Ply · Linux Multithreading · Android Java and XML · Vim · Eclipse · Linux CLI · UML
Familiarity with: Switched Mode Power Converters · Bash Scripting · Assembly (IA32 & ARM) · HTML5 · Bandgap Voltage References · Op-Amp Analysis and Design · Regular Expressions · OrCAD · Mathematica · Win32 CLI · Verilog HDL · LaTeX

Work History

Qualcomm Inc. May 2013 – August 2013
Software Engineering Intern

- Developed log parsing system in Python from scratch in a pair programming environment with a Scrum workflow.
- Created UML documentation for the system.
- Wrote extensive documentation for the system using Sphinx.
- Used Git and Gerrit for version control and code review.

Qualcomm Inc. May 2012 – August 2012
Software Engineering Intern

- Worked on the team responsible for development of Femto Site Modems.
- Wrote, tested, and debugged Perl scripts and modules to automate continuous integration testing of all codebase changes.
- Developed continuous integration testing framework that interfaces with a Wiki and Testlink server so engineers can immediately identify regressions or other errors.
- Extensively documented work and changes and kept team up to speed.
- Used Git and Gerrit for version control and code review.

Blue Canyon Technologies June 2011 – January 2012
Engineering Intern

- Wrote, tested, and debugged C code for National Instruments Data Acquisition Unit to interface with Simulink and reaction wheel rate sensors.
- MATLAB & Simulink experience implementing C code and assisted in developing control algorithms.
- Designed form factored PCBs for final flight boards.

Colorado Space Grant Consortium January 2011 – January 2012
Electrical Power Systems Engineer

- Developed, built, tested, and debugged Battery Charging and Protection Circuitry to interface with a maximum power point tracker and 918 solar cell array.
- Designed form factored PCB for final flight board.
- Requirement verification and tracking, along with setting and meeting hard deadlines.
- Programmed MSP-430 microcontroller to communicate with Command and Data Handling processor over SPI.
- PCB design experience with power circuit layouts on a 4-layer PCB.
- LTspice logic analysis for power allocation (Separation Switch).

University of Colorado at Boulder Fall 2010
Tutor in C Programming

- Tutored C Programming Course that covered structs, linked lists, database management, and basic graphics implementation (Allegro).