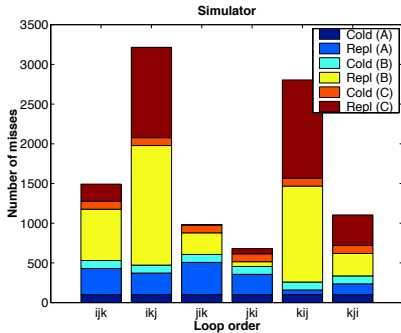**Figure 2: Miss counts for Problem 1, scenario 1. (a) Our approach. (b) Cache simulation.**