

(a)

Loop order	A					B					C					Grand Total
	Bnd	Int	Tot	Cold	Repl	Bnd	Int	Tot	Cold	Repl	Bnd	Int	Tot	Cold	Repl	
ijk	25	405	430	100	330	85	661	746	100	646	18	298	316	100	216	1492
ikj	23	349	372	100	272	73	1533	1606	100	1506	32	1205	1237	100	1137	3215
jik	97	409	506	100	406	28	345	373	100	273	3	97	100	100	0	979
jki	95	261	356	100	256	28	131	159	100	59	5	160	165	100	65	680
kij	13	146	159	100	59	33	1276	1309	100	1209	82	1254	1336	100	1236	2804
kji	16	220	236	100	136	31	352	383	100	283	81	404	485	100	385	1104

(b)

Loop order	A					B					C					Grand Total
	Bnd	Int	Tot	Cold	Repl	Bnd	Int	Tot	Cold	Repl	Bnd	Int	Tot	Cold	Repl	
ijk	21	964	985	111	874	90	1799	1889	111	1778	0	2393	2393	111	2282	5267
ikj	1	864	865	111	754	110	1846	1956	111	1845	0	2556	2556	111	2445	5377
jik	107	578	685	111	574	4	1900	1904	111	1793	0	2123	2123	111	2012	4712
jki	111	558	669	111	558	0	1789	1789	111	1678	0	2232	2232	111	2121	4690
kij	5	545	550	111	439	20	1866	1886	111	1775	86	2299	2385	111	2274	4821
kji	6	577	583	111	472	5	1823	1828	111	1717	100	2229	2329	111	2218	4740

(c)

k	A					B					C					Grand Total
	Bnd	Int	Tot	Cold	Repl	Bnd	Int	Tot	Cold	Repl	Bnd	Int	Tot	Cold	Repl	
1	25	405	430	100	330	85	661	746	100	646	18	298	316	100	216	1492
2	40	305	345	100	245	71	1198	1269	100	1169	17	227	244	100	144	3350
3	40	449	489	100	389	68	1119	1187	100	1087	20	311	331	100	231	5357

Figure 3: Miss counts from our approach (Bnd and Int) and from cache simulation (Cold and Repl). (a) Problem 1, scenario 2. (b) Problem 1, scenario 3. (c) Problem 1, scenario 4.