

# On-Site Gamma-Hadron Separation with Deep Learning on FPGAs

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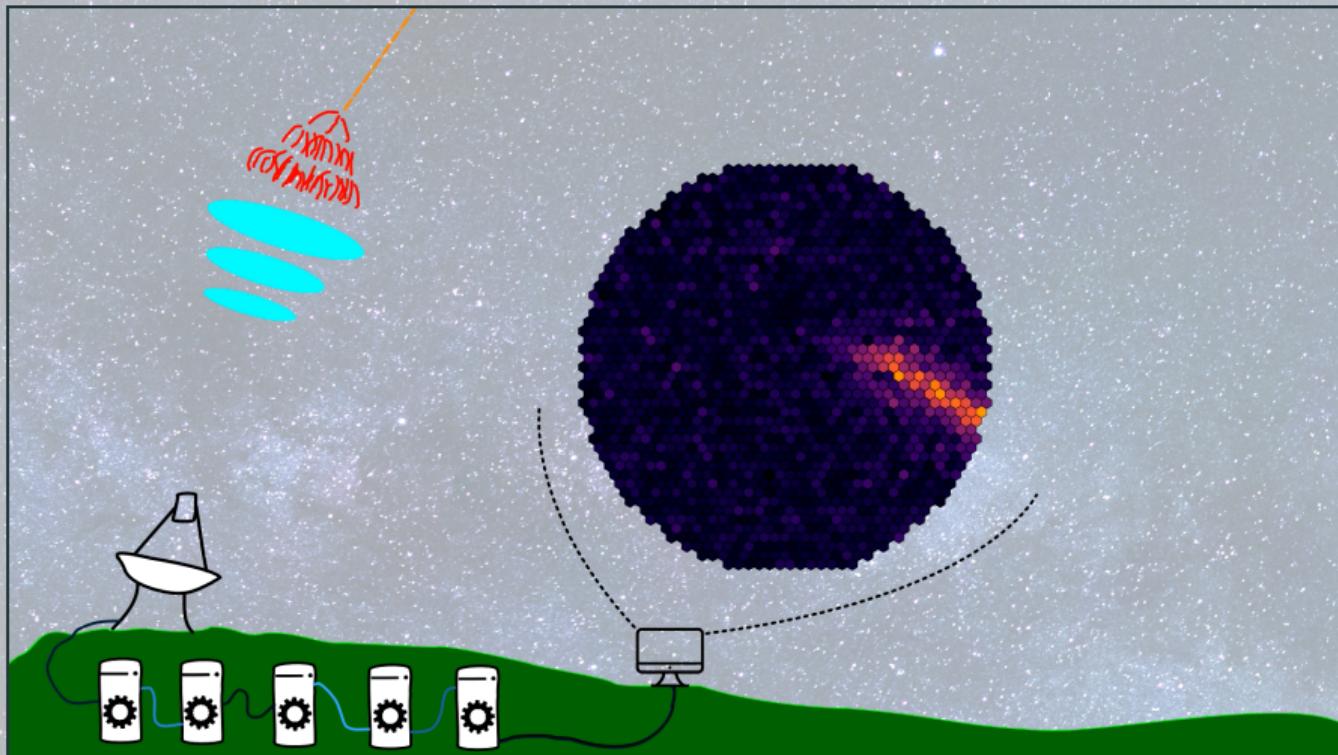
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- Collaborative Research Center 876



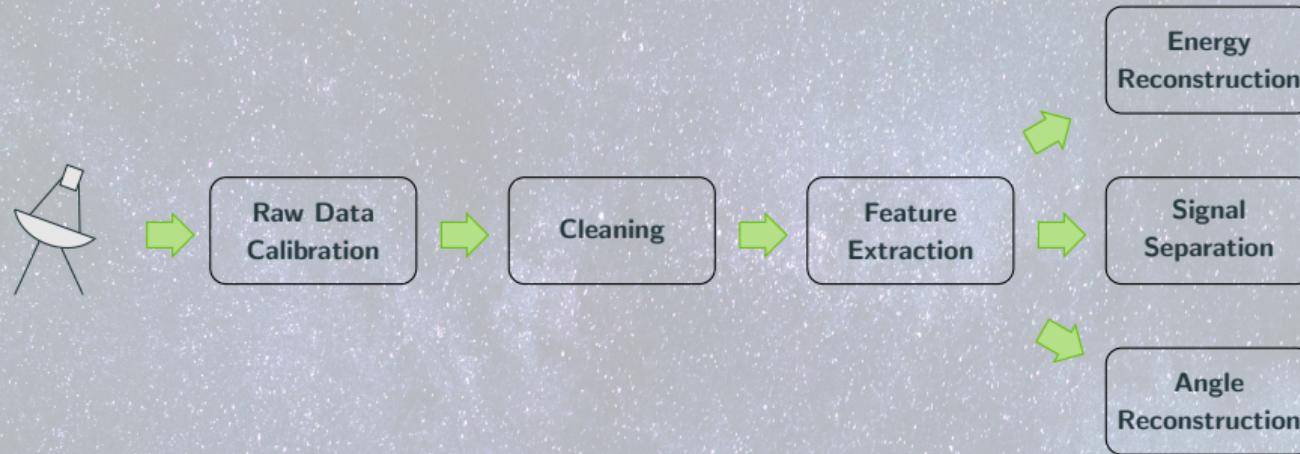
# Gamma-Ray Astronomy



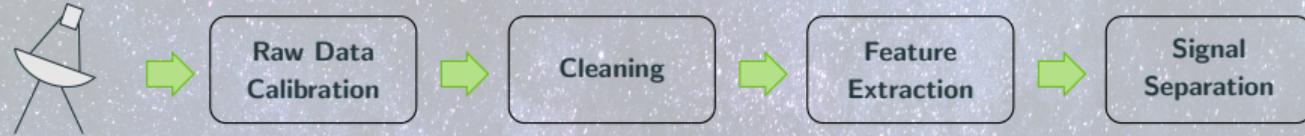
**FACT** First G-APD Cherenkov Telescope continuously monitors the sky for gamma rays

- It produces roughly 180 MB/s of data
- Only 1 in 10.000 measurements is interesting
- Bandwidth / computation power / physical space is limited

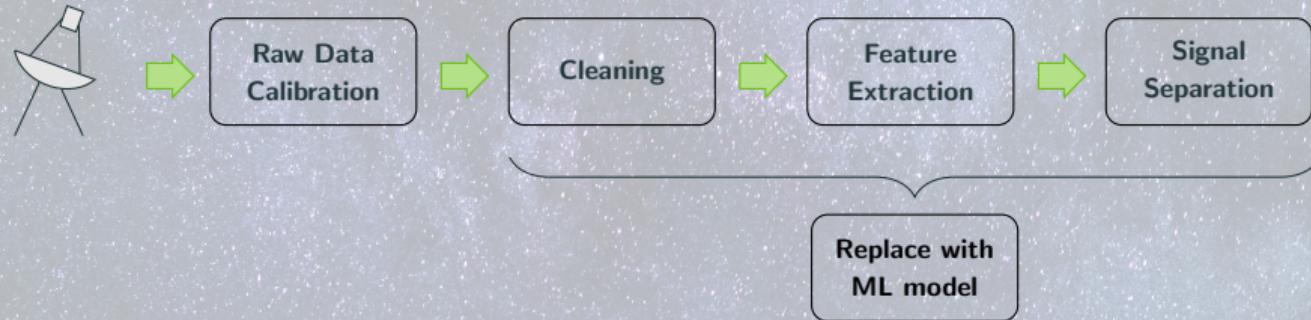
# Telescope processing pipeline



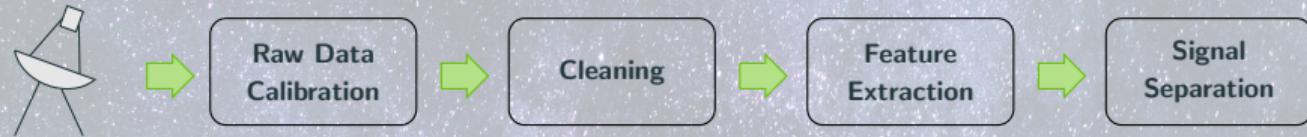
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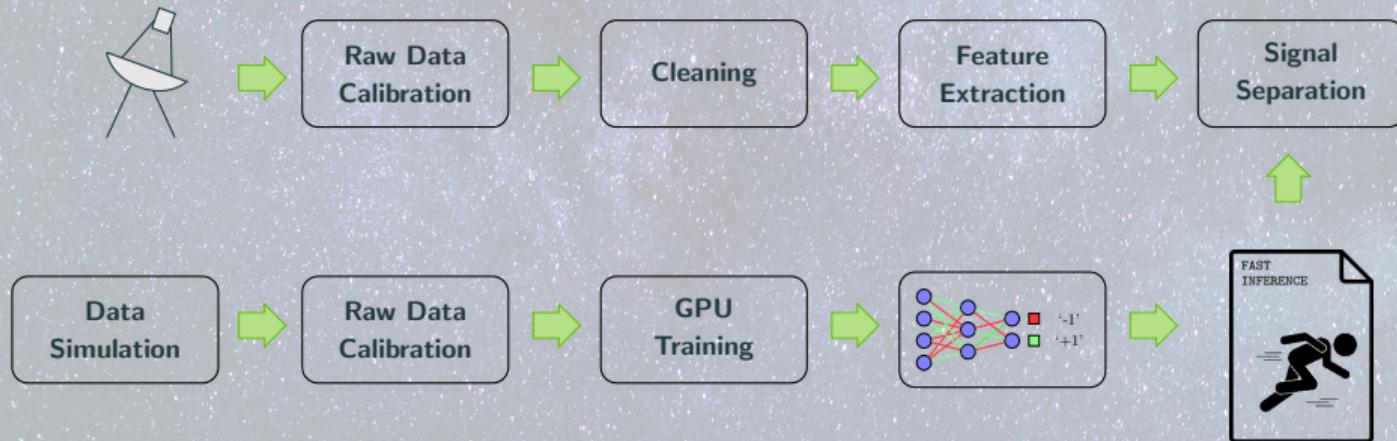
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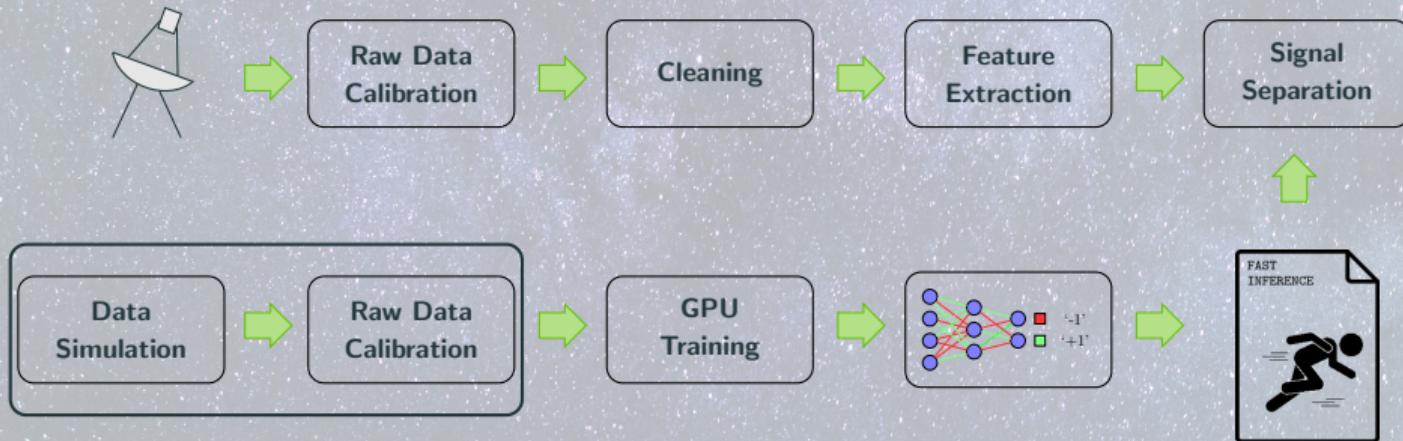
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## Simulation Data

- CORSIKA simulation
- with and without quality cuts
- downsampled to 200K/100K train/test data, equal distribution

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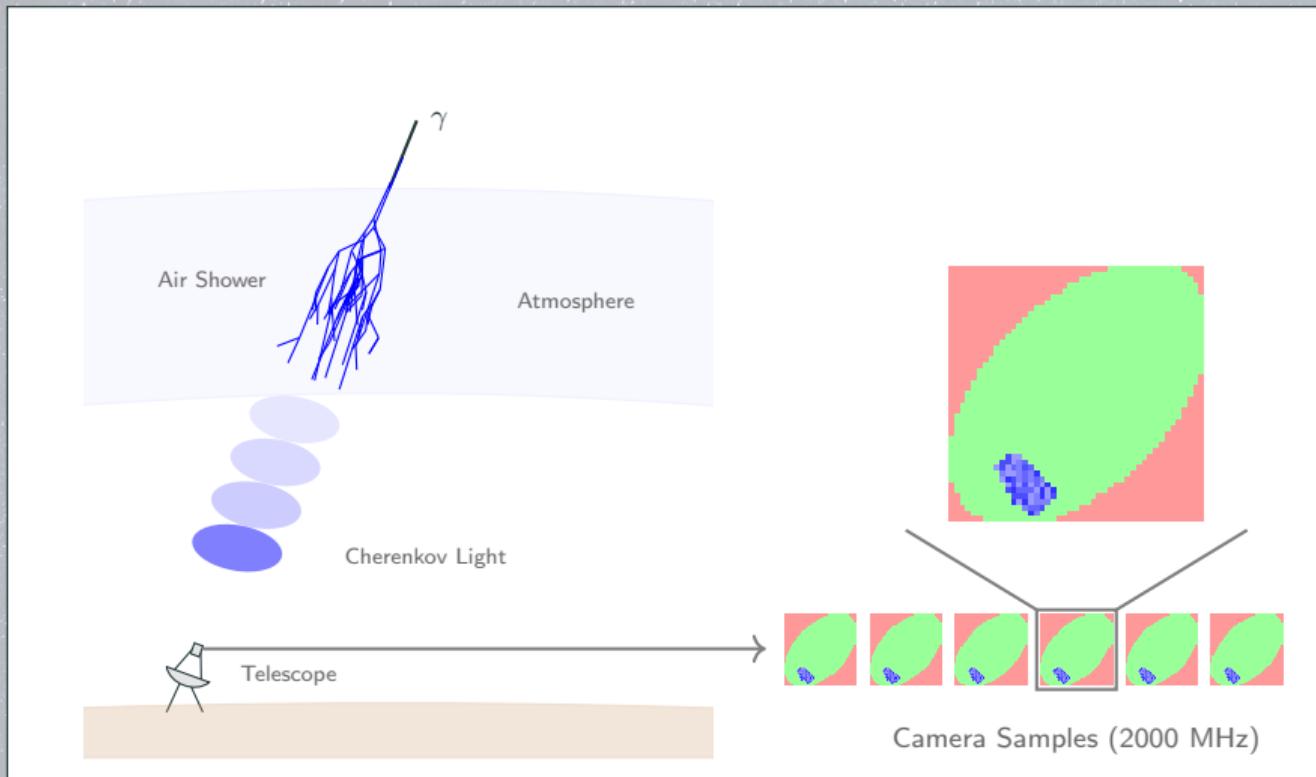
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## Pre-processing

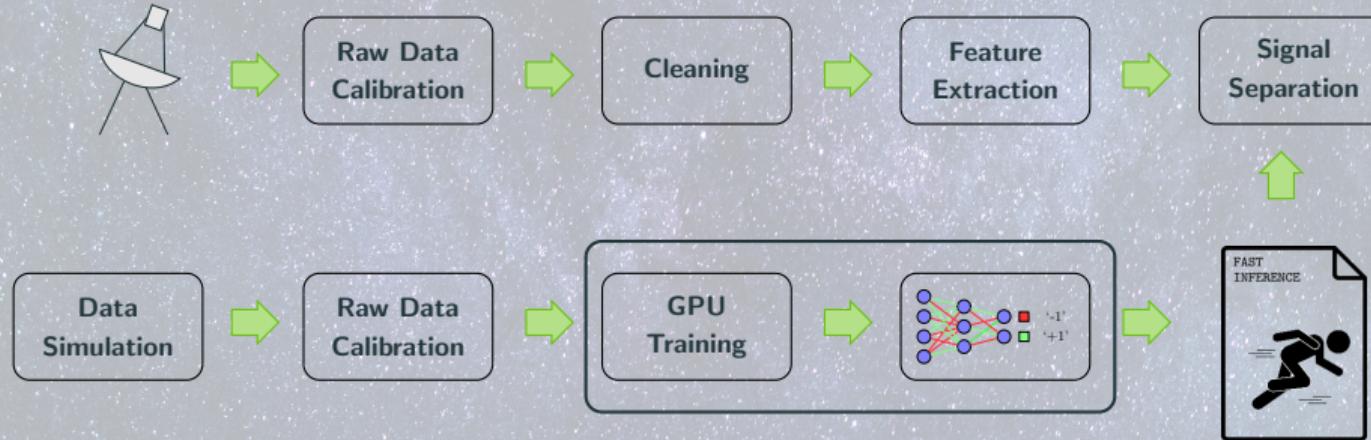
- Subtract reference voltage curves from measurement to count no of photons
- Focus on 50 ns ROI from 300ns time series

**Result**  $45 \times 45$  images where each pixel contains a photon count

# The FACT data



# Telescope processing pipeline



**General approach** Start with something simple and gradually increase complexity

**Input data**  $\mathcal{D} = \{(x_1, y_1), \dots, (x_N, y_N)\}$  with  $x_i \in \mathbb{N}^{45 \times 45}$ ,  $y_i \in \{0, 1\}$ ,  $N_{train} = 200K$ ,  $N_{test} = 100K$

**Take-Aways** In total 1178 experiments performed

- Smaller architectures work better
- Early stopping / Learning-rate scheduler helps
- ResNet does not seem to improve performance

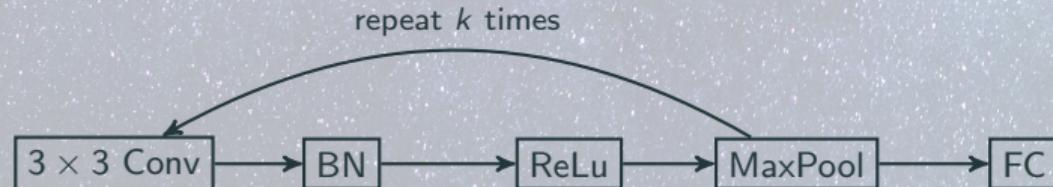
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**Final model** Simple VGG-like architecture



## Binarized Neural Networks on FACT

**Binarized Neural Networks** Use weights from  $\{-1, 1\}$  instead of  $\mathbb{R}$

**For training** Use deterministic binarization + full precision SGD

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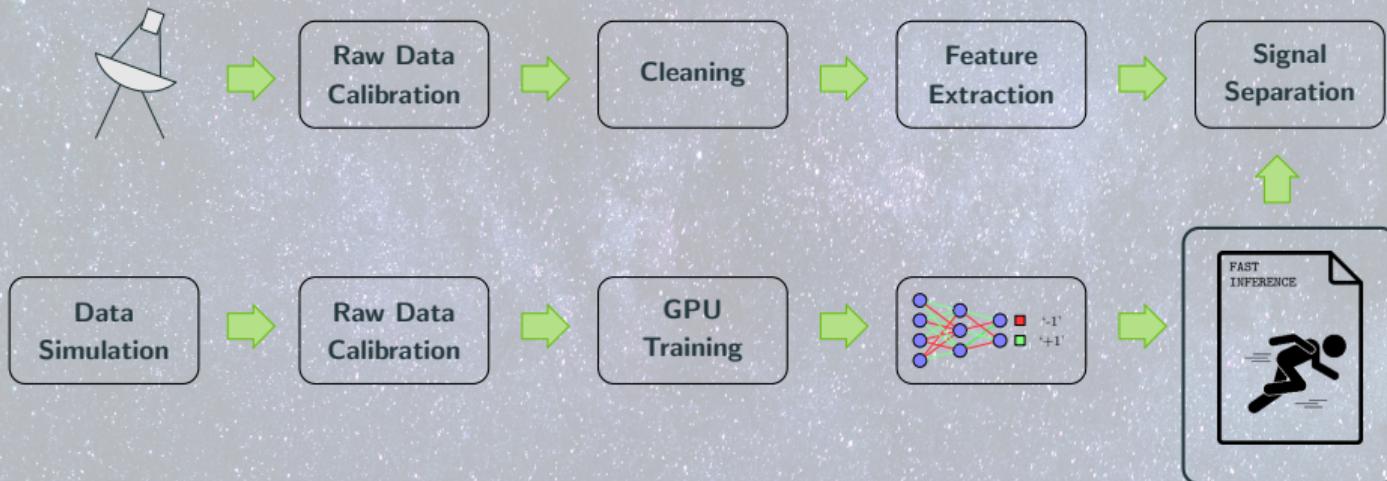
**Why BNNs?** Only 2 clocks needed to process 32/64/128/256 bits (= weights)

**Approach** Map weights/inputs to bitstring ' $-1 \rightarrow 0$ ' and ' $+1 \rightarrow 1$ '

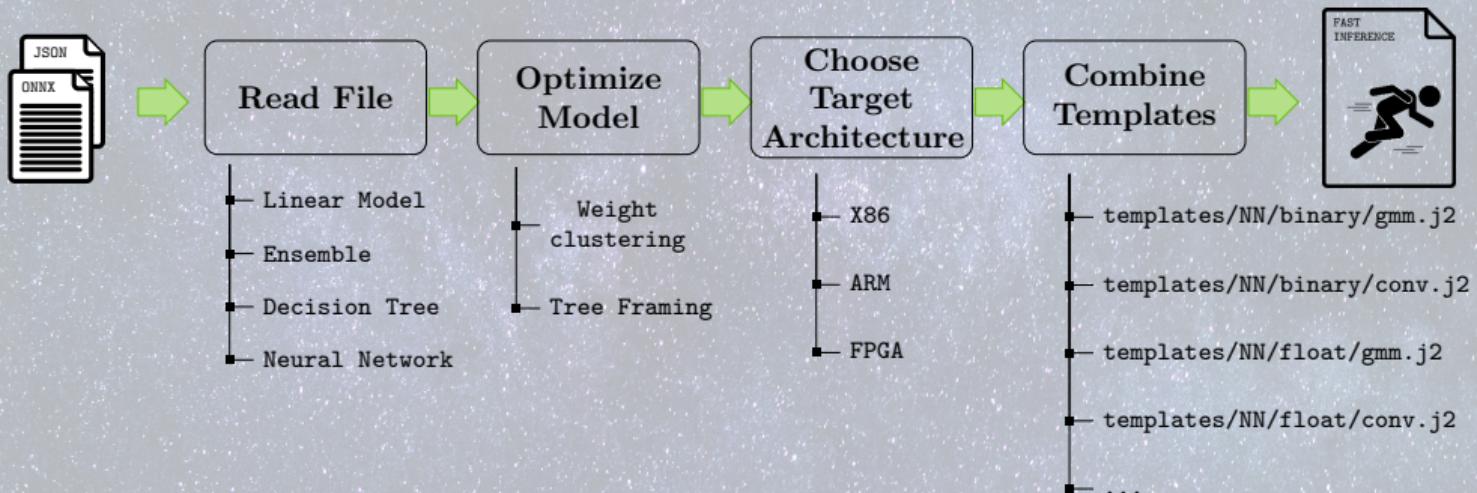
- $f_i w_i$  is ' $+1$ ' if same sign, else ' $0$ '. This is an XOR operation
- $\sum_i f_i w_i$  counts occurrences of same sign. This is the popcount operation.

$$\sum_i f_i w_i = \text{POPCNT}(f \text{ XOR } W)$$

# Telescope processing pipeline



# FastInference: Workflow and Capabilities



1. How do RandomForest, CNNs and Binary CNNs perform on simulation data?  
→ CNNs should outperform Binary CNNs should outperform RF on simulation
2. How do RandomForest, CNNs and Binary CNNs perform on real-world data?  
→ (Binary) CNNs hopefully outperform RF on real-world data
3. Is the implementation of FastInference real-time capable?  
→ Binary CNNs should be faster than regular CNNs, regardless the target architecture

## Deep Learning vs Random Forest on Simulation Data

Model	Data	Accuracy, no QC		Accuracy, QC	
		epochs:100	epochs:10	epochs:100	epochs:10
RF	DL2	0.70959		0.78483	
RF	PhC	0.74711		0.78839	
CNN(small)	PhC	0.90825	0.88867	0.93441	0.93846
BNN(small)	PhC	0.90861	0.88644	0.90440	0.88866
CNN(large)	PhC	<b>0.91094</b>	0.90251	0.93735	<b>0.94228</b>
BNN(large)	PhC	0.90011	0.89925	0.93112	0.91369

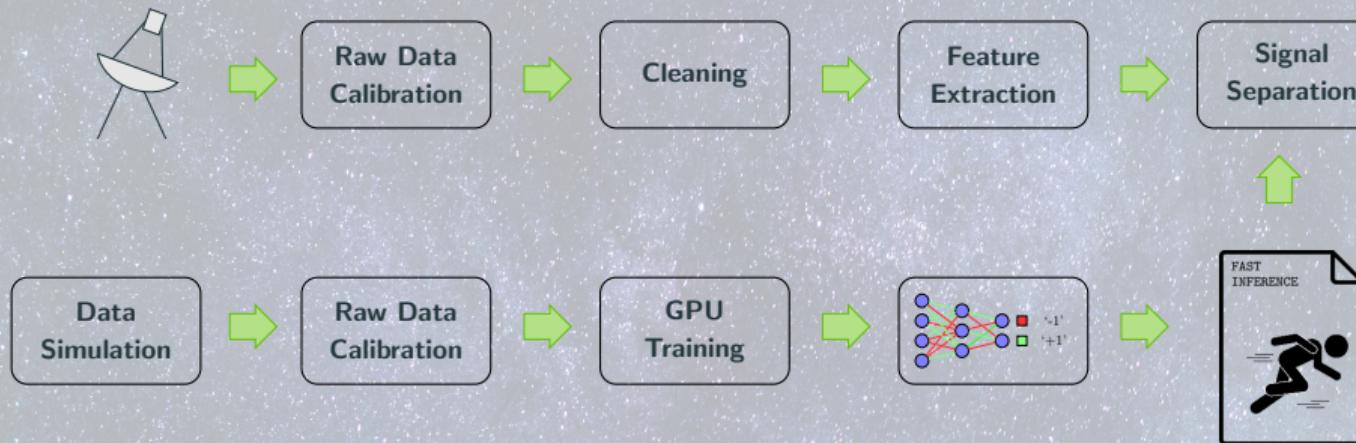
# Deep Learning vs Random Forest on Crab Nebula Data

Model	Data	S <sub>li&amp;ma</sub> , no QC		S <sub>li&amp;ma</sub> , QC	
		epoch: 100	epoch: best loss	epoch: 100	epochs:best loss
RF	DL2		22.86 $\sigma$		23.82 $\sigma$
RF	PhC		2.09 $\sigma$		3.35 $\sigma$
CNN(small)	PhC	24.09 $\sigma$	25.83 $\sigma$	24.12 $\sigma$	24.89 $\sigma$
BNN(small)	PhC	19.55 $\sigma$	25.87 $\sigma$	22.96 $\sigma$	21.67 $\sigma$
CNN(large)	PhC	23.68 $\sigma$	24.64 $\sigma$	24.20 $\sigma$	23.17 $\sigma$
BNN(large)	PhC	22.70 $\sigma$	22.92 $\sigma$	22.35 $\sigma$	22.26 $\sigma$

# X86 CPU vs FPGAs for Deep Learning

System	Type	Runtime [ms/event]	
		float	binary
ONNX Runtime	large	$21.083 \pm 0.078$	$26.642 \pm 0.100$
	small	$0.957 \pm 0.020$	$1.861 \pm 0.037$
Generated Code	large	$78.583 \pm 1.704$	$11.250 \pm 0.077$
	small	$2.757 \pm 0.026$	$1.574 \pm 0.014$
FPGA	large	-	$561.588 \pm 0.000$
	small	-	$4.221 \pm 0.000$
FPGA pipelined	large	-	$72.657 \pm 0.000$
	small	-	$0.662 \pm 0.000$

## Recap: (Binarized) CNNs work well on simulated and real-world FACT data



- ✓ Excellent performance on training data
- ✓ Improved performance on real-world data
- ✓ Real-time capabilities on small devices
- ✓ FPGA implementation available if necessary