



Certificate no: UC-9acfd2a6-937b-4277-aa81-ca670ea123b4

Certificate url: [ude.my/UC-9acfd2a6-937b-4277-aa81-ca670ea123b4](https://ude.my/UC-9acfd2a6-937b-4277-aa81-ca670ea123b4)

Reference Number: 0004

CERTIFICATE OF COMPLETION

# VLSI - Design For Test (DFT)- JTAG, Boundary SCAN and IJTAG

Instructors **VLSI Foundation**

**Sahana Waikar**

Date **Jan. 14, 2023**

Length **2 total hours**