Grade Sheet for Lab #4

Winter 2015.

Group Number: _48_

Group Member Name: _Stephen Carter_ .   Student Number: _260500868_ .

Group Member Name: _Greg Rohtek._ .   Student Number: _260516980_ .

Marks		
2	1.	VHDL description of the note timer circuit
2	2.	Simulation of the note timer circuit
2	3.	Testing of the note timer circuit on the Altera DE1 board
2	4.	Observing the wave data section size on the control panel
2	5.	Reading the wave data section size on the Altera DE1 board
2	6.	Modified VHDL code for the flash read control module
2	7.	Playback of the wave on the Altera DE1 board

TA Signatures

Each part should be demonstrated to one of the TAs who will then give a grade and sign the grade sheet. Grades for each part will be either 0, 1, or 2. A mark of 2 will be given if everything is done correctly. A grade of 1 will be given if there are significant problems, but an attempt was made. A grade of 0 will be given for parts that were not done at all, or for which there is no TA signature.

McGill University ECSE-323 Digital System Design / Prof. J. Clark