

Winter 2015.

Group Number: 48 Group Member Name: Stock Care Student Number: 2600088  Group Member Name: Crop Roblinek.  2 1. VHDL description of the note timer circuit  2 2. Simulation of the note timer circuit  1 Testing of the note timer circuit on the Altera DE1 board  2 3. Testing of the wave data section size on the control panel  3 Reading the wave data section size on the Altera DE1 board  4 Modified VHDL code for the flash read control module  7 Playback of the wave on the Altera DE1 board  4 TA Signatures  TA Signatures													
Student Number: 2600868  Crey Robbleck.  Student Number: 2600868  Student Number: 2600086  Student Number: 260086  Student Number: 2600086  Studen		[	0	10	7	פ	2	7	2	Marks			
Student Number: 260088  Crey Rower.  Student Number: 260088  Student Number: 260088  Student Number: 2605169  In of the note timer circuit  note timer circuit  e timer circuit on the Altera DE1 board  e data section size on the Altera DE1 board  code for the flash read control module  vave on the Altera DE1 board			7	6.	.5	4.	ω.	2.	<u> </u>		Grou	Grou	Grou
			Playback of the wave on the Altera DE1 board	Modified VHDL code for the flash read control module	Reading the wave data section size on the Altera DE1 board	Observing the wave data section size on the control panel	Testing of the note timer circuit on the Altera DE1 board	Simulation of the note timer circuit	VHDL description of the note timer circuit		Student Num	Student Num	p Number: 48
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there is no TA signature attempt was made. A grade of 0 will be given for parts that were not done at all, or for which everything is done correctly. A grade of 1 will be given if there are significant problems, but an grade sheet. Grades for each part will be either 0, 1, or 2. A mark of 2 will be given if Each part should be demonstrated to one of the TAs who will then give a grade and sign the