

# SHIVANI BHATIA

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## EDUCATION

**BOSTON UNIVERSITY COLLEGE OF ENGINEERING | BOSTON, MA**  
BACHELOR OF SCIENCE IN COMPUTER ENGINEERING, MAY 2018-EXP

## TECHNICAL SKILLS

- ♦ Programming Languages: C++, C#, CSS3, HTML5, SQL, C, MATLAB, VHDL, Python, JavaScript, PHP
- ♦ Programming Environments: CCS, ISE Design Suite 14.7, Microsoft .NET framework, Microsoft Visual Studio
- ♦ Operating Systems: Linux, Mac OS, Windows

## PROFESSIONAL EXPERIENCE

**BOSTON UNIVERSITY ELECTRICAL AND COMPUTER ENGINEERING**  
LAB ASSISTANT | BOSTON, MA | SEPTEMBER 2017– PRESENT

- ♦ Assist students in creating various Windows forms applications in Visual Studio using the .NET framework and C#
- ♦ Assess the quality and accuracy of submitted assignments by testing the applications

**BOSTON UNIVERSITY MECHANICAL ENGINEERING**

LEARNING ASSISTANT | BOSTON, MA | JANUARY 2016– DECEMBER 2016, SEPTEMBER 2017 - PRESENT

- ♦ Acted as a lab assistant to freshman and sophomores in a 25 student introductory design class
- ♦ Assisted students in creating their prototypes, generating design reports, and recite oral presentations to market their product
- ♦ Supervise the design lab and stock room to ensure that students receive appropriate parts for their prototypes

## ANDESITE

RESEARCH ASSISTANT (POWER ENGINEER) | BOSTON, MA | FEBRUARY 2016– MAY 2017

- ♦ Created schematics and designed printed circuit boards for solar panels using Eagle CAD
- ♦ Conducted thermal analysis and tested efficiency of solar cells using IV characteristic curves
- ♦ Assembled solar panels using solder paste and silver epoxy
- ♦ Assisted in calculating the power budget for the satellite while in space

**BOSTON UNIVERSITY ELECTRICAL AND COMPUTER ENGINEERING**

TEACHING ASSISTANT | BOSTON, MA | SEPTEMBER 2016– DECEMBER 2016

- ♦ Taught an introductory logic design lab with 20 students to design and construct circuits
- ♦ Solved real world tasks using Boolean algebra, combinational logic, and finite state machines manually and electronically
- ♦ Designed, constructed, and debugged programs written in Verilog HDL and loaded them to FPGA boards

**ORIGO | [HTTP://ORIGO.US](http://origo.us)**

LEAD PROGRAMMER | JAMESTOWN, RI | JULY 2015– FEBRUARY 2016

- ♦ Designed and developed a website for a startup business using HTML5 and CSS3
- ♦ Generated online submission forms using JavaScript
- ♦ Programed picture slideshows and generated templates for different browsers and devices
- ♦ Launched the website

## LEADERSHIP

**DELTA PHI OMEGA SORORITY INC. | BOSTON UNIVERSITY**

PRESIDENT, MAY 2016 – MAY 2017

HISTORIAN, SEPTEMBER 2015 – MAY 2016

## LANGUAGES

- ♦ English
- ♦ Spanish
- ♦ Hindi
- ♦ Kutchi