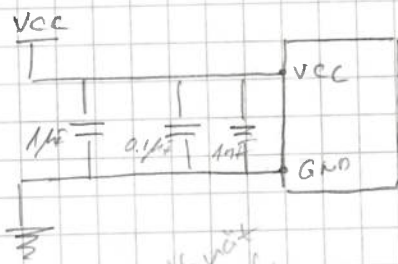
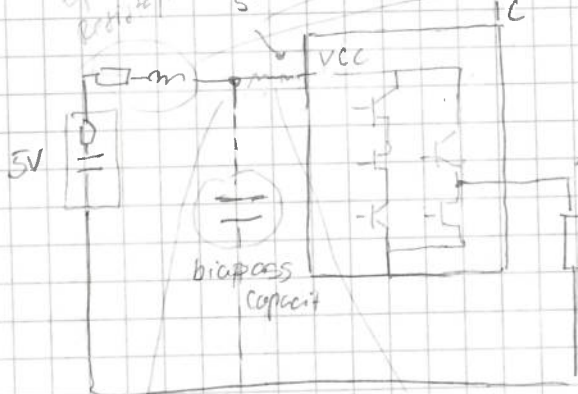


WHY MULTIPLE CAPACITORS

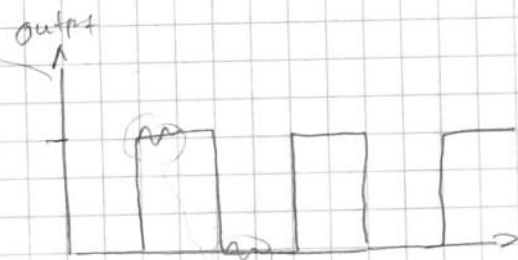
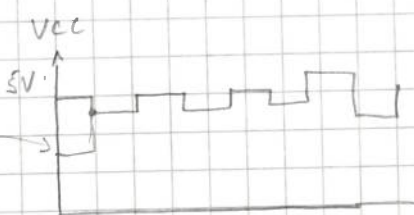


erhöht mit
Reihe/Induktiv



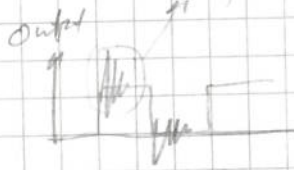
Sollte sein möglich
Till IC
low impedance
low conductance

Induktiv



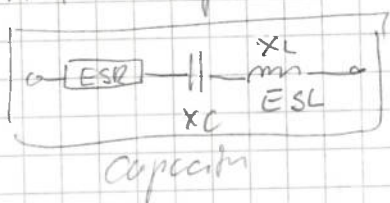
10kHz signal (high frequency)

high frequency



Varianz in behavior different
würde die Kapazität (nicht bar, a en)

A Real Capacitor

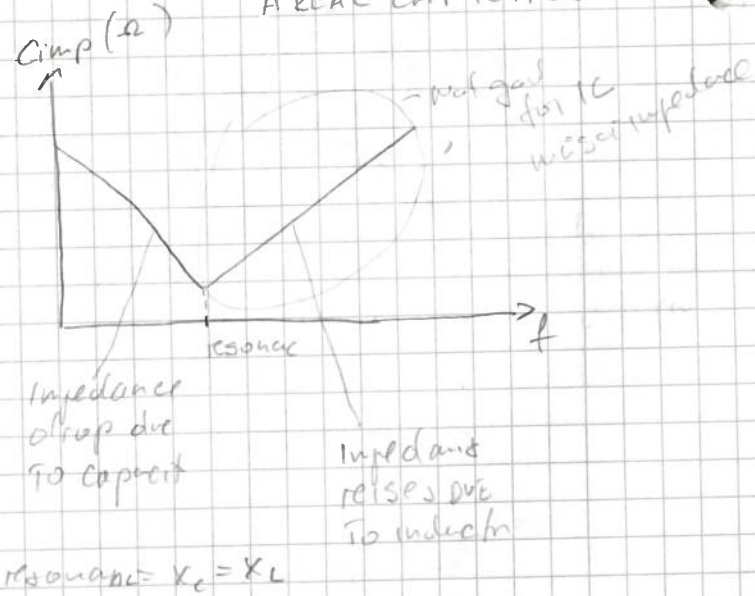


$$X_C = \frac{1}{2\pi f C}$$

$$X_L = 2\pi f L$$

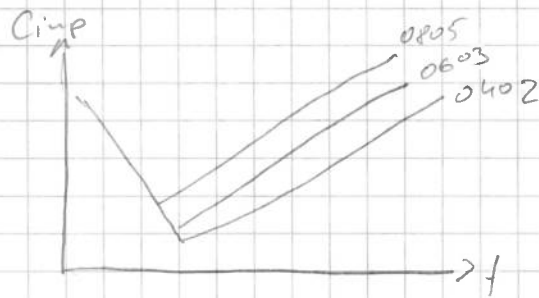
$$Z_{imp} = ESR + X_C + X_L$$

A REAL CAPACITOR

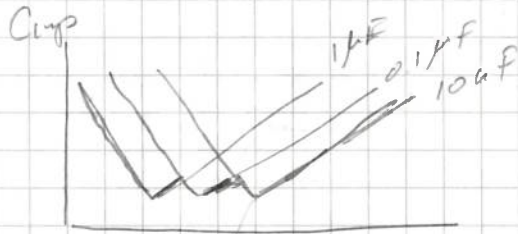


$$resonance = X_C = X_L$$

Different poles at different impedances:

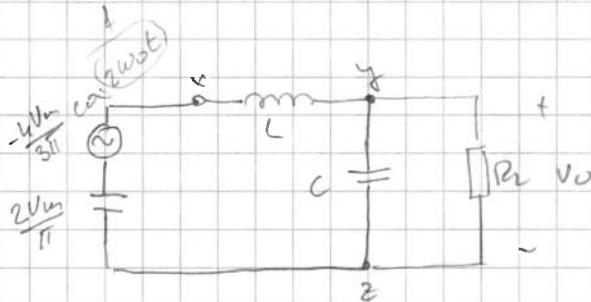
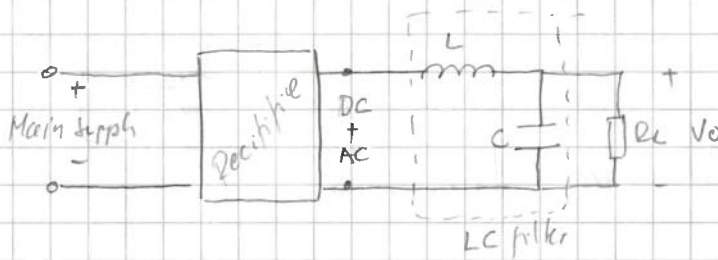


for highest pole value
just same pole value

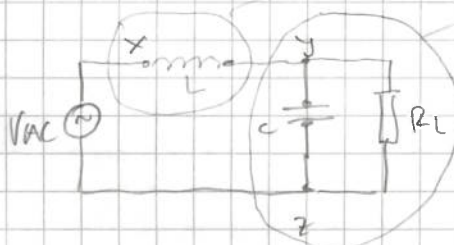
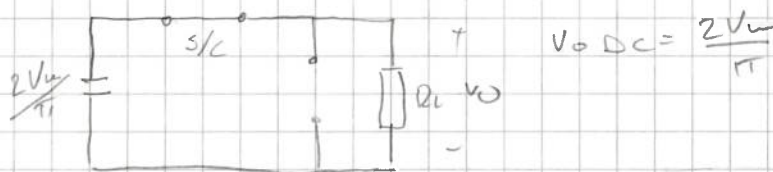


start pole value over rate all like
vid bröps multiple effects

LC FILTER



⊗ DC



$$Z_{xy} = j\omega L$$

$$Z_{xz} = (Y_C || R_L)$$

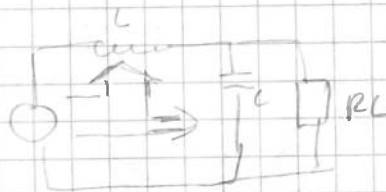
$$V_{oAC} = \frac{Z_{yz}}{Z_{xy} + Z_{yz}} V_{AC}$$

if $Z_{X0} \gg Z_{Y2} \Rightarrow V_0 = \frac{Z_{Y2}}{Z_{X0}} V_{ac}$

$X_C \ll R_L$

$X_C \parallel X_L \approx R_C \Rightarrow V_0 = \frac{X_C}{X_L} V_{ac}$

* $\boxed{X_L \gg (X_C \parallel R_L)}$ Two options
 $X_C \ll R_L$



Critical Inductor (L_c):

$I_{DC} = \frac{2V_m}{\pi R_L}$

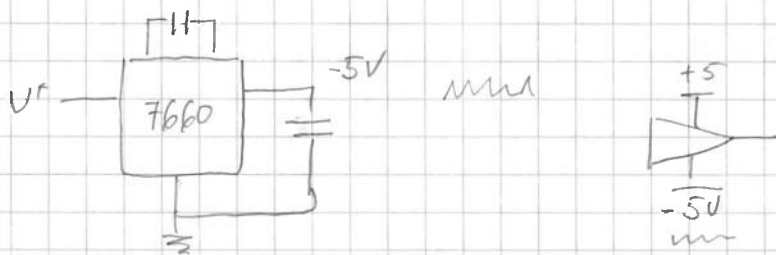
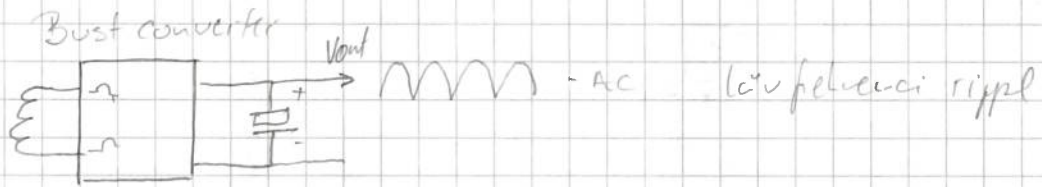
$|I_{ac}|_{peak} = \frac{|V_{ac}|_{peak}}{|X_L|} = \frac{4 V_m}{3\pi (2\omega L)} = \frac{2V_m}{3\pi \omega L}$

$L = L_c \quad I_{DC} = |I_{ac}|_{peak} \Rightarrow \frac{2V_m}{\pi R_L} = \frac{2V_m}{3\pi \omega L} \quad |_{L=L_c}$
 \downarrow
 $c = critical$

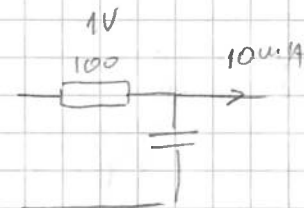
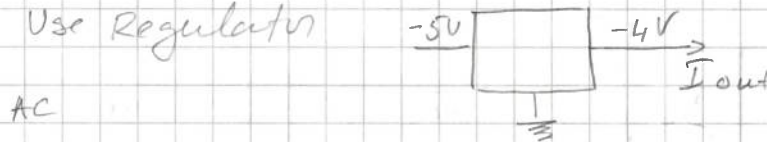
* $\boxed{L_c = \frac{R_L}{3\omega_0}}$

Allways $L > L_c \quad |I_{ac}|_{peak} < I_{DC}$

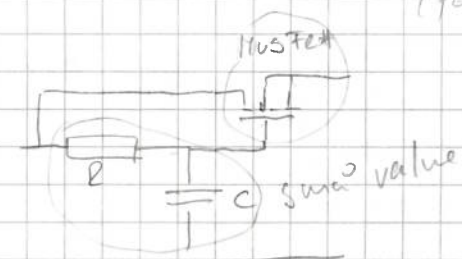
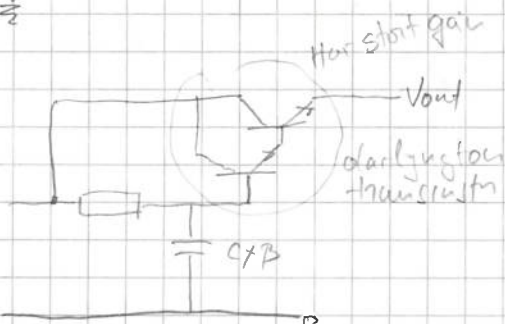
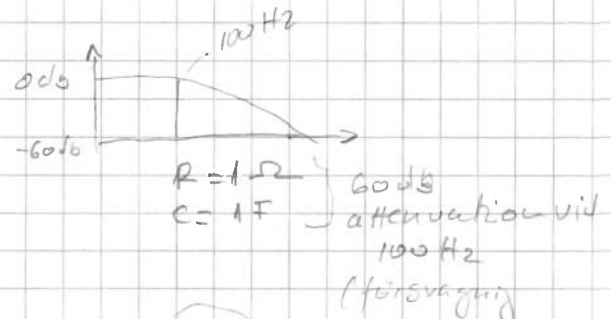
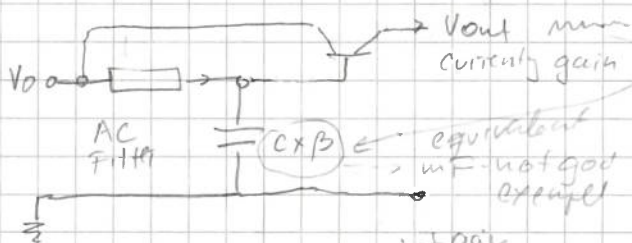
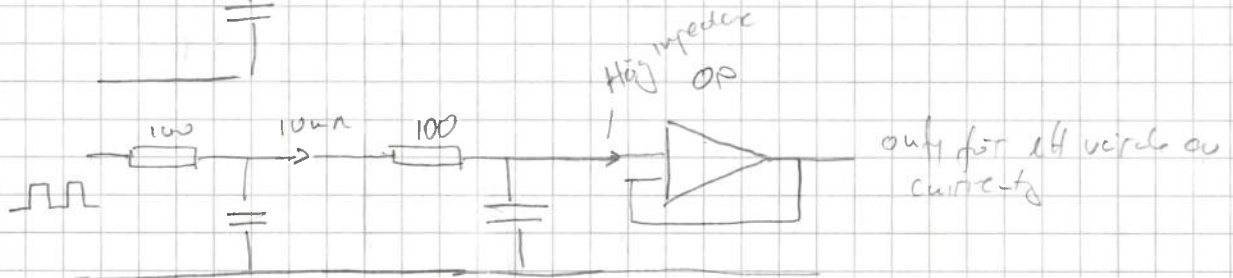
How Remove Power Supplyer Ripphe?



Use Regulator

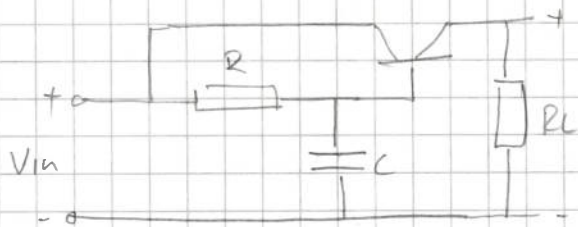


↓ resistor \Rightarrow ↑ capacitor value



Only use for Riple!
NOT GOOD FOR REGULATION

TEST



10k
 $\uparrow R = \text{Dimp ökar}$
 $\uparrow \text{Vin} C = 7 \mu F$

DC input:
 \Rightarrow output:
 Ripple

$$f_{-3dB} = \frac{1}{2\pi RC} \quad \left. \begin{array}{l} R = 1k \\ C = 100n \end{array} \right\} \Rightarrow f_{-3dB} = 1.59 \text{ kHz}$$

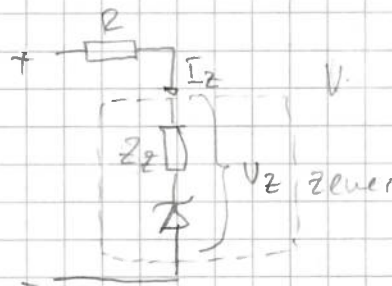
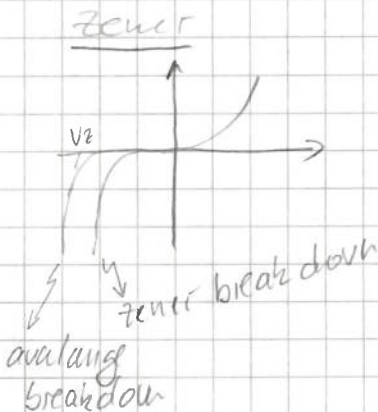
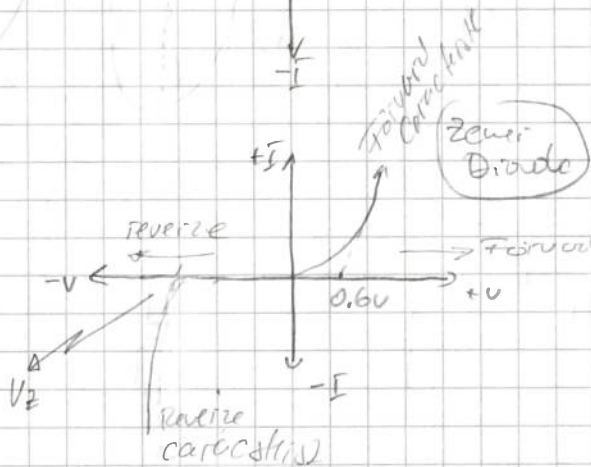
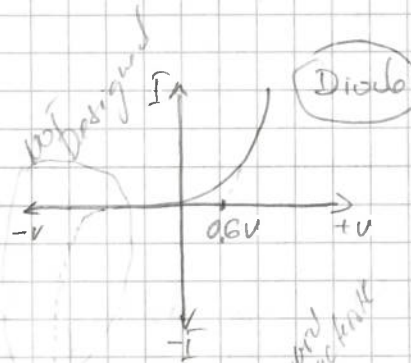
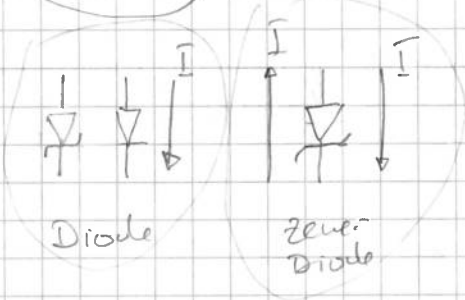
men $\beta = 100$ 80°:

$$\text{for } 1k\Omega \text{ \& } (100nF \times 10^3) \Rightarrow f_{-3dB} = 15.9 \text{ Hz}$$

$\Rightarrow 10 \mu F$

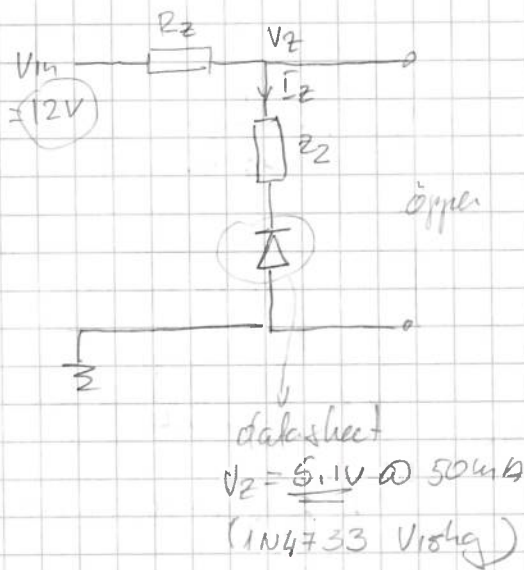
input
 output

DIODE



Z_z är insidan
 of zener diode

Regulator (typical for zener diode)



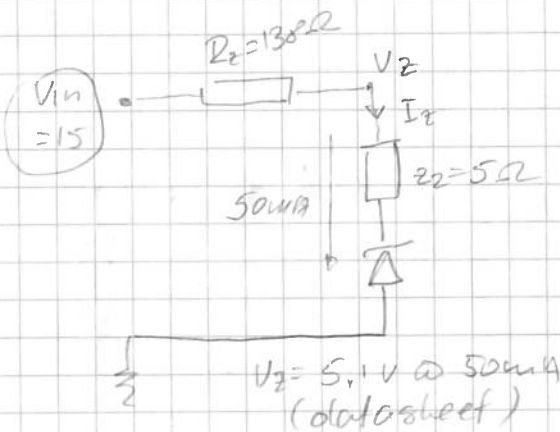
öppen:

$$V_{in} = 12V \quad I_c = \text{open}$$

$$\begin{aligned} I_{zT_1} &= 50mA \\ Z_{T_1} &= 5\Omega \end{aligned} \quad \text{datasheet}$$

$$R_2 = \frac{V_{in} - 5.1}{I_z}$$

$$R_2 = \frac{12 - 5.1}{50mA} = 138\Omega$$



$$V_{in} = 15V$$

$$R_2 = 138\Omega$$

$$Z_2 = 5\Omega$$

$$I_z = \frac{15 - 5.1}{138\Omega} = 71.7\mu A$$

$$\text{so } I_z = 71.7\mu A - 50\mu A = 21.7\mu A$$

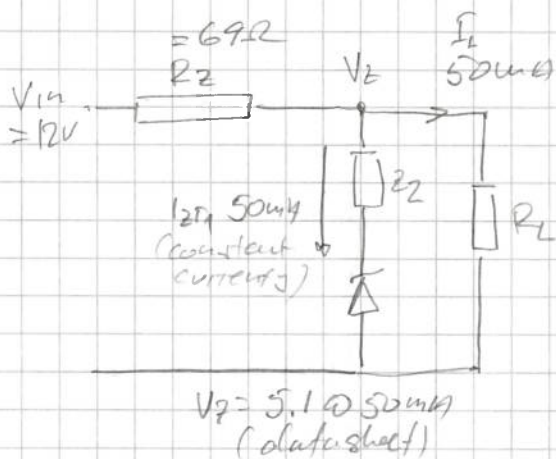
$$\Delta V_z = \Delta I_z \cdot Z_2$$

$$= 21.7\mu A \cdot 5\Omega$$

$$= 0.11V$$

lag current is negligible

$$P_z = V_z \cdot I_z = 5.1 \cdot 50 = 0.25W$$



$$V_{in} = 12V$$

$$V_z = 5.1V$$

$$I_L = 50mA$$

$$R_2 = \frac{V_{in} - V_z}{I_{zT} + I_L} = \frac{12V - 5.1V}{50mA + 50mA} = 69\Omega$$

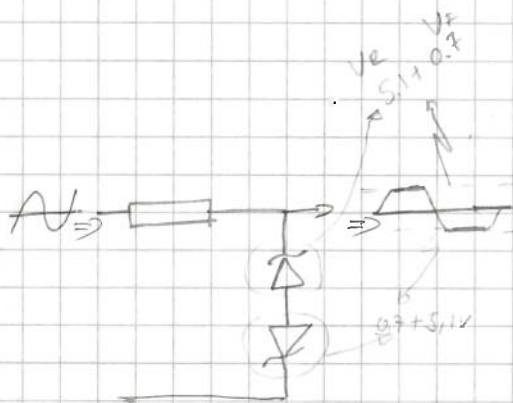
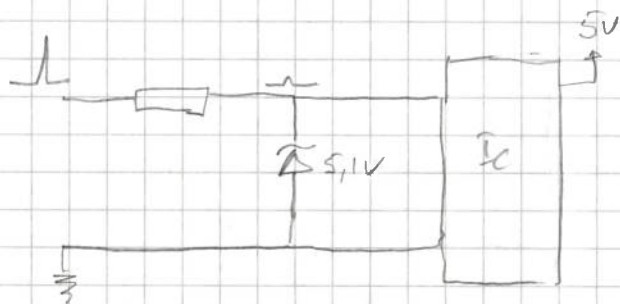
From Datasheet

$$I_{zT} \neq I_z$$

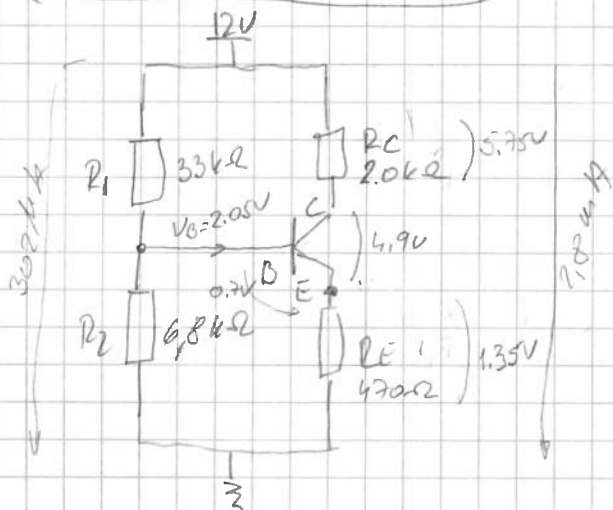
$$I_z = \frac{12V - 5.1V}{69\Omega} = 100mA$$

$$P_E = 5.1V \times 100mA = 0.51W$$

Clipping/clamping protection



TRANSISTOR BIASING



$$R_1 + R_2$$

$$12V / 39.8k\Omega = 302\mu A$$

$$V_{R2} = 6.8k\Omega \cdot 302\mu A = 2.05V$$

$$V_{RE} = 2.05 - 0.7V = 1.35V$$

($V_B - V_{BE}$)

$$I_{RE} = 1.35V / 470\Omega = 2.87mA$$

$$V_{RC} = 2.0k\Omega \cdot 2.87mA = 5.75V$$

$$V_{CE} = V_{CC} - V_{RC} - V_{RE} = 4.9V$$