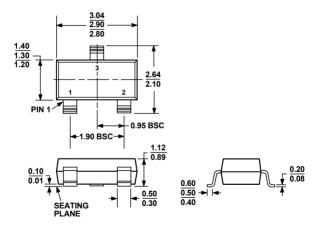
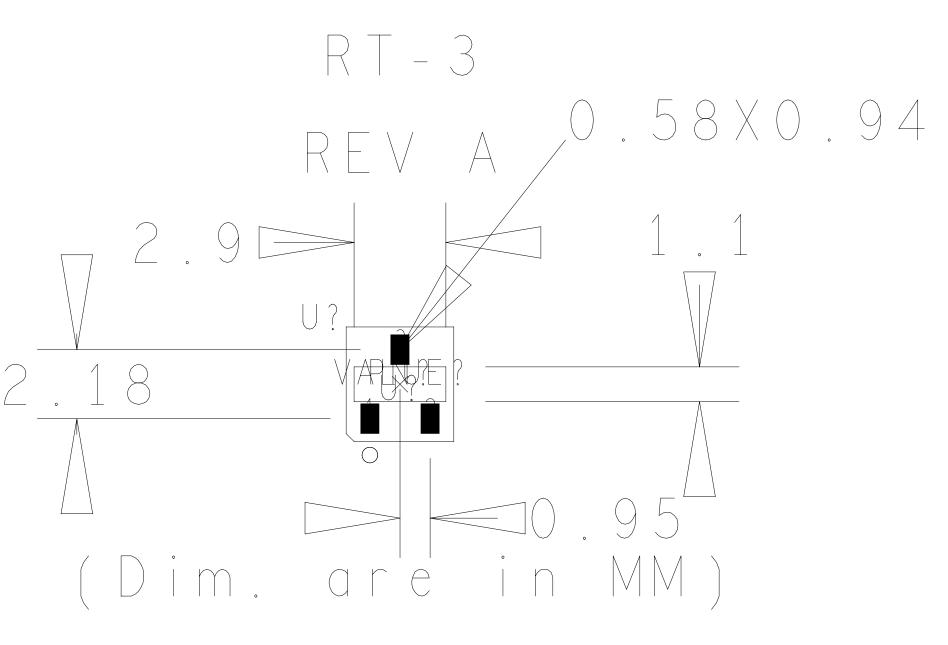


3-Lead Small Outline Transistor Package [SOT-23-3] [RT-3] Dimensions shown in millimeters



COMPLIANT TO JEDEC STANDARDS TO-236-AB

Analog Devices



LAST MODIFIED 11/07/06