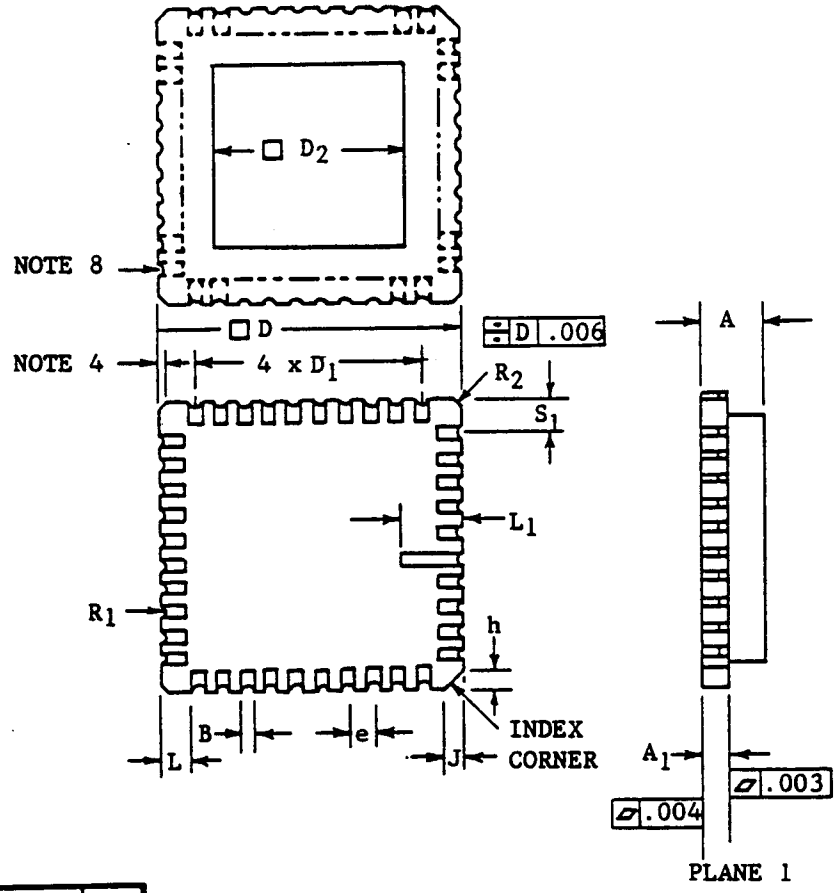

SYMBOL	COMMON DIM. MILLIMETER MIN	MAX	NOTES
B	.432	.584	6
e	.940	1.092	
h		.711 BSC	
J		.711 BSC	
L	.84	1.19	
L1	1.96	2.36	9
R1		.190 REF	
R2		.190 REF	3
Note	1, 2, 7, 10		
Ref.:	JC-11.3 ITEM 112		

NOTES:

1. Refer to applicable Symbol List.
2. Dimensioning and tolerancing per ANSI Y14.5M-1982.
3. Metallized corner castellations permissible.
4. Burr permissible on outside edges within dimensions shown.
5. N is the number of terminal positions, refer to terminal numbering convention.
6. Terminals to be centered about notches within +/- .05 mm (.002").
7. Terminal spacing tolerance is non-cumulative within tolerance limitations of indicated areas.
8. Terminal metallization of edges and opposite side optional.
9. Terminal extended on plane 1 for electrical/optical orientation handling purposes.
10. Controlling dimension: INCH

JEDEC SOLID STATE PRODUCT OUTLINES	TITLE STANDARD Single Layer Chip Carrier Family 1.02 (.040") Terminal Spacing (CERAMIC)	ISSUE	DATE	MS-014 AA-AJ
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VARIATIONS

ALL DIMENSIONS IN MILLIMETER

SYMBOLS	MIN	MAX	NOTES
	AA		
A	1.27	1.90	
A1	.39	1.14	
D	5.85	6.22	4
D1	2.93	3.17	
D2	5.34	5.71	
N	16		5
S1	1.27	1.65	
NOTES:	1, 2, 7, 10		
REF.:	JC-11.3 ITEM 112		
	AB		
A	1.27	1.90	
A1	.39	1.14	
D	8.26	8.63	4
D1	3.94	4.19	
D2	7.50	7.87	
N	20		5
S1	2.04	2.41	
NOTES:	1, 2, 7, 10		
REF.:	ITEM 112		
	AC		
A	1.27	1.90	
A1	.39	1.14	
D	8.77	9.14	4
D1	4.96	5.20	
D2	7.88	8.38	
N	24		5
S1	1.78	2.15	
NOTES:	1, 2, 7, 10		
REF.:	ITEM 112		
	AD		
A	1.27	1.90	
A1	.39	1.14	
D	10.55	10.92	4
D1	6.99	7.23	
D2	9.40	10.16	
N	32		5
S1	1.66	2.03	
NOTES:	1, 2, 7, 10		
REF.:	ITEM 112		
	AE		
A	1.27	1.90	
A1	.39	1.14	
D	12.07	12.49	4
D1	9.02	9.27	
D2	11.05	11.68	
N	40		5
S1	1.40	1.77	
NOTES:	1, 2, 7, 10		
REF.:	JC-11.3 ITEM 112		
	AF		
A	1.27	2.03	
A1	.39	1.14	
D	14.08	14.52	4
D1	11.05	11.30	
D2	12.70	13.58	
N	48		5
S1	1.40	1.77	
NOTES:	1, 2, 7, 10		
REF.:	ITEM 112		
	AG		
A	1.27	2.03	
A1	.39	1.52	
D	18.09	18.61	4
D1	15.09	15.39	
D2	14.74	15.87	
N	64		5
S1	1.40	1.77	
NOTES:	1, 2, 7, 10		
REF.:	ITEM 112		
	AH		
A	1.27	2.03	
A1	.64	1.52	
D	23.14	23.77	4
D1	20.12	20.52	
D2	16.39	17.90	
N	84		5
S1	1.40	1.77	
NOTES:	1, 2, 7, 10		
REF.:	ITEM 112		
	AJ		
A	1.27	2.28	
A1	.64	1.52	
D	26.19	26.79	4
D1	23.14	23.59	
D2	16.39	17.90	
N	96		5
S1	1.40	1.77	
NOTES:	1,2,7,10		
REF.:	JC-11.3 ITEM 112		

JEDEC SOLID STATE PRODUCT OUTLINES	TITLE STANDARD Single Layer Chip Carrier Family 1.02 (.040") Terminal Spacing	ISSUE	DATE	MS-014 AA-AJ
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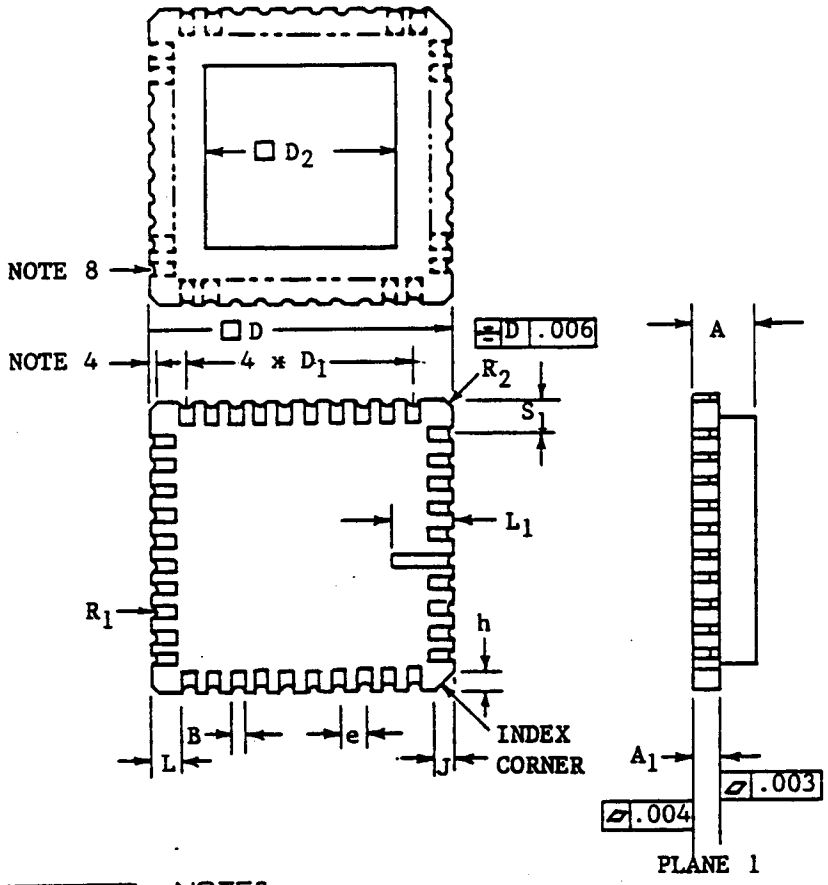

SYMBOL	COMMON DIM. INCHES MIN	MAX	NOTES
B	.017	.023	6
e	.037	.043	
h	.028 BSC		
J	.028 BSC		
L	.033	.047	
L1	.077	.093	9
R1	.0075 REF		
R2	.0075 REF		3
Note:	1, 2, 7, 10		
Ref.:	JC-11.3 ITEM 112		

NOTES:

1. Refer to applicable Symbol List.
2. Dimensioning and tolerancing per ANSI Y14.5M-1982.
3. Metallized corner castellations permissible.
4. Burr permissible on outside edges within dimensions shown.
5. N is the number of terminal positions, refer to terminal numbering convention.
6. Terminals to be centered about notches within +/- .05 mm (.002").
7. Terminal spacing tolerance is non-cumulative within tolerance limitations of indicated areas.
8. Terminal metallization of edges and opposite side optional.
9. Terminal extended on plane 1 for electrical/optical orientation handling purposes.
10. Controlling dimension: INCH

JEDEC SOLID STATE PRODUCT OUTLINES	TITLE STANDARD Single Layer Chip Carrier Family 1.02 (.040") Terminal Spacing (CERAMIC)	ISSUE A	DATE 8/1/85	MS-014 AA-AJ

VARIATIONS

ALL DIMENSIONS IN INCHES

SYMBOLS	MIN	MAX	NOTES	MIN	MAX	NOTES	MIN	MAX	NOTES	MIN	MAX	NOTES
	AA			**AB**			**AC**			**AD**		
A	.050	.075		.050	.075		.050	.075		.050	.075	
A1	.015	.045		.015	.045		.015	.045		.015	.045	
D	.230	.245	4	.325	.340	4	.345	.360	4	.415	.430	4
D1	.115	.125		.155	.165		.195	.205		.275	.295	
D2	.210	.225		.295	.310		.310	.330		.370	.400	
N	16		5	20		5	24		5	32		5
S1	.050	.065		.080	.095		.070	.085		.065	.080	
NOTES:	1, 2, 7, 10			1, 2, 7, 10			1, 2, 7, 10			1, 2, 7, 10		
REF.:	JC-11.3 ITEM 112			ITEM 112			ITEM 112			ITEM 112		
	AE			**AF**			**AG**			**AH**		
A	.050	.075		.050	.080		.050	.080		.050	.080	
A1	.015	.045		.015	.045		.015	.060		.025	.060	
D	.475	.492	4	.554	.572	4	.712	.733	4	.911	.936	4
D1	.355	.365		.435	.445		.594	.606		.792	.808	
D2	.435	.460		.500	.535		.580	.625		.645	.705	
N	40		5	48		5	64		5	84		5
S1	.055	.70		.055	.070		.055	.070		.055	.070	
NOTES:	1, 2, 7, 10			1, 2, 7, 10			1, 2, 7, 10			1, 2, 70, 10		
REF.:	JC-11.3 ITEM 112			ITEM 112			ITEM 112			ITEM 112		
	AJ											
A	.050	.090										
A1	.025	.060										
D	1.031	1.055	4									
D1	.911	.929										
D2	.645	.705										
N	96		5									
S1	.055	.070										
NOTES:	1, 2, 7, 10											
REF.:	JC-11.3 ITEM 112											

JEDEC SOLID STATE PRODUCT OUTLINES	TITLE STANDARD Single Layer Chip Carrier Family 1.02 (.040") Terminal Spacing	ISSUE	DATE	MS-014 AA-AJ
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