

JEDEC
SOLID STATE
PRODUCT OUTLINE

THIS **REGISTERED OUTLINE** HAS BEEN PREPARED BY THE JEDEC JC-11 COMMITTEE AND REFLECTS A PRODUCT WITH ANTICIPATED USAGE IN THE ELECTRONICS INDUSTRY; CHANGES ARE LIKELY TO OCCUR.

TITLE LOW PROFILE
PLASTIC DUAL FLAT
NO LEAD PACKAGE

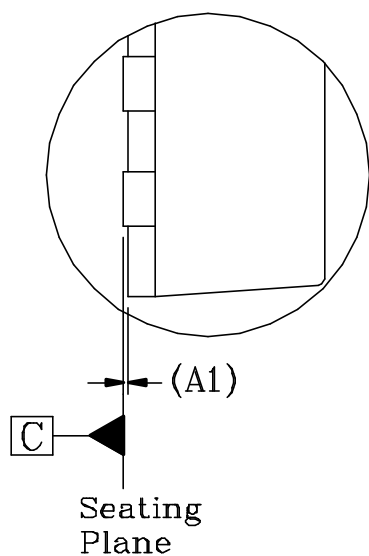
PACKAGE DESIGNATOR
L-PDFP-N

ISSUE
A

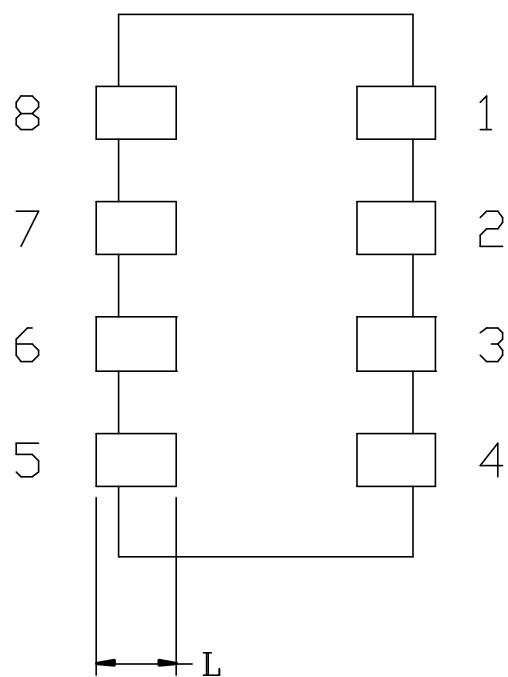
DATE
08/2001

MO-232

SHEET
1 OF 4



DETAIL A



VIEW A-A


VARIATIONS


AA

| Symbol | MIN. | NOM. | MAX. | NOTES |
|---|--------|----------|------|-------|
| A | — | — | 1.10 | |
| A1 | 0 | — | 0.05 | |
| b | 0.25 | 0.30 | 0.35 | |
| c | 0.10 | 0.15 | 0.20 | |
| D | — | 3.05 BSC | — | 3 |
| e | — | 0.65 BSC | — | |
| E | — | 1.90 BSC | — | 4 |
| E1 | 1.60 | 1.65 | 1.70 | 3 |
| L | 0.28 | 0.35 | 0.42 | |
| θ | 0° | — | 5° | |
| | | | | |
| | | | | |
| | | | | |
| | | | | |
| | | | | |
| | | | | |
| TOLERANCES OF FORM AND POSITION | | | | |
| aaa | 0.20 | | | |
| bbb | 0.10 | | | |
| | | | | |
| | | | | |
| Notes | 1,2 | | | |
| Ref | 10-412 | | | |
| Issue | A | | | |
| | | | | |


Notes:

1. Dimensioning and tolerancing per ASME Y14.5M-1994
2. All dimensions in millimeter

 Dimensions D and E1 do not include mold flash protrusions or gate burrs. Mold flash protrusions or gate burrs shall not exceed 0.10 mm per side.

 Dimension E does not include interterminal flash or protrusion. Interterminal flash or protrusion shall not exceed 0.15 mm per side

 A visual index feature must be located within the hatched area

 Exact shape and size of this feature is optional