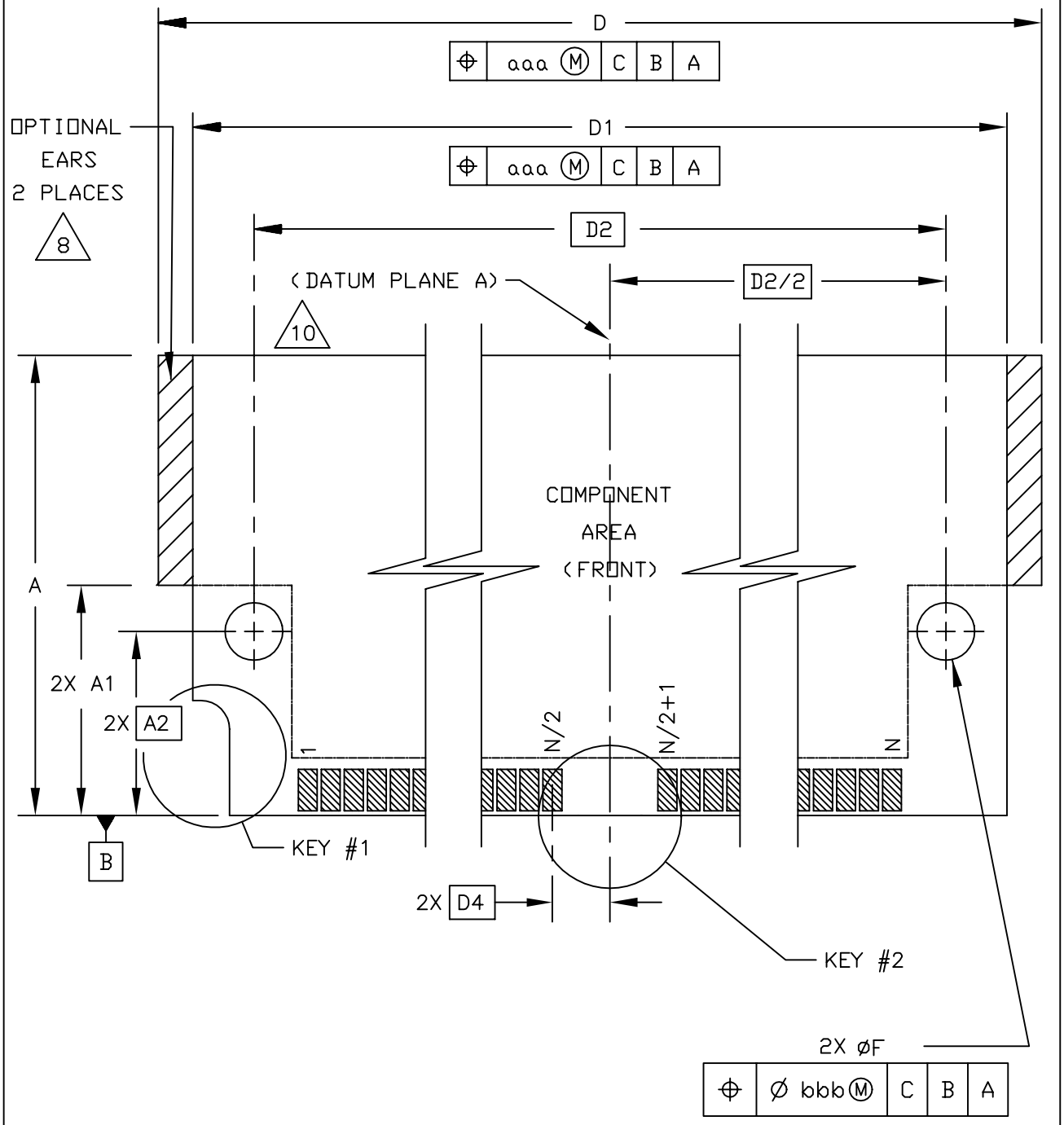
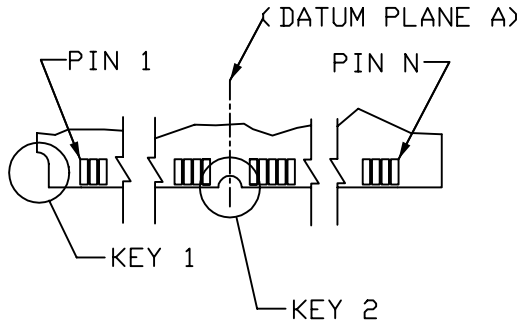
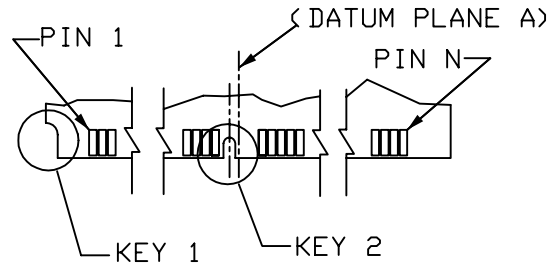
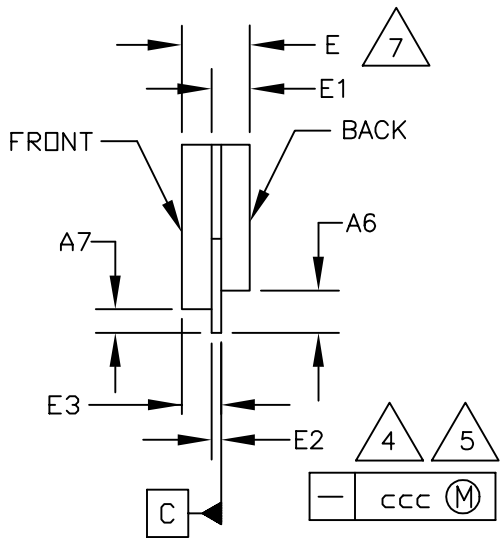


JEDEC SOLID STATE PRODUCT OUTLINE	THIS REGISTERED OUTLINE HAS BEEN PREPARED BY THE JEDEC JC-11 COMMITTEE AND REFLECTS A PRODUCT WITH ANTICIPATED USAGE IN THE ELECTRONICS INDUSTRY; CHANGES ARE LIKELY TO OCCUR.					
TITLE PLUGGABLE SINGLE IN-LINE MEMORY MODULE (SIMM) WITH TABS ON .050 CENTERS		PACKAGE DESIGNATOR MA/SIMM	ISSUE B	DATE 5/98	MO-116	SHEET 1 OF 6

CONFIGURATION

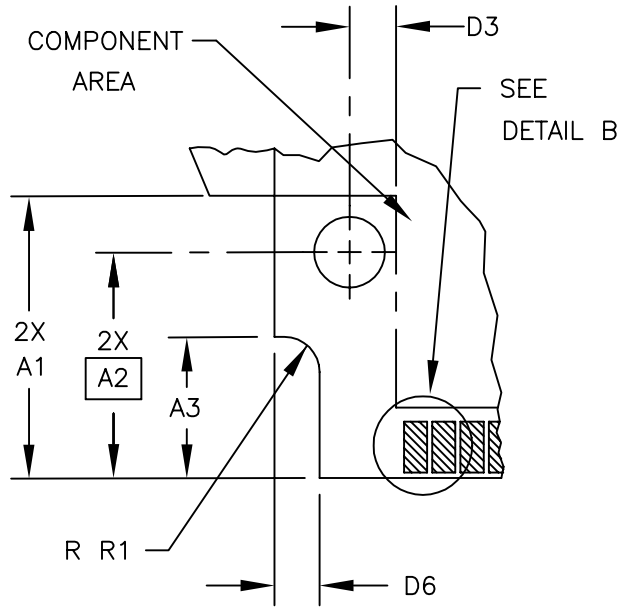
NOTE: REFER TO SHEET 3 FOR DETAILS.

VARIATION	KEY #1	KEY #2 VOLTAGE KEY	NO. PINS	MODULE KEY POSITION DETAIL AS VIEWED FROM FRONT SIDE
AA AB	DETAIL A	DETAIL C (5.0 VOLT)	72	
BA BB	DETAIL A	DETAIL C (5.0 VOLT)	72	
CA CB	DETAIL A	DETAIL D (3.3 VOLT)	72	 CENTER KEY OFFSET .050 TO LEFT TOWARD PIN #1
DA DB	DETAIL A	DETAIL D (3.3 VOLT)	72	

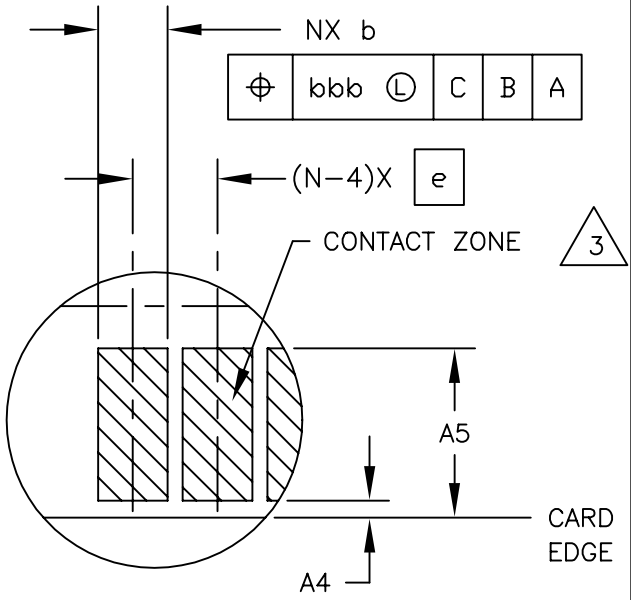


END VIEW

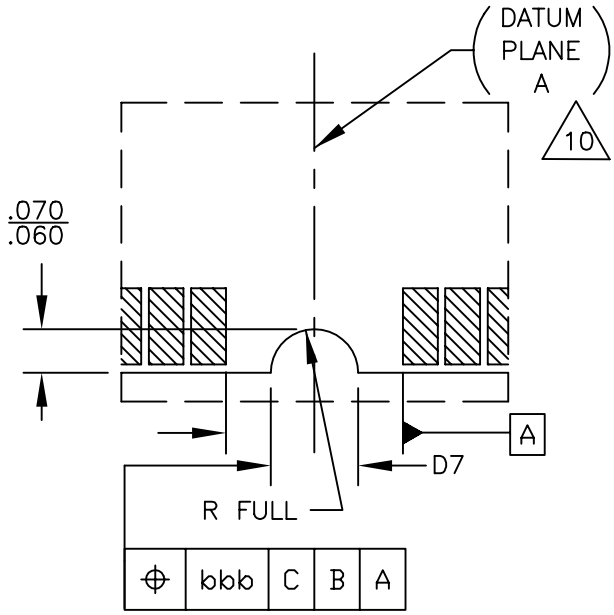
TITLE	PACKAGE DESIGNATOR	ISSUE	DATE		SHEET
PLUGGABLE SINGLE IN-LINE MEMORY MODULE (SIMM) WITH TABS ON .050 CENTERS	MA/SIMM	B	05/98	MO-116	2 OF 6



DETAIL A

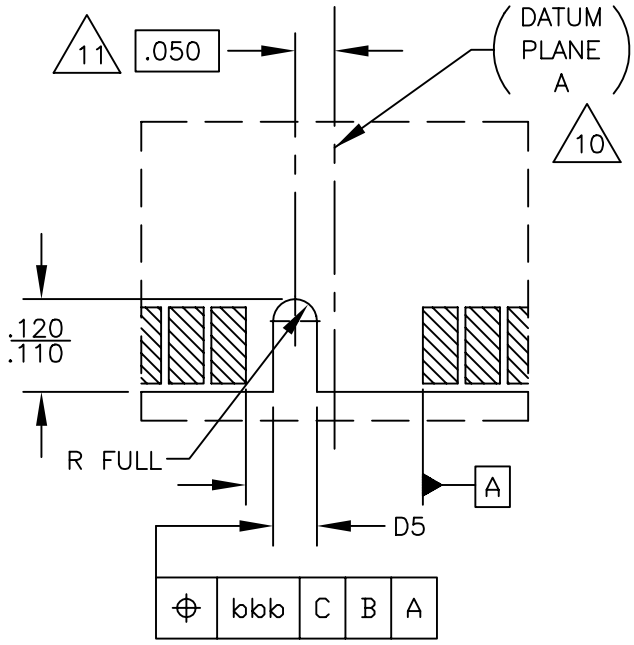


DETAIL B 3



DETAIL C

CENTER KEYWAY (5.0 V)



DETAIL D

OFFSET KEYWAY (3.3 V)

TITLE	PACKAGE DESIGNATOR	ISSUE	DATE		SHEET
PLUGGABLE SINGLE IN-LINE MEMORY MODULE (SIMM) WITH TABS ON .050 CENTERS	MA/SIMM	B	5/98	MO-116	3 OF 6

COMMON DIMENSIONS									
SYMBOL	MIN.	NOM.	MAX.	NOTE	SYMBOL	MIN.	NOM.	MAX.	NOTE
A1	.500	———	———		e	.050 BASIC			
A2	.400 BASIC				E	———	———	.360	7
A3	.245	.250	.255		E1	———	———	.208	7
A4	———	———	.010		E2	.047	.050	.054	4
A5	.100	———	———		E3	———	———	.208	
b	.040	.041	.042		R1	.057	.062	.067	
D3	.085	———	———		F	.123	.125	.127	
D4	.125 BASIC								
D6	.075	.080	.085						
	TOLERANCES OF POSITION AND FORM								
aaa	.006								
bbb	.004								
ccc	.012								5
NOTE	1, 2, 6								
REF.	11.14-023								
ISSUE	B								

TITLE	PACKAGE DESIGNATOR	ISSUE	DATE		SHEET
PLUGGABLE SINGLE IN-LINE MEMORY MODULE (SIMM) WITH TABS ON .050 CENTERS	MA/SIMM	B	5/98	MO-116	4 OF 6

VARIATIONS

	AA			NOTE	AB			NOTE	BA			NOTE	BB			NOTE
	MIN.	NOM.	MAX.		MIN.	NOM.	MAX.		MIN.	NOM.	MAX.		MIN.	NOM.	MAX.	
A	.995	——	1.005		.995	——	1.005		1.715	——	1.725		1.715	——	1.725	
A6	.225	——	——	7	.125	——	——	7	.225	——	——	7	.125	——	——	7
A7	.125	——	——		.225	——	——		.125	——	——		.225	——	——	
D	4.545	4.550	4.555		4.545	4.550	4.555		4.545	4.550	4.555		4.545	4.550	4.555	
D1	4.245	4.250	4.255		4.245	4.250	4.255		4.245	4.250	4.255		4.245	4.250	4.255	
D2	3.984 BASIC				3.984 BASIC				3.984 BASIC				3.984 BASIC			
D7	.120	.124	.128		.120	.124	.128		.120	.124	.128		.120	.124	.128	
N	——	72	——	9	——	72	——	9	——	72	——	9	——	72	——	9
NOTE	1, 2, 6				1, 2, 6				1, 2, 6				1, 2, 6			
REF.	11.14-023				11.14-023				11.14-023				11.14-023			
ISSUE	B				B				B				B			

VARIATIONS

	CA			NOTE	CB			NOTE	DA			NOTE	DB			NOTE
	MIN.	NOM.	MAX.		MIN.	NOM.	MAX.		MIN.	NOM.	MAX.		MIN.	NOM.	MAX.	
A	.995	——	1.005		.995	——	1.005		1.715	——	1.725		1.715	——	1.725	
A6	.225	——	——	7	.125	——	——	7	.225	——	——	7	.125	——	——	7
A7	.125	——	——		.225	——	——		.125	——	——		.225	——	——	
D	4.545	4.550	4.555		4.545	4.550	4.555		4.545	4.550	4.555		4.545	4.550	4.555	
D1	4.245	4.250	4.255		4.245	4.250	4.255		4.245	4.250	4.255		4.245	4.250	4.255	
D2	3.984 BASIC				3.984 BASIC				3.984 BASIC				3.984 BASIC			
D5	.060	.062	.064		.060	.062	.064		.060	.062	.064		.060	.062	.064	
N	——	72	——	9	——	72	——	9	——	72	——	9	——	72	——	9
NOTE	1, 2, 6				1, 2, 6				1, 2, 6				1, 2, 6			
REF.	11.14-023				11.14-023				11.14-023				11.14-023			
ISSUE	B				B				B				B			

TITLE	PACKAGE DESIGNATOR	ISSUE	DATE		SHEET
PLUGGABLE SINGLE IN-LINE MEMORY MODULE (SIMM) WITH TABS ON .050 CENTERS	MA/SIMM	B	5/98	MO-116	5 OF 6

NOTES

1. ALL DIMENSIONS AND TOLERANCES CONFORM TO ASME Y14.5M-1994.
2. THE CONTROLLING UNIT FOR ALL DIMENSIONS IS INCHES.
3. OPPOSING CONTACT TABS ON BOTH SIDES OF CARD MUST BE ELECTRICALLY CONNECTED, EXCEPT NON-FUNCTIONAL TABS. VIAS MAY NOT BE IN CONTACT ZONE.
4. CARD THICKNESS INCLUDES TAB PLATING AND/OR METALLIZATION.
5. STRAIGHTNESS REQUIREMENT APPLIES TO AREA DEFINED BY A6 AND A7 OVER LENGTH D1.
6. TOLERANCE ON ALL DIMENSIONS +/- .005 UNLESS OTHERWISE SPECIFIED.
7. DIMENSION APPLICABLE WHEN COMPONENTS MOUNTED ON BOTH SIDES OF CARD.
8. EARS ARE OPTIONAL, BUT WHEN USED MUST BE AS DIMENSIONED.
9. N IS THE NUMBER OF CIRCUIT TABS PER SIDE.
10. DATUM PLANE A IS THE CENTERLINE OF DATUM A .
11. 3.3 VOLT KEY IS OFFSET FROM DATUM A BY .050 TOWARD PIN 1.

APPLICATION NOTES:

1. THE FOLLOWING PLATING REQUIREMENTS ARE SUGGESTED:
   a. CONTACT PAD PLATING 30 MICROINCHES MINIMUM GOLD OVER 75 MICROINCHES MINIMUM NICKEL.
   b. CONTACT PAD PLATING 100 MICROINCHES MINIMUM TIN-LEAD OVER 50 MICROINCHES MINIMUM NICKEL.

2. THE FOLLOWING MUST BE CONSIDERED FOR THE NEXT LEVEL OF MODULE ASSEMBLY:
   a. LEFT POLARIZATION (VARIANTS AA, BA, CA, DA) IS THE PREFERRED IMPLEMENTATION. POLARIZATION IS "LEFT" WHEN PIN 1 IS ON THE LEFT AND THE MODULE IS INSERTED INTO THE SOCKET AND ROTATED AWAY TO BE LATCHED.
   b. RIGHT POLARIZATION (VARIANTS AB, BB, CB, DB) IS THE NON-PREFERRED IMPLEMENTATION BUT IS ALLOWED BY THIS STANDARD. POLARIZATION IS "RIGHT" WHEN PIN 1 IS ON THE RIGHT AND THE MODULE IS INSERTED INTO THE SOCKET AND ROTATED AWAY TO BE LATCHED.
   c. GOLD-PLATED SOCKETS SHOULD BE USED WITH GOLD-PLATED PLATED MODULES. TIN-LEAD PLATED SOCKETS SHOULD BE USED WITH TIN-LEAD PLATED MODULES.