

JEDEC
SOLID STATE PRODUCT
OUTLINE

THIS REGISTERED OUTLINE HAS BEEN PREPARED BY THE JEDEC
JC-11 COMMITTEE AND REFLECTS A PRODUCT WITH ANTICIPATED USAGE
IN THE ELECTRONICS INDUSTRY: CHANGES ARE LIKELY TO OCCUR.

TITLE
DUAL INLINE MEMORY
MODULE (DIMM) FAMILY
1.27 LEAD CENTERS, 160 LEADS

JESD-30 DESIGNATOR

ISSUE

DATE

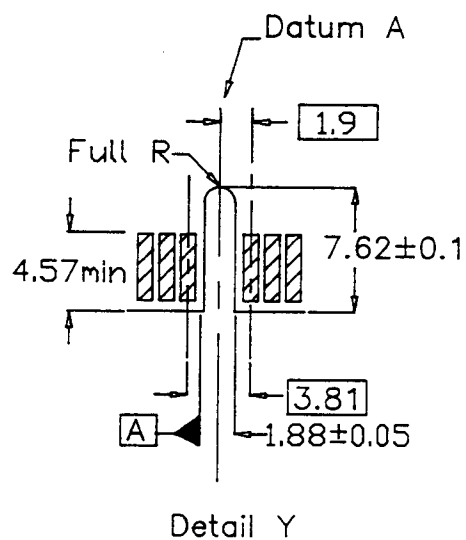
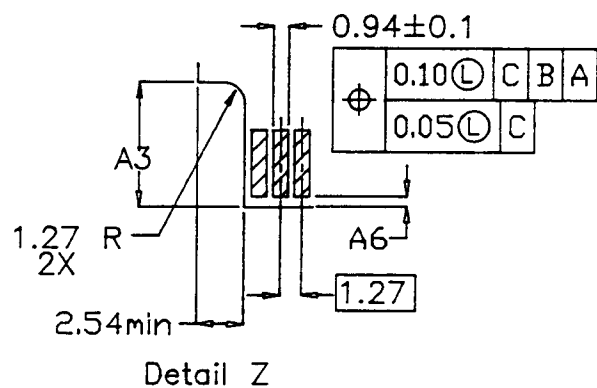
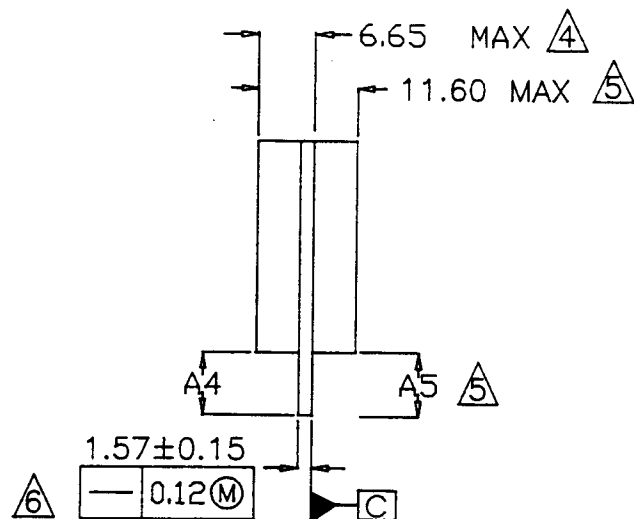
SHEET

A

12/96

MO-191

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VARIATIONS									
	AA			AB					
SYMBOL	MIN	NOMINAL	MAX	MIN	NOMINAL	MAX	MIN	NOMINAL	MAX
A	32.26	32.77	33.27	28.45	28.70	28.96			
A3	7.24		7.75	7.24		7.75			
A4	7.8			7.8					
A5	7.8			7.8					
A6	0.05		0.35	0.05		0.35			
D	109.98	110.24	110.49	109.98	110.24	110.49			
D1	104.64 REF			104.64 REF					
D2	102.86BSC			102.86BSC					
D3	54.61	54.86	55.12	54.61	54.86	55.12			
D4	49.53	49.78	50.04	49.53	49.78	50.04			
e1/e3	52.07BSC			52.07BSC					
e2/e4	46.99BSC			46.99BSC					
N	160			160					
ISSUE									
Ref	14-027			14-027					

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NOTES:

1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5M-1994.
2. TOLERANCES ON ALL DIMENSIONS ± 0.15 UNLESS OTHERWISE SPECIFIED.
3. ALL DIMENSIONS ARE IN MILLIMETERS.
4. DIMENSION APPLICABLE WHEN COMPONENTS ARE MOUNTED ON ONE SIDE.
5. DIMENSIONS APPLICABLE WHEN COMPONENTS MOUNTED ON BOTH SIDES.
6. CARD THICKNESS APPLIES ACROSS TABS AND INCLUDES PLATING AND/OR METALIZATION.
7. N IS THE NUMBER OF CIRCUIT TABS (CONTACTS).
8. APPLICATION NOTE:
GOLD CONTACT PAD : GOLD PLATING 0.76 μ m MIN.
OVER Ni PLATING 2 μ m MIN.

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