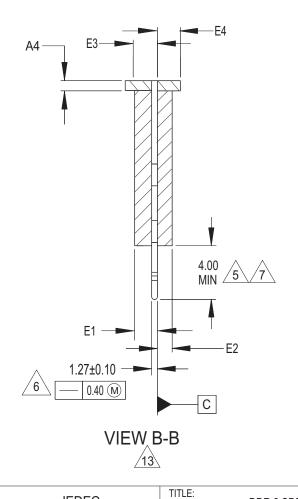
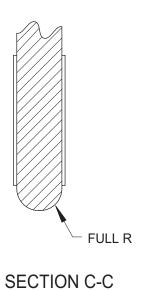


4

DETAIL A: CENTER KEY ZONE



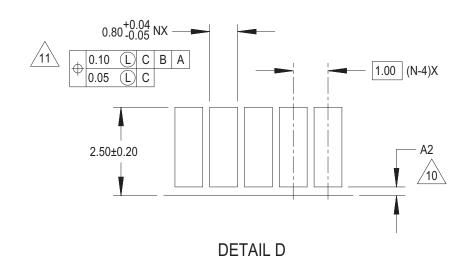


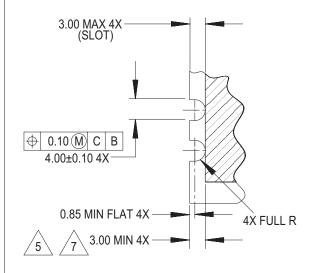
JEDEC
SOLID STATE
PRODUCT OUTLINE

ILL.	DDR 2 SDRAM DIMM
(DUAI	L INLINE MEMORY MODULE) FAMILY,
FLEX-I	BASED, 1.00 mm CONTACT CENTERS

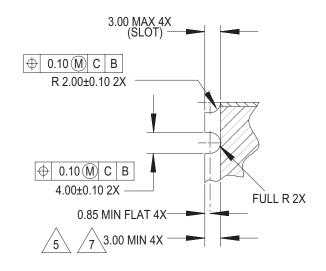
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DETAIL E (VARIATION) AA, AB, BA, BB



DETAIL F (VARIATION) CA, CB

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COMMON DIMENSION TABLE

SYMBOL	MIN	NOM	MAX	NOTES
A1		10.00 BASIC		
A2	0.35	0.55	0.75	
A3		17.80 BASIC		
D	133.20	133.35	133.50	
D1		4.00 BASIC		4
D2		2.50 BASIC		4
e1		63.00 BASIC		
e2		55.00 BASIC		
N		240		8
NOTES	1, 2, 3, 5			
REF	11.14-100			
ISSUE	А			

VARIATIONS

VAR	AA			AB					NOTES	
SYMBOL	MIN	NOM	MAX	MIN	NOM	MAX	MIN	NOM	MAX	NOTES
Α	29.85	30.00	30.50	29.85	30.00	30.50				
A4	0.40			0.40						
E1			3.80			4.45				
E2			2.40			3.10				
E3			4.05			4.45				
E4			2.70			3.10				
NOTES	1, 2, 3									
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VARIATIONS

VAR		ВА		BB					NOTES	
SYMBOL	MIN	NOM	MAX	MIN	NOM	MAX	MIN	NOM	MAX	NOTES
Α	37.85	38.00	38.15	37.85	38.00	38.15				
A4	7.75			7.75						
E1			3.80			4.45				
E2			2.40			3.10				
E3			4.05			4.45				
E4			2.70			3.10				
NOTES	1, 2, 3									
REF	11.14-100									
ISSUE	А									

VARIATIONS

VAR		CA		СВ					NOTES	
SYMBOL	MIN	NOM	MAX	MIN	NOM	MAX	MIN	NOM	MAX	NOTES
A	18.15	18.30	18.45	18.15	18.30	18.45				
A4	0.40			0.40						
E1			4.05			5.95				
E2			2.70			4.60				
E3			4.05			5.95				
E4			2.70			4.60				
NOTES	1, 2, 3									
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ISSUE	А									

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NOTES:

- 1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5M-1994.
- 2. TOLERANCES ON ALL DIMENSIONS ±0.15 UNLESS OTHERWISE NOTED.
- 3. ALL DIMENSIONS ARE IN MILLIMETERS (mm).



 $\overline{/4}$ THE JC-45.4 COMMITTEE CONTROLS THE SIGNIFICANCE OF OFFSET KEY POSITION. IT IS SHOWN FOR REFERENCE ONLY, AND IS SUBJECT TO CHANGE.



25 DIMENSIONS APPLICABLE WHEN COMPONENTS MOUNTED ON ONE OR BOTH SIDES.

/6 CARD THICKNESS APPLIES ACROSS TABS AND INCLUDES PLATING AND/OR METALIZATION. STRAIGHTNESS CALLOUT APPLIES TO ZONE DEFINED BY THE 4.00 CONTACT AREA DIMENSION FOR THE ENTIRE LENGTH OF 133.35.



△ BORDER OF COMPONENT AREA. COMPONENT AREA INCLUDES ANY FLEX CIRCUIT MATERIAL PROJECTING BEYOND 1.37 MAX THICKNESS.



 $\stackrel{/}{8}$ N IS THE TOTAL NUMBER OF CIRCUIT CONTACTS (PINS, LEADS, TABS, PADS).



OFFSET KEY FEATURES SHALL BE DEFINED BY NON-FLEX CIRCUIT MATERIAL. NO FLEX CIRCUIT MATERIAL SHALL BE ALLOWED TO PROTRUDE INTO OFFSET KEY.



/10\ LEADING EDGE OF CONTACT PADS SHALL BE FREE OF EXTERNAL TRACES.

APPLICATION NOTES:



11\ RECOMMENDED PLATING FOR CONTACT PADS ARE:

- 1) PREFERABLE PLATING: ELECTROLYTIC GOLD PLATING 0.76 MICROMETERS MINIMUM OVER ELECTROLYTIC NICKEL 2.00 MICROMETERS MINIMUM.
- 2) ALTERNATIVE PLATING: GOLD PLATING 0.05-0.75 MICROMETERS OVER NICKEL 2.00 MICROMETERS MINIMUM MUST USE AN ELECTRONIC CONTACT GRADE CORROSIVE BARRIER LUBRICANT.

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12 STAKTEK HAS STATED THAT CERTAIN U.S. PATENT APPLICATIONS MAY APPLY TO CONFIGURATIONS OF THIS OUTLINE. THESE APPLICATIONS INCLUDE:

10/934,027 11/005,992

11/007,551

11/068,688

11/077,952

11/123,721

11/157,565

11/193,954

11/321,418

11/331,969

STAKTEK INTENDS TO COMPLY WITH THE JEDEC PATENT POLICY.

13 VIEW B-B MODULE THICKNESS INCLUDES EXTERNAL HEAT SPREADER FOR VARIATIONS AB, BB, AND CB.

JEDEC
SOLID STATE
PRODUCT OUTLINE

Change Record

If the change involves any words added or deleted (excluding deletion of accidentally repeated words), the change is to be included below. Punctuation changes may or may not be included.

Change Record History					
Issue:	Date:	Item Number:			
Description of Changes					

JEDEC
SOLID STATE
PRODUCT OUTLINE

TITLE:
DDR 2 SDRAM DIMM
(DUAL INLINE MEMORY MODULE) FAMILY,
ELEX-BASED 1.00 mm CONTACT CENTERS

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