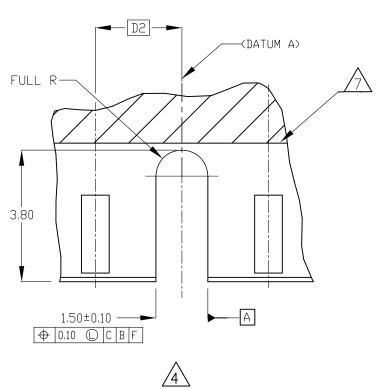


FDHS VOLUMETRIC KEEPIN VARIATIONS HSAB, HSGB, HSHB

JEDEC	DDR3 SDRAM DIMM	ISSUE:	DATE:	ITEM	PAGE:
SOLID STATE PRODUCT OUTLINE	(DUAL INLINE MEMORY MODULE) FAMILY with 1.00mm CONTACT CENTERS	E	AUG 08	MD-269	4 OF 19



DETAIL A: KEY ZONE

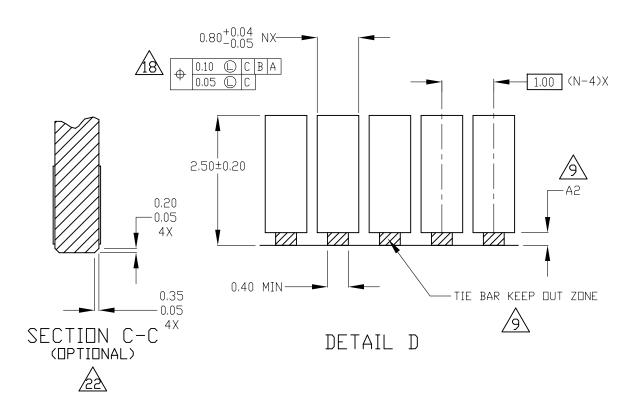
DDR3 SDRAM DIMM

(DUAL INLINE MEMORY MODULE)

FAMILY with 1.00mm CONTACT CENTERS

SOLID STATE

PRODUCT OUTLINE



ISSUE:

Ε

DATE:

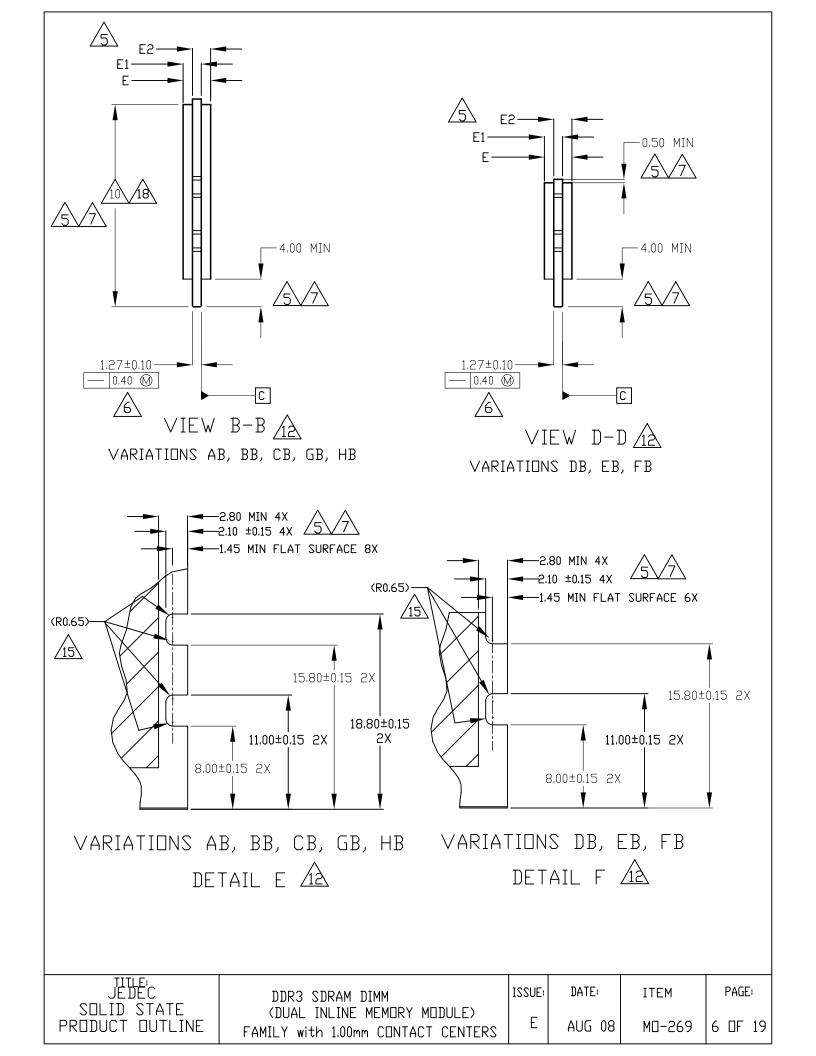
AUG 08

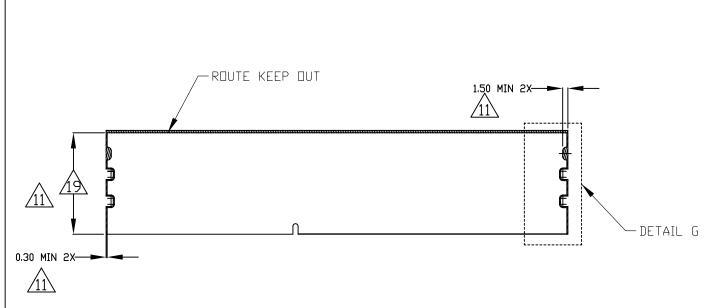
ITEM

MD-269

PAGE:

5 DF 19

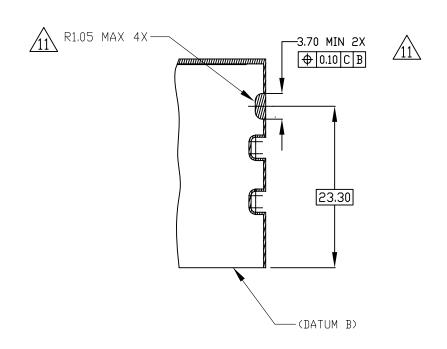




FRONT SIDE

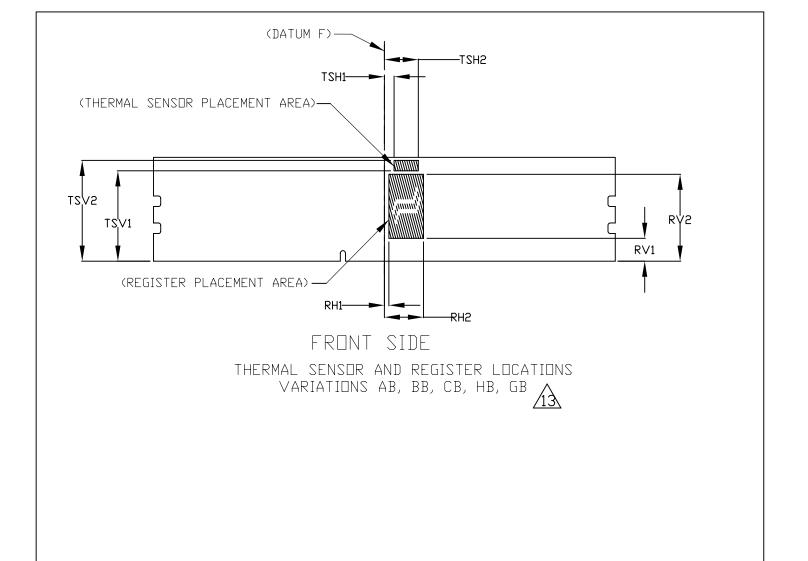
ROUTE KEEPOUT - RDIMM (BOTH SIDES)

(REFERENCE TO DIMM NOMINAL DIMENSIONS)



DETAIL G ROUTE KEEPOUT (REFERENCE TO DIMM NOMINAL DIMENSIONS)

JEDEC	DDR3 SDRAM DIMM	ISSUE:	DATE:	ITEM	PAGE:
SOLID STATE PRODUCT OUTLINE	(DUAL INLINE MEMORY MODULE) FAMILY with 1.00mm CONTACT CENTERS	E	AUG 08	M□-269	7 OF 19



TITLE: JEDEC	DDR3 SDRAM DIMM	ISSUE:	DATE:	ITEM	PAGE:
SOLID STATE PRODUCT OUTLINE	(DUAL INLINE MEMORY MODULE) FAMILY with 1.00mm CONTACT CENTERS	E	AUG 08	MD-269	8 OF 19

# MECHANICAL KEYING (FRONT VIEWS)

VARIATION	VDD	KEY POSITION 4
×В	1.5∨	(DATUM A)

TITLE: JEDEC	DDR3 SDRAM DIMM	ISSUE:	DATE:	ITEM	PAGE:
SOLID STATE PRODUCT OUTLINE	(DUAL INLINE MEMORY MODULE) FAMILY with 1.00mm CONTACT CENTERS	E	AUG 08	MD-269	9 OF 19

## COMMON DIMENSION TABLE

SYMBOL	MIN	NDM	MAX	NOTES
A2	0.05	0.20	0.35	
D	133.20	133.35	133.50	
e1		47.00 BASIC		
e2		71.00 BASIC		
N		240		8
NOTES	1, 2, 3			
REF	11.14-086			
ISSUE	А			

## THERMAL SENSOR AND REGISTER PLACEMENT DIMENSION TABLE

CVADEI		RDIMM			UDIMM					NOTES
SYMBOL	MIN	NDM	MAX	MIN	NDM	MAX	MIN	N□M	MAX	INDIES
TSH1	2.80	-	_	2,80	_	=				
TSH2	_	-	9,80	_	_	9,80				
TSV1	25.60	_	_	9.00	_	_				
TSV2	-	_	29,10	-	_	17.5				
RH1	1.30	_	-	-	_	-				
RH2	1	_	11.30	1	_	ı				
RV1	6,60	_	_	ı	_	ı				
RV2	1	-	25.60	1	_	ı				
NOTES	1, 2	, 3								
REF	11.14-120									
ISSUE	D	D								

JEDEC	DDR3 SDRAM DIMM	ISSUE:	DATE:	ITEM	PAGE:
SOLID STATE PRODUCT OUTLINE	(DUAL INLINE MEMORY MODULE) FAMILY with 1.00mm CONTACT CENTERS	E	AUG 08	M□-269	10 OF 19

# LP VARIATIONS

CVMDEI		AB			ВВ			СВ			
SYMBOL	MIN	NDM	MAX	MIN	NDM	MAX	MIN	NDM	MAX	NOTES	
А	29,85	30,00	30.50	29.85	30.00	30.50	29.85	30.00	30.50		
D1	12.00 BASIC			12	2.00 BAS	[C	12	4			
D2	2.50 BASIC			2.50 BASIC			2.50 BASIC			4	
E	ı	ı	4.00	_	-	6.75	1	-	7.55		
E1	-	ı	2.70	_	_	4.05	-	-	4.45		
E2	-	-	2.70	_	_	4.05	_	_	4.45		
NOTES	1, 2, 3, 10, 12										
REF	14-086										
ISSUE	Α										

CVMDEI		GB			НВ					
SYMBOL	MIN	NDM	MAX	MIN	NDM	MAX	MIN	NDM	MAX	NOTES
А	29,85	30.00	30,50	29,85	30.00	30,50				
D1	12.00 BASIC			12	2.00 BAS	[C				4
D2	2.50 BASIC			2.50 BASIC					4	
E	_	_	4.00	_	ı	6.75	1	_	_	
E1	_	_	2.70	_	ı	4.05	-	_	_	
E2	_	_	2.70	_	ı	4.05	_	_	_	
NOTES	1, 2,	3, 12,	18							
REF	14-127									
ISSUE	E									

## VLP VARIATIONS

CAMDU		DB			EB			FB			
SYMBOL	MIN	NDM	MAX	MIN	NDM	MAX	MIN	NDM	MAX	NOTES	
А	18.60	18.75	18.90	18.60	18.75	18.90	18.60	18.75	18.90		
D1	12.00 BASIC			12	2.00 BAS	[C	12	4			
D2	2	.50 BASI	С	2.50 BASIC		2.50 BASIC			4		
E	ı	ı	4.00	-	ı	6.75	ı	ı	7.55		
E1	ı	ı	2.70	-	ı	4.05	1	ı	4.45		
E2	_	_	2.70	_	_	4.05	_	_	4.45		
NOTES	1, 2,	3, 12	•				•	•	•		
REF	14-086										
ISSUE	Α										

JEDEC	DDR3 SDRAM DIMM	ISSUE:	DATE:	ITEM	PAGE:
SOLID STATE PRODUCT OUTLINE	(DUAL INLINE MEMORY MODULE) FAMILY with 1.00mm CONTACT CENTERS	E	AUG 08	MD-269	11 OF 19

# FDHS VARIATIONS

CVMDIII		HSAB								NOTES
SYMBOL	MIN	NDM	MAX	MIN	NDM	MAX	MIN	NDM	MAX	
Α	29.85	30.00	30.50							
D1	12	2.00 BAS	IC							4
D2	2	.50 BASI	C							4
EHS	_	_	7,55	-	-	_	_	_	_	
EHS1	_	_	4,45	-	-	_	_	_	_	
EHS2	_	ı	4.45	ı	ı	ı	_	_	_	
ECL	_	_	8.5	_	_	_	_	_	_	
ECL1	_	1	4.95	ı	ı	ı	_	_	_	
ECL2	_	-	4.95	1	ı	-	_	_	_	
NOTES	1, 2, 3									
REF	11.14-120	)		·					·	
ISSUE	D									

SYMBOL		HSGB			HSHB		NOTES
SIMPUL	MIN	NDM	MAX	MIN	NDM	MAX	INDIES
А	29.85	30.00	30.50	29,85	30,00	30.50	
D1	12	2.00 BAS	[C	12	2.00 BAS	IC	4
D2	2	.50 BASI	С	2	.50 BASI	iC	4
EHS	_	ı	7.55	_	_	9,55	
EHS1	_	-	4.45	_	_	5.45	
EHS2	_	ı	4.45	-	-	5,45	
ECL	_	ı	8,5	_	_	10.0	
ECL1	_	ı	4.95	_	_	5.95	
ECL2	_	1	4.95	_	_	5.95	
NOTES	1, 2, 3		·			·	
REF	14-127						
ISSUE	E						

TITLE: JEDEC	DDR3 SDRAM DIMM	ISSUE:	DATE:	ITEM	PAGE:
SOLID STATE PRODUCT OUTLINE	(DUAL INLINE MEMORY MODULE) FAMILY with 1.00mm CONTACT CENTERS	Е	AUG 08	MD-269	12 OF 19

## NNTFS:

- 1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5M-1994.
- 2. TOLERANCES ON ALL DIMENSIONS ±0.15 UNLESS OTHERWISE NOTED.
- 3. ALL DIMENSIONS ARE IN MILLIMETERS (mm).



THE JC-45 COMMITTEE CONTROLS THE SIGNIFICANCE OF THE OFFSET KEY POSITION. IT IS SHOWN FOR REFERENCE ONLY AND IS SUBJECT TO CHANGE.



DIMENSION APPLICABLE WHEN COMPONENTS MOUNTED ON ONE OR BOTH SIDES.



CARD THICKNESS APPLIES ACROSS TABS AND INCLUDES PLATING AND/OR METALLIZATION. STRAIGHTNESS CALLOUT APPLIES TO ZONE DEFINED BY THE 4.00MM CONTACT AREA DIMENSION FOR THE ENTIRE LENGTH OF 133.35MM.



BURDER OF COMPONENT AREA. PCB THICKNESS NOT TO EXCEED OUTSIDE THE COMPONENT AREA.

8. N IS THE TOTAL NUMBER OF CIRCUIT CONTACTS (PINS, LEADS, TABS, PADS).



LEADING EDGE OF CONTACT PADS SPECIFIED BY THE KEEPOUT ZONE SHALL BE FREE OF BURRS AND EXTERNAL TIE BARS FOR OPTIMUM PERFORMANCE. THE TIE BAR IS TO BE ON AN INTERNAL LAYER SO THAT THE REMNANT CANNOT CAUSE CONTACT DAMAGE.



THE COMPONENT KEEPIN ON VIEW B-B DIFFERS BETWEEN UDIMM AND RDIMM. UDIMM: 29.5MM MAX COMPONENT KEEPIN. RDIMM: 29.2MM MAX COMPONENT KEEPIN.



ROUTE KEEP OUT REFERENCE TO THE MEMORY MODULE AT NOMINAL DIMENSION CONDITIONS AT ALL LAYERS.



VIEWS DEPICT DIMM WITHOUT THE FULL DIMM HEAT SPREADER (FDHS) ATTACHED.



VIEW DEPICTS PLACEMENT LOCATIONS FOR THE THERMAL SENSOR AND REGISTER FOR THE RDIMM. THE THERMAL SENSOR PLACEMENT AREA IS FOR ALL RDIMM RAW CARDS. THE REGISTER PLACEMENT AREA IS REQUIRED FOR ALL RDIMM RAW CARDS WHERE THE REGISTER AND THE DRAM HEIGHTS ARE NOT EQUAL. PLEASE REFER TO DDR3 RDIMM SPECIFICATION FOR SPECIFIC RAW CARD REQUIREMENTS.



DIMENSIONS AND HATCHED AREA DEPICT LOCATION FOR THE HEAT SPREADER CLIP LEAD IN FEATURE WHICH MAY PROTRUDE HIGHER THAN THE EHS DIMENSION.



THE (R0.65) DIMENSION IS FOR REFERENCE ONLY. THE 1.45MM MIN FLAT AND THE 2.10  $\pm$  0.15MM 4X CONTROL THE FEATURE.

- 16. THE THERMAL SENSOR MUST NOT CONTACT THE HEAT SPREADER OR THE THERMAL INTERFACE MATERIAL. A 0.15 MM GAP IS REQUIRED.
- 17. VARIATION HSAB IS DEFINED AS VARIATION AB WITH THE FULL DIMM HEAT SPREADER ATTACHED. MODULE AB IS DEFINED FOR BOTH SINGLE AND DUAL DIE APPLICATIONS.

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/ 1	o١

COMPONENT KEEPIN ON VIEW B-B FOR VARIATIONS GB and HB: UDIMM: 29.5MM MAX COMPONENT KEEPIN.
RDIMM: 29.5MM MAX COMPONENT KEEPIN EXCEPT INSIDE 0.3MM HORIZONTAL SPAN OF HEATSINK NOTCH EDGES WHERE KEEP-IN IS 29.2MM MAX.

JEDEC	DDR3 SDRAM DIMM	ISSUE:	DATE:	ITEM	PAGE:
SOLID STATE PRODUCT OUTLINE	(DUAL INLINE MEMORY MODULE) FAMILY with 1.00mm CONTACT CENTERS	E	AUG 08	M□-269	13 OF 19

# NOTES: /19\ MAX ROUTE KEEP-OUT IS 29.3MM FOR VARIATIONS GB AND HB: MAX ROUTE KEEP-OUT IS 29.7MM EXCEPT AREAS INSIDE 0.3MM HORIZONTAL SPAN OF HEATSINK NOTCH EDGES WHERE MAX ROUTE KEEP-OUT IS 29.3MM. PASSIVE COMPONENTS LOCATED UNDER THE HEAT SINK AREA SHALL MAINTAIN MINIMUM CLEARANCE 20. OF 0.13MM. IN THE EVENT THE MINIMUM COMPONENT CLEARANCE CANNOT BE MAINTAINED A SECONDARY ELECTRICAL INSULATOR MUST BE EMPLOYED TO PREVENT SHORTING.

JEDEC JEDEC	DDR3 SDRAM DIMM	ISSUE:	DATE:	ITEM	PAGE:
SOLID STATE PRODUCT OUTLINE	(DUAL INLINE MEMORY MODULE) FAMILY with 1.00mm CONTACT CENTERS	E	AUG 08	M□-269	14 OF 19

## APPLICATION NOTES:



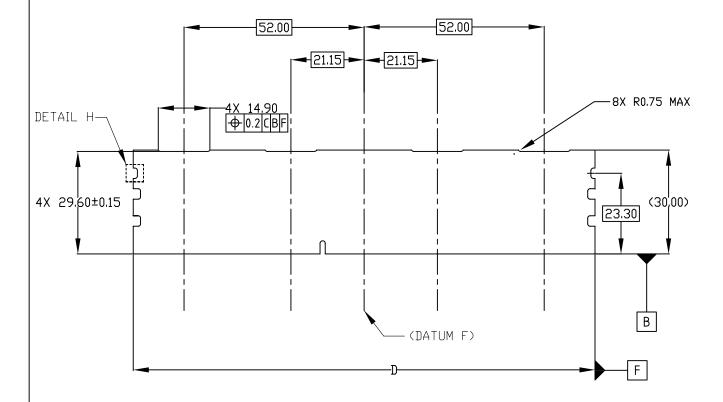
RECOMMENDED PLATING FOR CONTACT PADS ARE:

- 1) PREFERABLE PLATING: ELECTROLYTIC GOLD PLATING 0.76 MICROMETERS MINIMUM OVER ELECTROLYTIC NICKEL 2.00 MICROMETERS MINIMUM.
- 2) ALTERNATIVE PLATING: GOLD PLATING 0.05-0.75 MICROMETERS OVER NICKEL 2.00 MICROMETERS MINIMUM MUST USE AN ELECTRONIC CONTACT GRADE CORROSIVE BARRIER LUBRICANT.

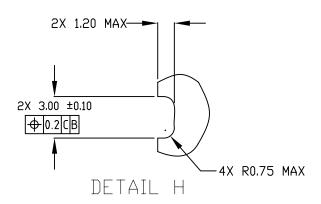


THE BEVEL IS A FABRICATION OPTION AND IS NOT REQUIRED. THE BEVEL AIDS THE INSERTION OF THE MODULE INTO THE CONNECTOR. THE BEVEL IS NOT TO HIT THE GOLD CONTACTS.

- 23. PATENT CLAIM: IT HAS BEEN STATED THAT US PATENT NO. 5,227,664 (HELD BY HITACHI) MAY RELATE TO CERTAIN IMPLEMENTATIONS OF THE PACKAGE DUTLINE.
- 24. VARIATION HSAB FDHS REFERENCE NOTCH.



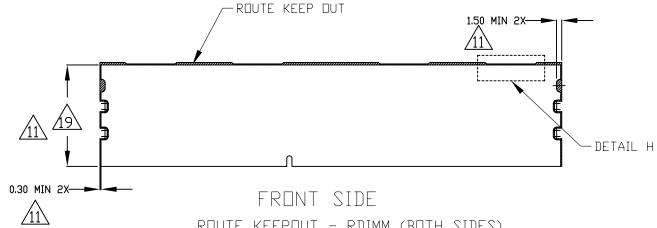
FRONT SIDE VIEW FDHS NOTCH REFERENCE DESIGN



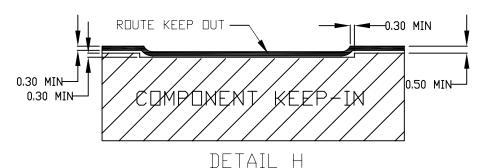
JEDEC JEDEC	DDR3 SDRAM DIMM	ISSUE:	DATE:	ITEM	PAGE:
SOLID STATE PRODUCT OUTLINE	(DUAL INLINE MEMORY MODULE) FAMILY with 1.00mm CONTACT CENTERS	E	AUG 08	M□-269	15 OF 19

## APPLICATION NOTES:

25. LP VARIATIONS GB AND HB ROUTE KEEP-OUT AND COMPONENT KEEP-IN REFERENCE:



ROUTE KEEPOUT - RDIMM (BOTH SIDES) (REFERENCE TO VARIATION HSAB NOMINAL DIMENSIONS)



ROUTE KEEPOUT AND COMPONENT KEEP-IN
(REFERENCE TO VARIATION HSAB NOMINAL DIMENSIONS)

- 1							
	TITLE: JEDEC	DDR3 SDRAM DIMM	ISSUE:	DATE:	ITEM	PAGE:	
	SOLID STATE PRODUCT OUTLINE	(DUAL INLINE MEMORY MODULE) FAMILY with 1.00mm CONTACT CENTERS	E	AUG 08	MO-269	16 OF 19	

#### CHANGE RECORD

IF THE CHANGE INVOLVES ANY WORDS ADDED OR DELETED (EXCLUDING DELETION OF ACCIDENTALLY REPEATED WORDS), THE CHANGE IS INCLUDED, PUNCTUATION CHANGES MAY OR MAY NOT BE INCLUDED.

[INITIAL ISSUE: A   DATE:	DECEMBER 2005	ITEM: JC11.14-086
	REVISION HISTORY	
ISSUE: B DATE: APR	IL 2006	Item: JC11.14-095
LEGATION	CHANCE EDDM	CHANCE TO
LOCATION:	CHANGE FROM:	CHANGE TD:
PAGE 4, DETAIL E & F	DEPTH OF LATCH NOTCH FROM 1.90	CHANGED TO 2.10 +/-0.15
PAGE 4, DETAIL E & F	RADIUS: FROM RO.30 MIN	RADIUS: FROM RO.70 MAX
ISSUE: C DATE: SEP	T 2007	Item: JC11.14-115 & 116
	OLIANGE EDEM	OLIANGE TO
LOCATION:	CHANGE FROM:	CHANGE TD:
PAGE 5, ROUTE KEEPOUT	N/A	ADDED PAGE 5, DETAIL G
PAGE 4, VIEW B-B	0.5 MIN	ADDED NOTE 10
PAGE 4, VIEW D-D	N/A	ADDED VIEW D-D
PAGE 2	VIEW B−B	VIEW D-D
PAGE 10	N/A	ADDED NOTES 10 & 11.
PAGES 10-11	APL. N□TES 10-12	RENAMED 12-14.
PAGE RENUMBERING	PAGES 5,6,7,8,9,10	PAGES 6,7,8,9,10,11
PAGES 10-11	N/A	UPDATED FONT STYLE/SIZE
PAGE 9, VARATIONS: DA, DB, DC, EA, EB, EC FA, FB & FC.	18.25 (MIN), 18.40(N□M) & 18.55(MAX)	18.60 (MIN), 18.75(N□M) & 18.90(MAX)

TITLE: JEDEC	DDR3 SDRAM DIMM	ISSUE:	DATE:	ITEM	PAGE:
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## CHANGE RECORD CONT.

ISSUE: D DATE: FEB 2008 Item: 11.14-120					
LOCATION:	CHANGE FROM:	CHANGE TO:			
PAGES 1 & 3	SHEET 3	SHEET 5			
I HdL3 I & 3	STILL 1 5	SHEET			
PAGE 2	N/A	ADDED PAGE 2			
PAGE 4	N/A	ADDED PAGE 4			
PAGE 5	0.20 MM	0.35MM			
PAGE 5 & 9	CENTER KEY ZONE	KEY ZONE			
PAGES 6, NOTE 10	TOP REFERENCE	BOTTOM EDGE REFERENCE			
PAGE 6	POSITIONAL TOLERANCE	8.00MM AND 15.80MM			
PAGE 7	0.7MM MAX TOP REFERENCE	29,30MM MAX BOTTOM REFERENCE			
PAGE 7	R1.00 MAX	R1.05 MAX			
PAGE 8.	N/A	ADDED PAGE 8 TO DEFINE THERMAL SENSOR AND REGISTER LOCATIONS			
PAGE 9	×A, ×B AND ×C	×B			
PAGE 10	A1 AND A3	REMOVED A1 & A3			
PAGE 10	N/A	ADDED THERMAL SENSOR VARIABLE TABLE.			
PAGE 11	N/A	UPDATED VARIATIONS TABLE			
PAGE 12	N/A	ADDED HSAB FDHS VARIATION			
PAGE 13	N/A	ADDED NOTES 12-17			
PAGE 14	N/A	ADDED APPLICATION NOTE 21.			
PAGE 7	46.60	23.30 CORRECTED SCALE ERROR.			

TITLE: JE DE C	DDR3 SDRAM DIMM	ISSUE:	DATE:	ITEM	PAGE:
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## CHANGE RECORD CONT.

ISSUE: E	_	DATE: AUG 08	Item	JC11.14-126	&	-127

LOCATION:	CHANGE FROM:	CHANGE TD:
PAGE 1	N/A	ADDED REFERENCE TO VARIATIONS GB AND HB
PAGES 2 & 4	N/A	ADDED REFERENCE TO VARIATIONS HSGB AND HSHB
PAGE 4	N/A	ADDED DIM 4.5mm MIN
PAGE 5	NOTE NUMBER 19	RENUMBERED TO 21
PAGE 5	DETAIL D DIMENSIONAL TOLERANCE OF ±0.05	+0.04/-0.05
PAGE 6	N/A	ADDED NOTE 18 AND REFERENCE TO VARIATIONS GB & HB
PAGE 7	"29.30mm MAX" ROUTE KEEP OUT	N□TE 19
PAGE 8	N/A	ADDED HB & GB
PAGE 11	N/A	ADDED NEW LP VARIATIONS GB & HB
PAGE 12	N/A	ADDED NEW VARIATIONS HSGB & HSHB
PAGE 13	N/A	ADDED NOTE 18
PAGE 14	N/A	ADDED NOTES 19 + 20
PAGE 15	NOTES 18-21	RENUMBERED TO 20-23
PAGE 16	N/A	ADDED NOTE 24

JE DE'C	DDR3 SDRAM DIMM (DUAL INLINE MEMORY MODULE) FAMILY with 1.00mm CONTACT CENTERS	ISSUE:	DATE:	ITEM	PAGE:
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