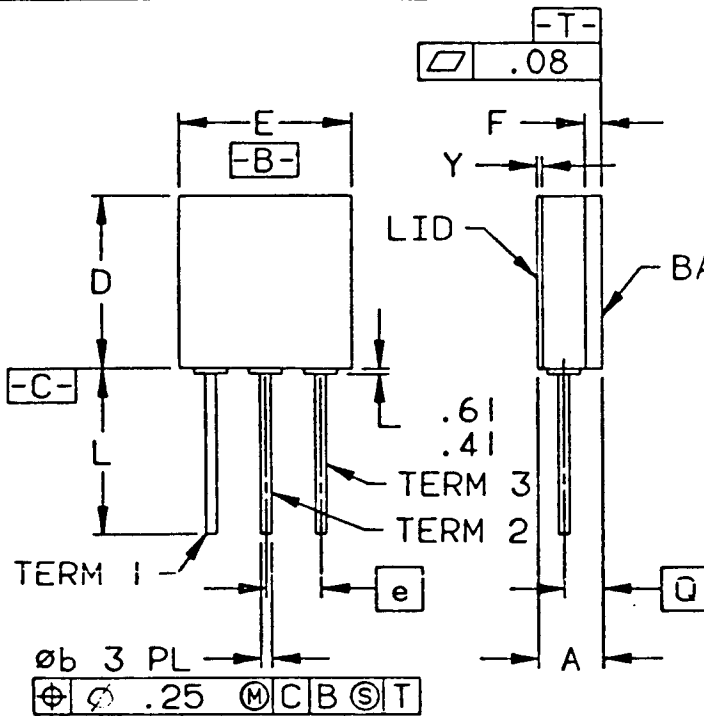


NOTES

1. REFER TO APPLICABLE SYMBOL LIST.
2. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
3. CONTROLLING DIMENSION: INCH.
4. MAXIMUM RADIUS OF 1.27 IS PERMITTED ON ALL BODY EDGES AND CORNERS.
5. Q TO BE MEASURED WITHIN 1.02 OF DATUM C.



VARIATIONS (ALL DIMENSIONS SHOWN IN MILLIMETERS)

SYMBOL	VARIATIONS (ALL DIMENSIONS SHOWN IN MILLIMETERS)												
	AA		NOTE			NOTE			NOTE			NOTE	
	MIN.	MAX.		MIN.	MAX.		MIN.	MAX.		MIN.	MAX.		
A	5.59	6.35	5										
Øb	.94	1.09											
D	15.75	16.01											
e	5.10	BSC											
E	15.75	16.01											
F	1.52	REF											
L	13.97	16.51											
Q	3.56	BSC											
Y	.13	REF											
NOTE	1, 2, 3												
REF	ITEM 10-245												
ISSUE													
JEDEC SOLID STATE PRODUCT OUTLINES				TITLE FLAT MOUNTED TRANSISTOR				ISSUE A		DATE 3/83		TO-256	



1. REFER TO APPLICABLE SYMBOL LIST.
2. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
3. CONTROLLING DIMENSION: INCH.
4. MAXIMUM RADIUS OF .050 IS PERMITTED ON ALL BODY EDGES AND CORNERS.
5. Q TO BE MEASURED WITHIN .040 OF DATUM C.

SYMBOL	VARIATIONS (ALL DIMENSIONS SHOWN IN INCHES)												
	AA		NOTE			NOTE			NOTE			NOTE	
	MIN.	MAX.		MIN.	MAX.		MIN.	MAX.		MIN.	MAX.		
A	.220	.250	5										
Øb	.037	.043											
D	.620	.630											
e	.200	BSC											
E	.620	.630											
F	.060	REF											
L	.550	.650											
Q	.140	BSC											
Y	.005	REF											
NOTE	1, 2, 3												
REF	ITEM 10-245												
ISSUE													
JEDEC SOLID STATE PRODUCT OUTLINES				TITLE FLAT MOUNTED TRANSISTOR				ISSUE A		DATE 3/88		TO-256	