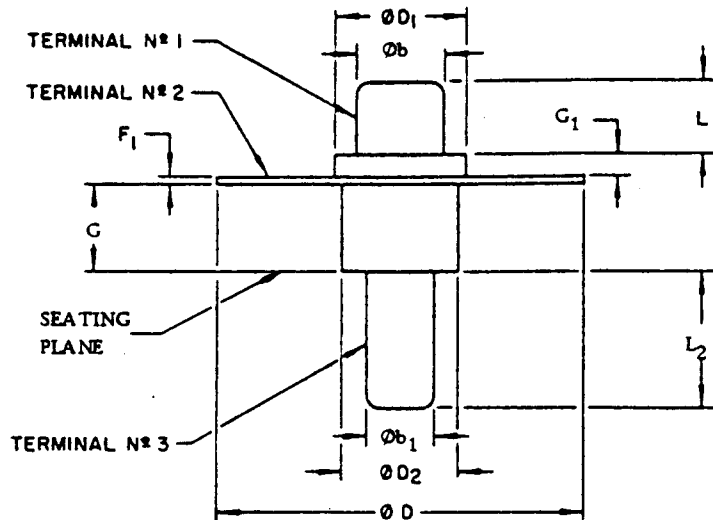


NOTES :

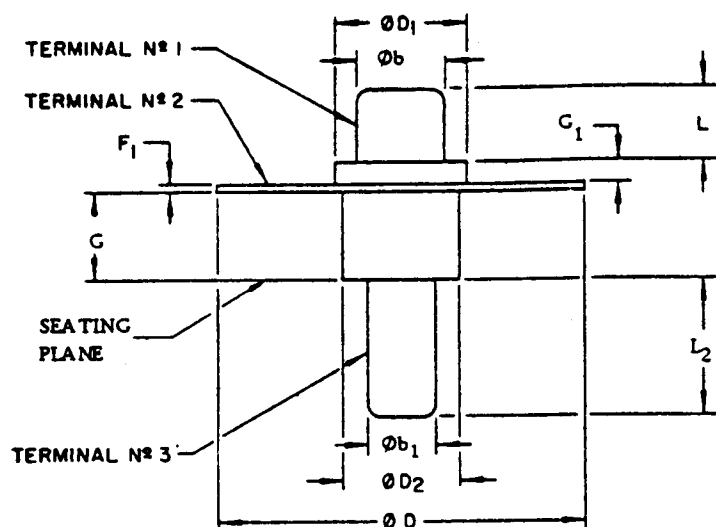
1. Refer to rules for dimensioning semiconductor product outlines



SYMBOL	VARIATIONS (ALL DIMENSIONS SHOWN IN MILLIMETERS)											
	AA		NOTE			NOTE			NOTE			NOTE
	MIN.	MAX.		MIN.	MAX.		MIN.	MAX.		MIN.	MAX.	
Øb	2.972	3.124										
Øb ₁	2.286	2.387										
ØD	12.599	12.801										
ØD ₁	4.445	4.699										
ØD ₂	3.988	4.241										
F ₁	.229	.304										
G	2.90	3.22										
G ₁	.69	1.01										
L	2.490	2.667										
L ₂	4.55	4.85										
NOTE	1											
REF.	Item 269G											
ISSUE	A June, 1971											
JEDEC SEMICONDUCTOR DEVICE OUTLINES			TITLE COAXIAL PACKAGE OUTLINE				ISSUE A		DATE JUNE 1971		T0-215AA	

NOTES :

1. Refer to rules for dimensioning semiconductor product outlines



SYMBOL	VARIATIONS (ALL DIMENSIONS SHOWN IN INCHES)											
	AA		NOTE			NOTE			NOTE			NOTE
	MIN.	MAX.		MIN.	MAX.		MIN.	MAX.		MIN.	MAX.	
ϕb	.117	.123										
ϕb_1	.090	.094										
ϕD	.496	.504										
ϕD_1	.175	.185										
ϕD_2	.157	.167										
F_1	.009	.012										
G	.114	.127										
G_1	.027	.040										
L	.098	.105										
L_2	.179	.191										
NOTE	1											
REF.	Item 269G											
ISSUE	A June, 1971											
JEDEC SEMICONDUCTOR DEVICE OUTLINES				TITLE COAXIAL PACKAGE OUTLINE				ISSUE A	DATE JUNE 1971	TO-215AA		