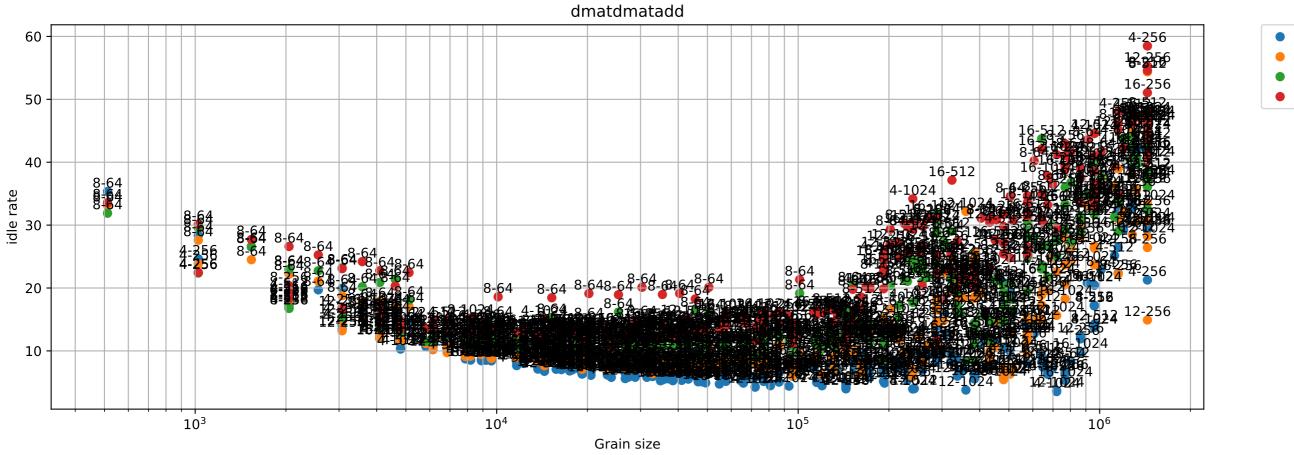
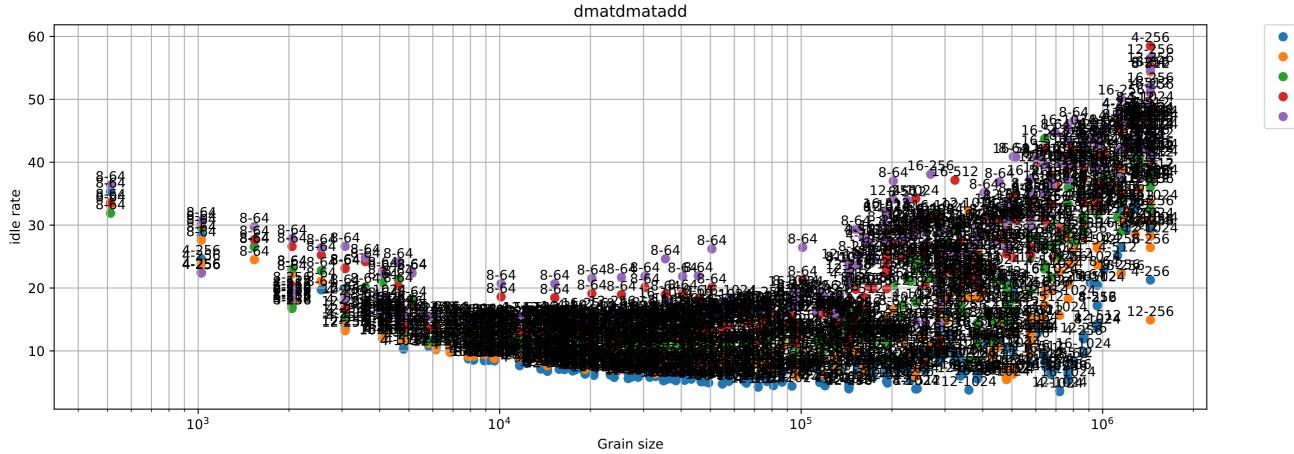


- 2 threads matrix_size:1200 core 03 threads matrix size:1200 core 0
 - 3 threads matrix_size:1200 core 0
 - 4 threads matrix_size:1200 core 0

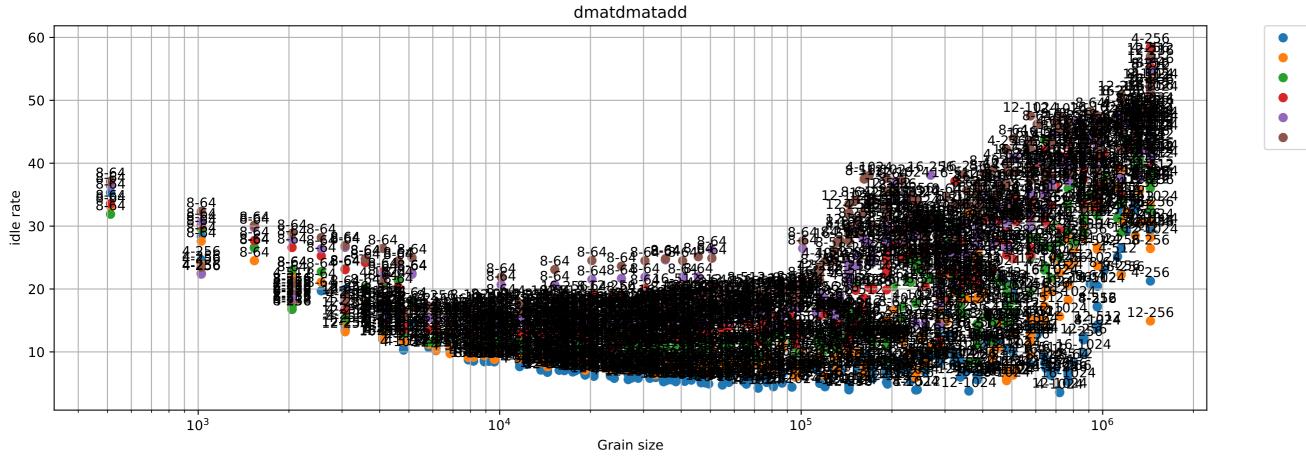


- 2 threads matrix_size:1200 core 0 3 threads matrix_size:1200 core 0

 - 4 threads matrix_size:1200 core 0
 - 5 threads matrix_size:1200 core 0

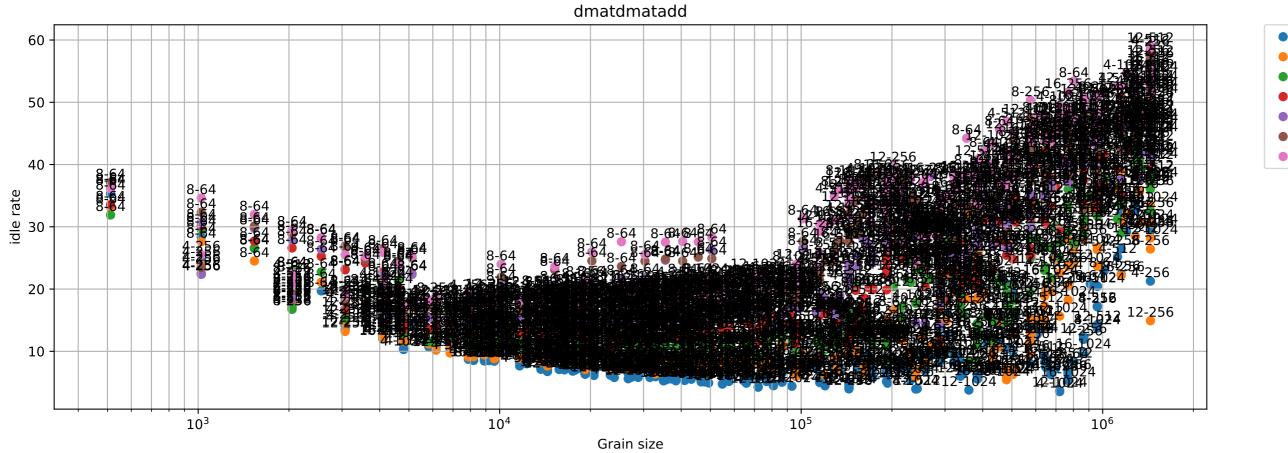


- 2 threads matrix_size:1200 core 0
 - 3 threads matrix_size:1200 core 0
 - 4 threads matrix_size:1200 core 0
- 5 threads matrix_size:1200 core 0
- 6 threads matrix_size:1200 core 0



2 threads matrix_size:1200 core 0 3 threads matrix_size:1200 core 0

- 4 threads matrix_size:1200 core 0
- 5 threads matrix_size:1200 core 0
- 6 threads matrix_size:1200 core 0
- - 7 threads matrix_size:1200 core 0



- 2 threads matrix_size:1200 core 0 3 threads matrix_size:1200 core 0
- 4 threads matrix_size:1200 core 0
- 5 threads matrix_size:1200 core 0
- 6 threads matrix_size:1200 core 0
- 7 threads matrix_size:1200 core 0
- 8 threads matrix_size:1200 core 0