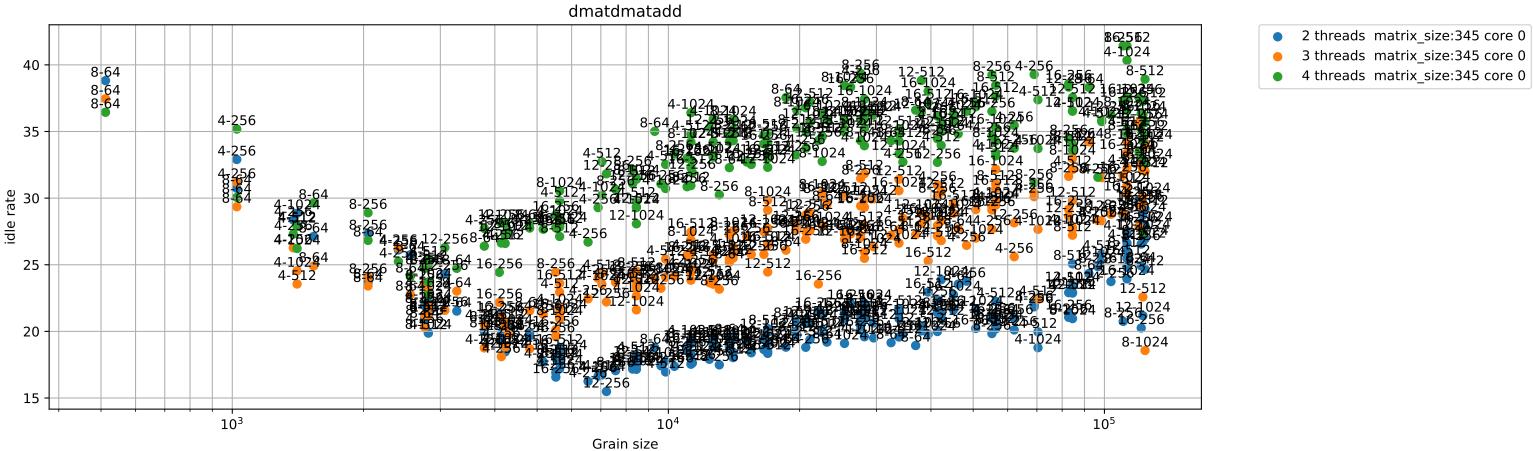
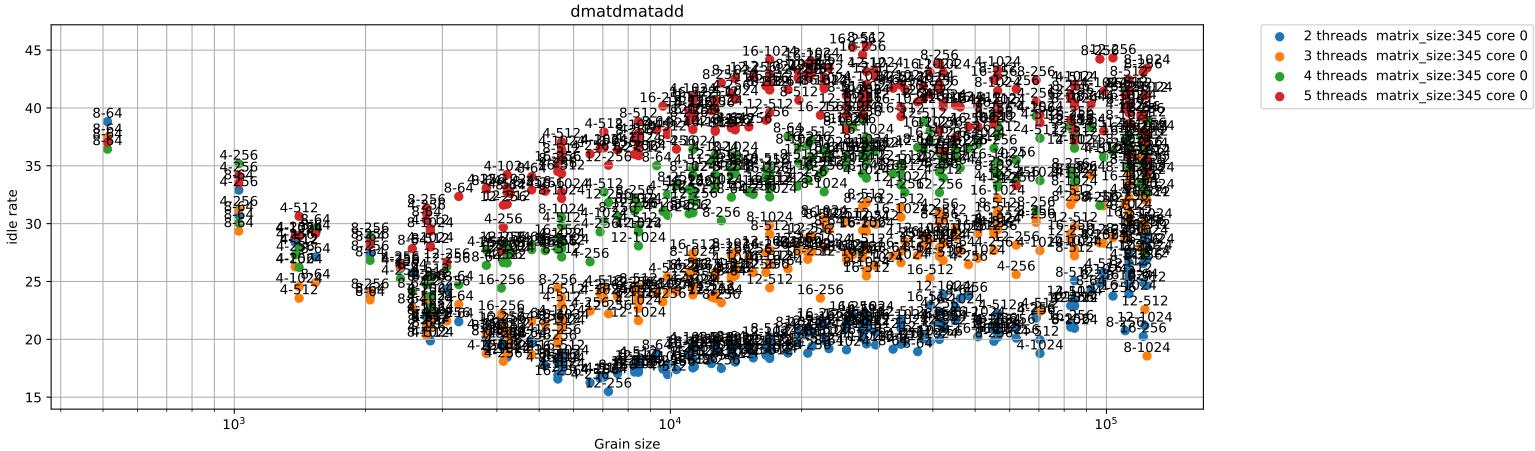


3 threads matrix_size:345 core 0

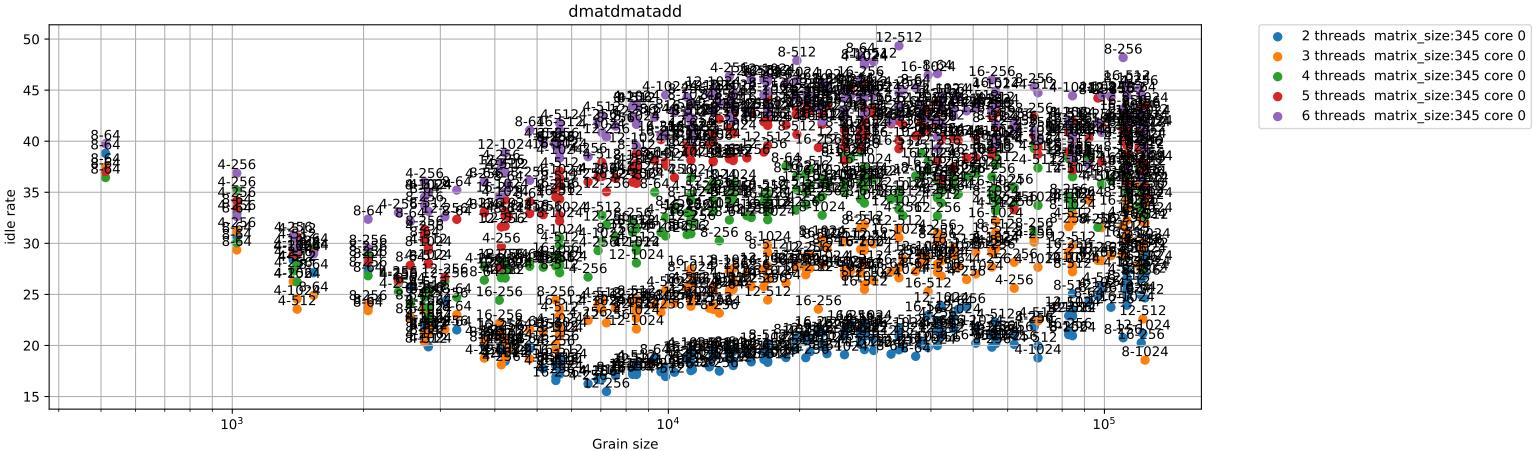


3 threads matrix_size:345 core 0 4 threads matrix_size:345 core 0

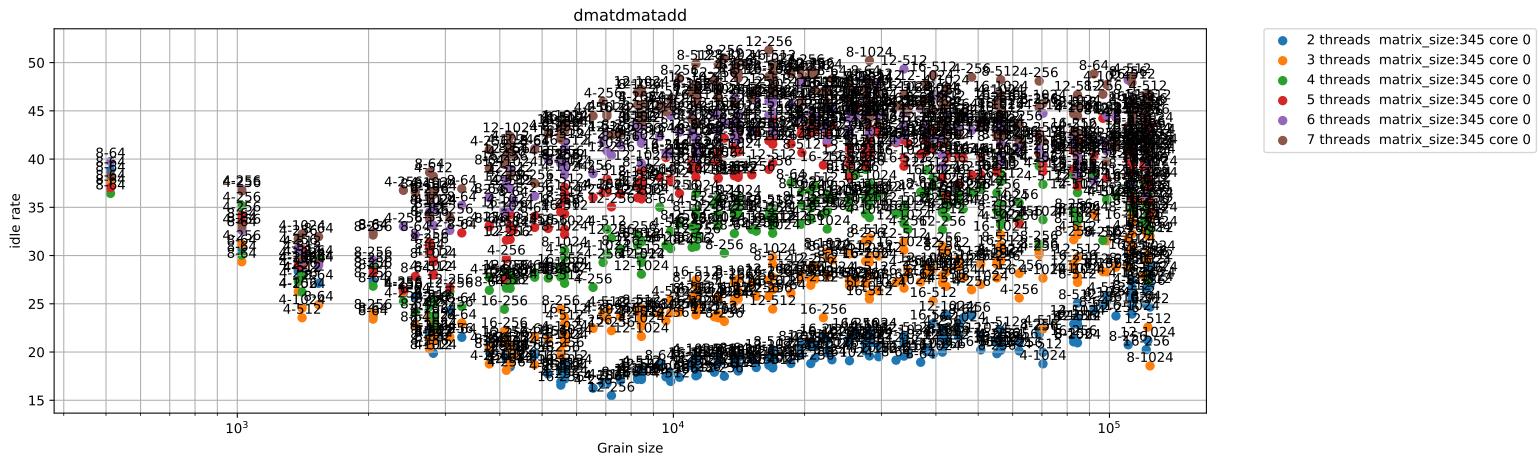


3 threads matrix_size:345 core 0 4 threads matrix_size:345 core 0

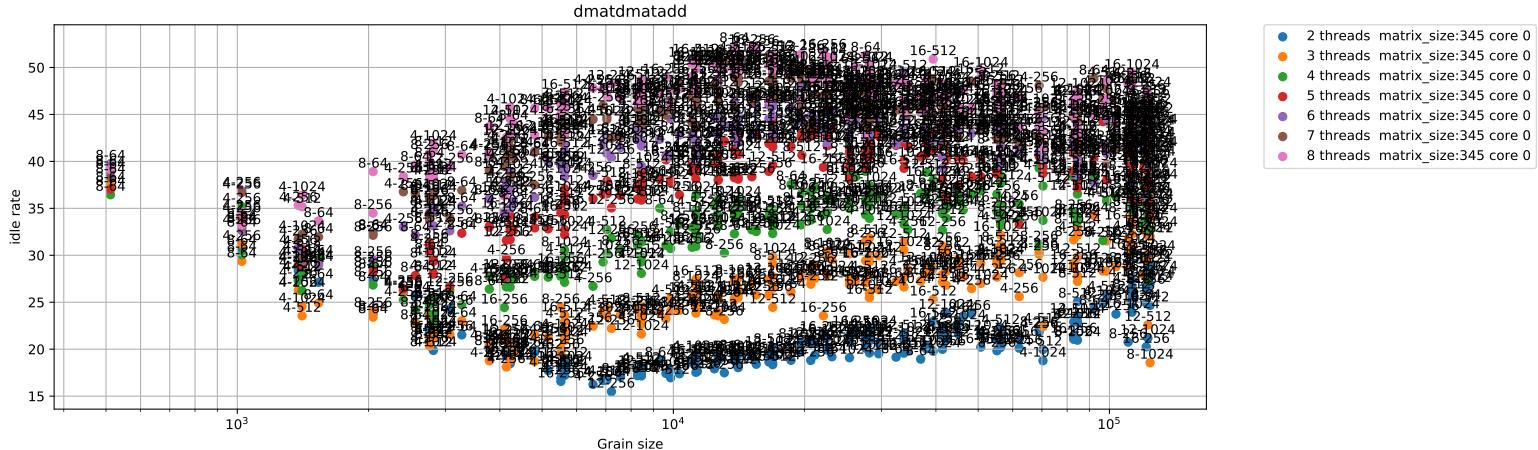
5 threads matrix_size:345 core 0



3 threads matrix_size:345 core 0 4 threads matrix_size:345 core 0 5 threads matrix_size:345 core 0 6 threads matrix_size:345 core 0



3 threads matrix_size:345 core 0 4 threads matrix_size:345 core 0 5 threads matrix_size:345 core 0 6 threads matrix_size:345 core 0 7 threads matrix_size:345 core 0



- 3 threads matrix_size:345 core 0 4 threads matrix_size:345 core 0 5 threads matrix_size:345 core 0 6 threads matrix_size:345 core 0
 - 7 threads matrix_size:345 core 0
 - 8 threads matrix_size:345 core 0