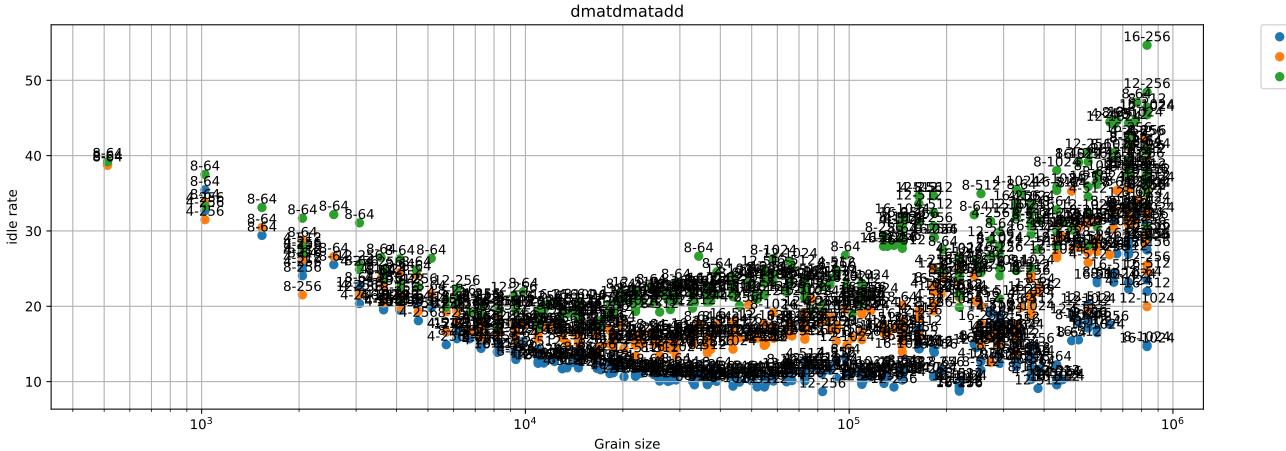
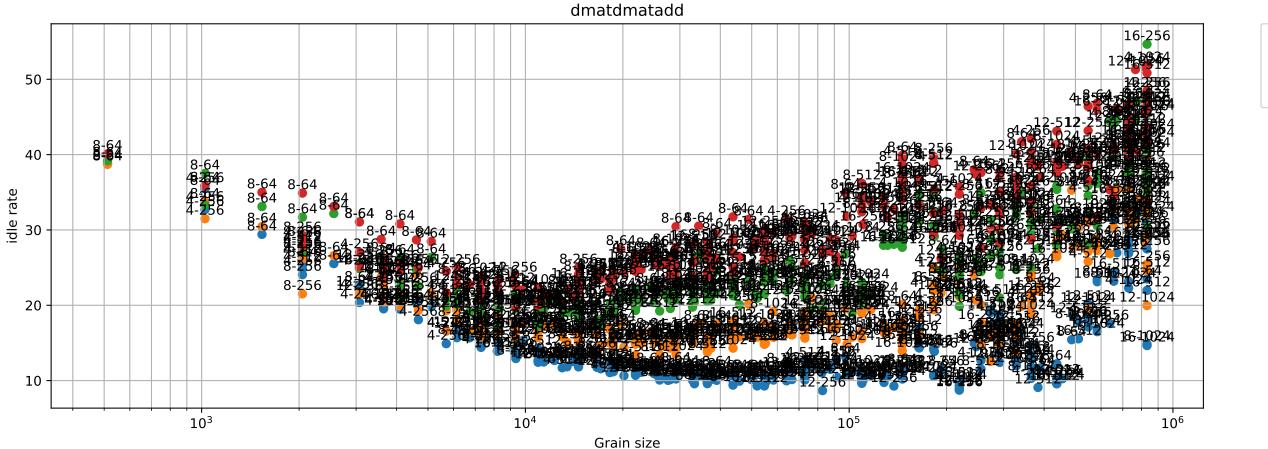


- 2 threads matrix_size:912 core 03 threads matrix_size:912 core 0
 - 3 threads matrix_size:912 core

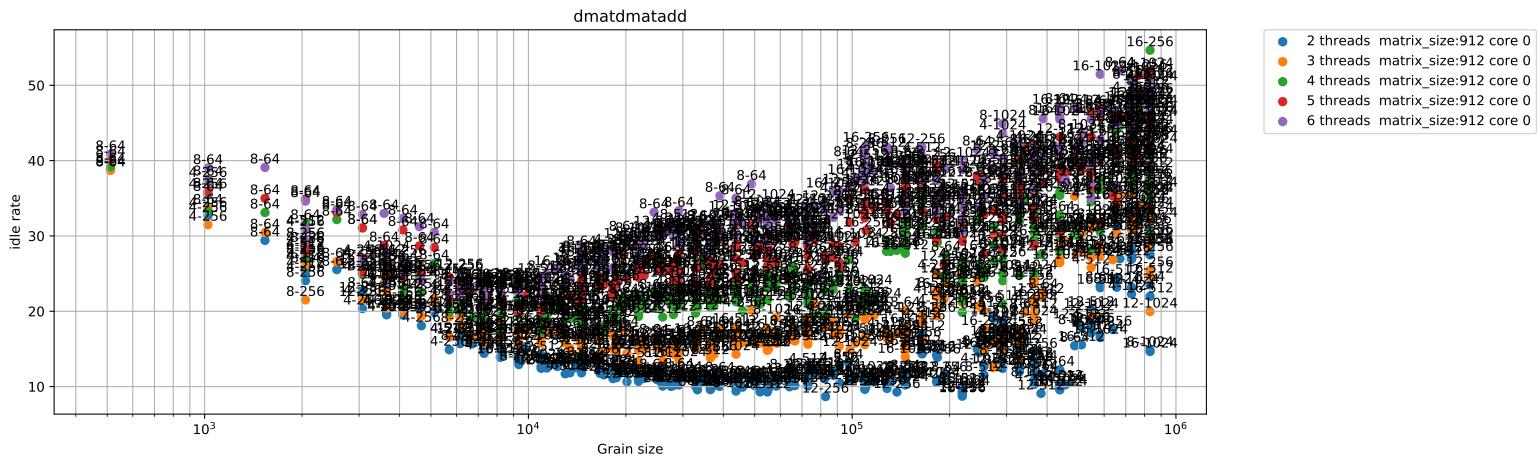


2 threads matrix_size:912 core 03 threads matrix_size:912 core 04 threads matrix_size:912 core 0

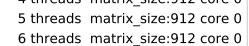


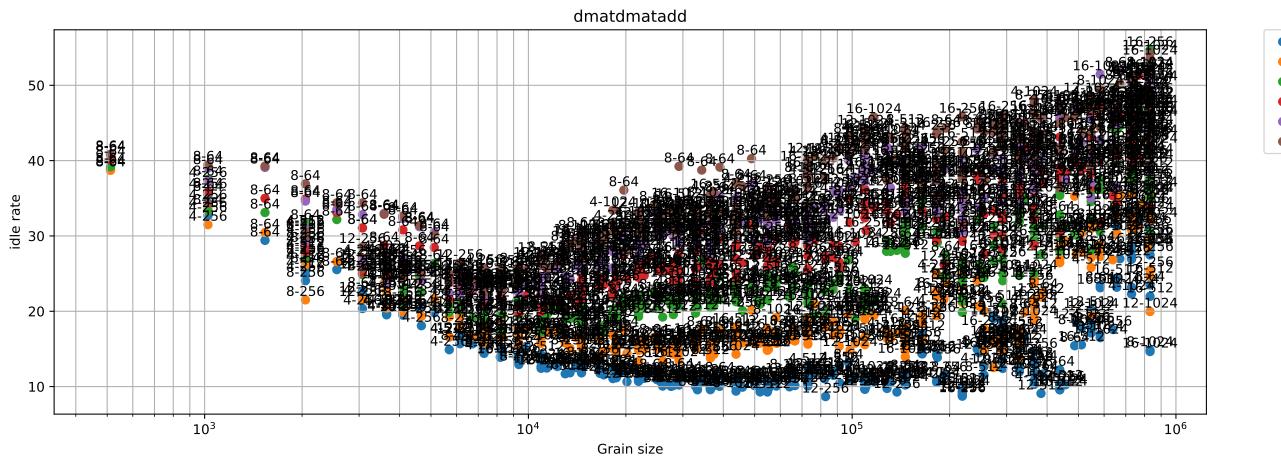
2 threads matrix_size:912 core 03 threads matrix_size:912 core 0

- 4 threads matrix_size:912 core 0
- 5 threads matrix_size:912 core 0

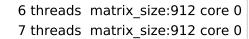


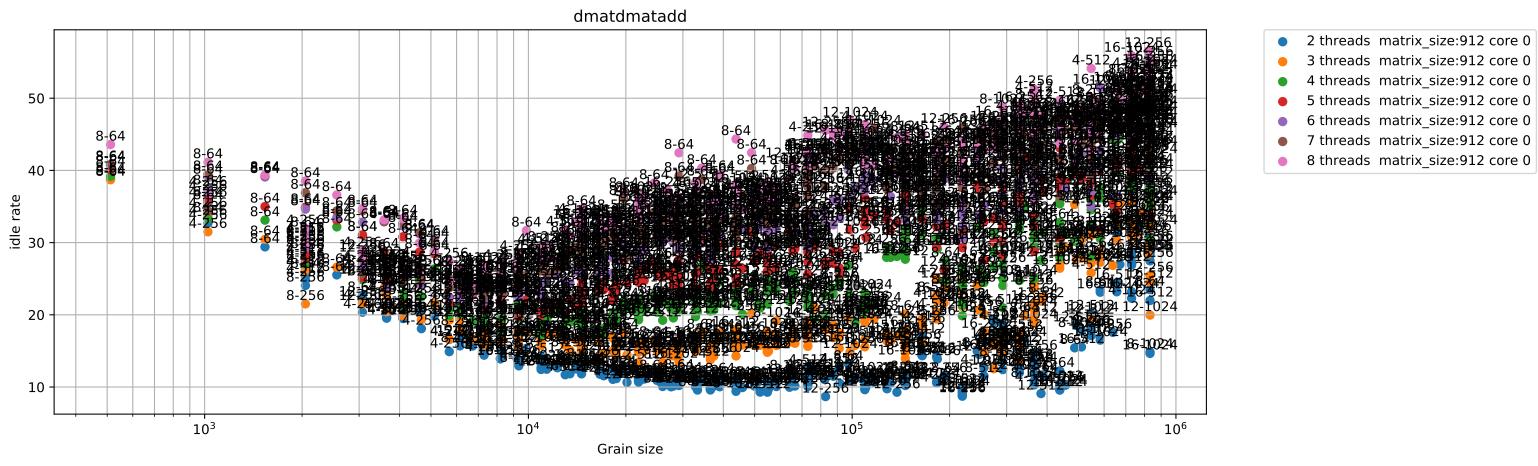
3 threads matrix_size:912 core 0 4 threads matrix_size:912 core 0





2 threads matrix_size:912 core 0
3 threads matrix_size:912 core 0
4 threads matrix_size:912 core 0
5 threads matrix_size:912 core 0





2 threads matrix_size:912 core 0 3 threads matrix_size:912 core 0 4 threads matrix_size:912 core 0 5 threads matrix_size:912 core 0 6 threads matrix_size:912 core 0 7 threads matrix_size:912 core 0