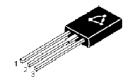
PNP Power Darlington Transistor







Pin Configuration:

- 1. Emitter
- 2. Collector
- 3. Base

Absolute Maximum Ratings

Parameter	Symbol	BD680	Unit	
Collector Base Voltage	V _{CBO}	90		
Collector Emitter Voltage	V _{CEO}	80	V	
Emitter Base Voltage	V _{EBO}	5		
Collector Current	I _C	4		
Base Current	I _B	0.1	А	
Total Power Dissipation at T _a = 25°C Derate above 25°C	D	1.25 10	W mW/°C	
Total Power Dissipation at T _C = 25°C Derate above 25°C	P _D	40 0.32	W W/°C	
Operating and Storage Junction Temperature Range	T _j , T _{stg}	-55 to +150	°C	

Thermal Resistance

Junction to Case	R _{th (j-c)}	3.13	°C/W
Junction to Ambient in Free Air	R _{th (j-a)}	100	C/VV





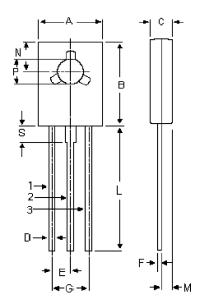
PNP Power Darlington Transistor



Electrical Characteristics (T_a = 25°C unless specified otherwise)

Parameter	Symbol	Test Condition	Min.	Max.	Unit
Collector Emiiter Voltage	V _{CEO} *	$I_{\rm C} = 50 {\rm mA}, I_{\rm B} = 0$	80	-	V
Collector Cut off Current	I _{CEO}	V_{CE} = Half Rated V_{CEO} , I_B = 0 V_{CB} = Rated V_{CBO} , I_E = 0	-	500 0.2	μA mA
	I _{CBO}	V_{CB} = Rated V_{CBO} , I_{E} = 0 T_{C} = 100°C		2	mA
Emitter Cut off Current	I _{EBO}	$V_{EB} = 5V, I_{C} = 0$	-	2	mA
Collector Emitter Saturation Voltage NON A	V _{CE (sat)} *	I _C = 1.5A, I _B = 6mA	-	2.5	V
Base Emitter On Voltage NON A	V _{EB (on)} *	I _C = 1.5A, V _{CE} = 3V	-	2.5	V
DC Current Gain NON A	h _{FE} *	I _C = 1.5A, V _{CE} = 3V	750	-	-
Small Signal Current Gain	h _{fe}	I _C = 1.5A, V _{CE} = 3V f = 1MHz	1	-	-

^{*}Pulse Test : Pulse Width = ≤300µs, Duty Cycle = ≤2%.



Pin Configuration:

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Dimensions	Min.	Max.
Α	7.4	7.8
В	10.5	10.8
С	2.4	2.7
D	0.7	0.9
E	2.25 (Typical)	
F	0.49	0.75
G	4.5 (Typical)	
L	15.7 (Typical)	
M	1.27 (Typical)	
N	3.75 (Typical)	
Р	3	3.2
S	2.5 (Typical)	

Dimensions: Millimetres

Part Number Table

Description	Part Number
Darlington Transistor, TO-126	BD680

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