DDDAC 1794 NOS

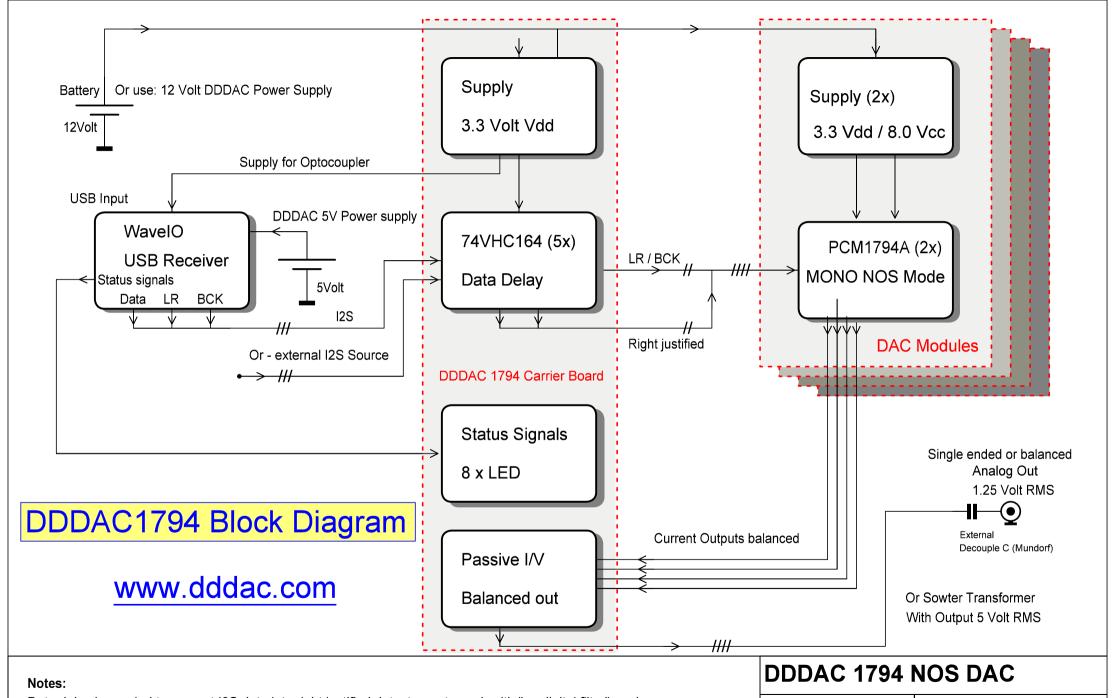
A Modular DAC, based on PCM1794 and USB WavelO, Input 192kHz / 24bit

By Doede Douma

Version 3.0

Updated November 22nd, 2012

http://www.dddac.com/

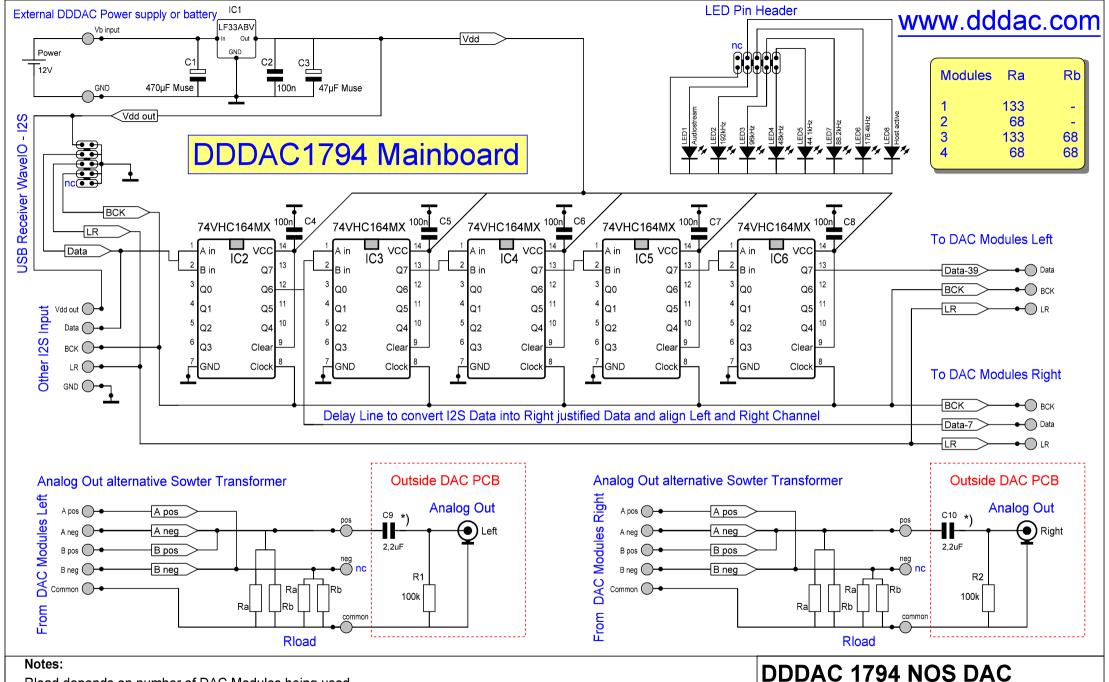


Data delay is needed to convert I2S data into right justified datastream to work with "no digital filter" mode Datastream must be delayed by 7 clock cycles (31 - 24)

Left Channel must be delayed another 32 clock cycles to allign left and right channel sample

Update: 14-11-2012 Author: Doede Douma

Revision: 2.0 Built: Design ready: Page: 1 # 6
YES YES



Rload depends on number of DAC Modules being used

Rload = 134 / number of DAC Modules

Rload can be combined on PCB by paralleling 2 resistors - see table at top right corner

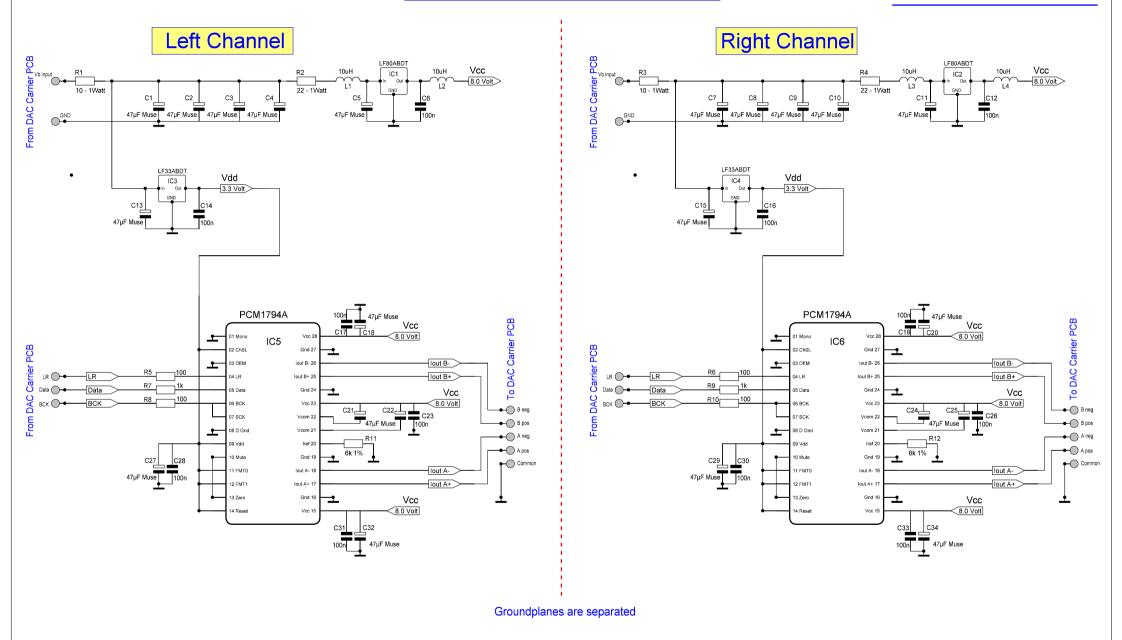
Output Capacitor should always be changed to one of own personal choice! This is the best tweak possible

*) DDDAC personal choice is 2,2uF Mundorf Silver/Gold - Oil

| Update: 14-11-2012 | | Author: Doede Douma | | |
|--------------------|---------------|---------------------|-------|-------------|
| Revision: 2.0 | Built: YES | Design re | eady: | Page: 2 # 6 |

DDDAC1794 DAC Module PCB

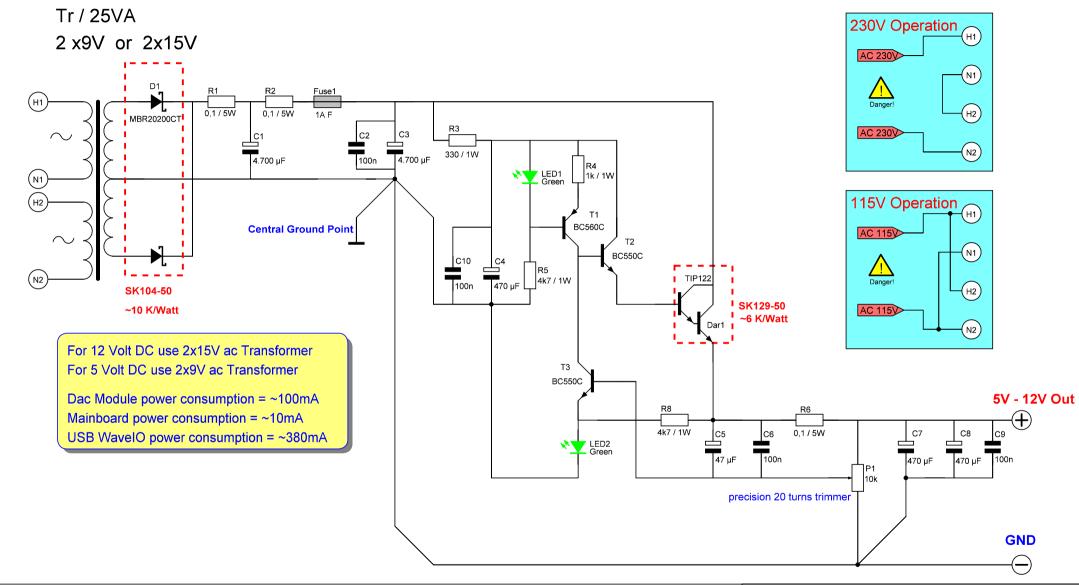
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|--------------------|---------------|----------------------|-------------|--|--|
| Revision: 2.0 | Built: YES | Design ready: YES | Page: 3 # 6 | | |

DDDAC1794 Power Supply 5V / 12V 1A

www.dddac.com



Notes:

Use Trimmer P1 to adjust output at 5.0 Volt or 12 Volt
Actual Output current is limited by Fuse and rating of Transformer
47uF and 470uF are Nichicon Muze Audio Grade KZ series Capacitors - (25 or 50 Volts)
4.700uF are Nichicon Long Life Capacitors. Series HE (25 Volts)

DDDAC 1794 NOS DAC

| Update: 14-11- | 2012 | Author: Doede Douma | | |
|----------------|--------|---------------------|-----------|--|
| Revision: 3.2 | Built: | Design ready: | Page: 4#6 | |

