

[illegible]

# CASSETTE

The edge connector is dual-sided with corresponding pads connected

Also CASS\_RD

SRQ

C\_WRITE

C\_MOTOR

Components:

- Q1: 2SD313
- Q2: 2SC1815
- D1: RD6.8EB
- R1: 470
- R3: 1k
- C1: 0.1u
- C2: 470p
- C3: 470p
- C84: 0.1u

Power supplies: +5V, +9V\_UNREG

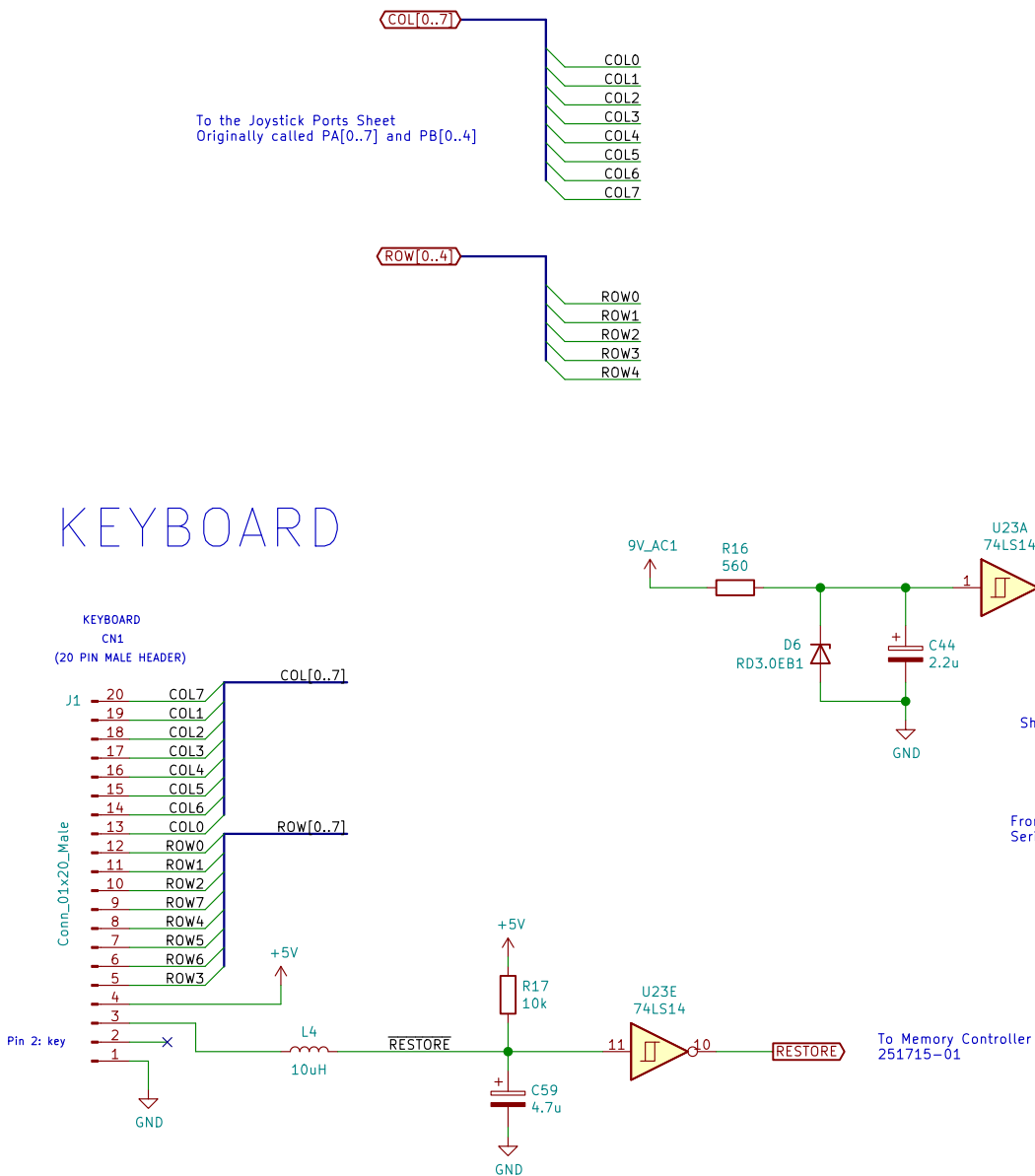
Ground: GND

Connectors: J3 (Conn\_02x6\_Edge), CN3 (6 PIN MALE EDGE)

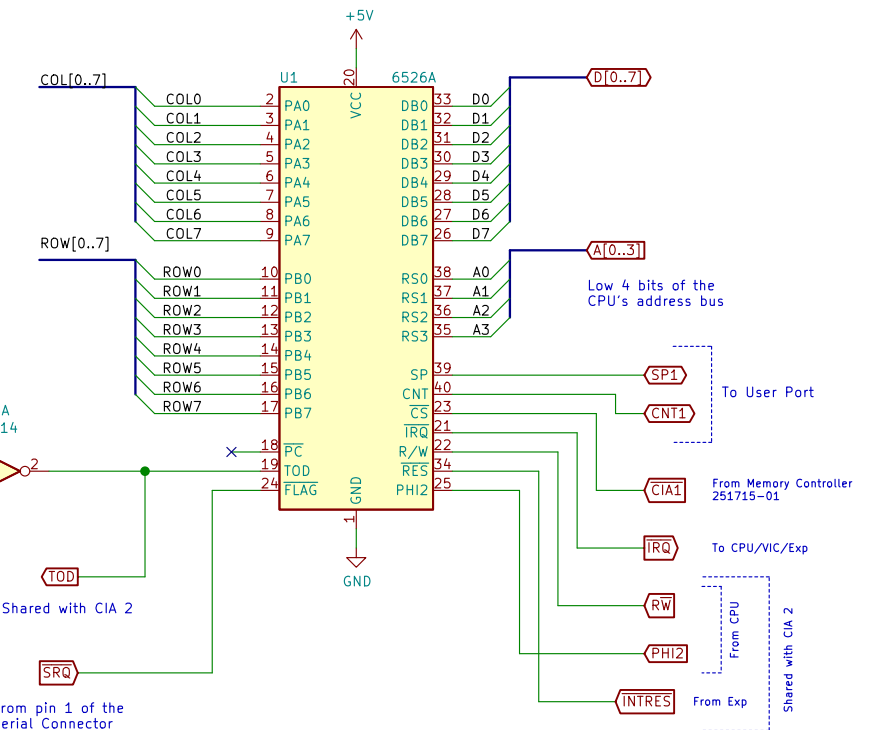
Labels: MOTOR, CASS\_RD, SRQ, C\_WRITE, C\_MOTOR

|  |                  |          |
|--|------------------|----------|
| Sheet: /Tape - Serial/<br>File: tape_serial.sch      |                  |          |
| <b>Title: Commodore C64C - Assy 250469-01 Rev. A</b> |                  |          |
| Size: A4   | Date: 2020-04-15 | Rev: 1   |
| KiCad E.D.A.    kicad 5.1.6                          |                  | Id: 2/11 |

# KEYBOARD



# CIA 1 (DC00-DCFF)



Author: Andrea Cisternino <a.cisternino@gmail.com>  
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<https://github.com/KiCadRetroArchive>

Sheet: /Keyboard - CIA 1/  
File: keyboard.sch

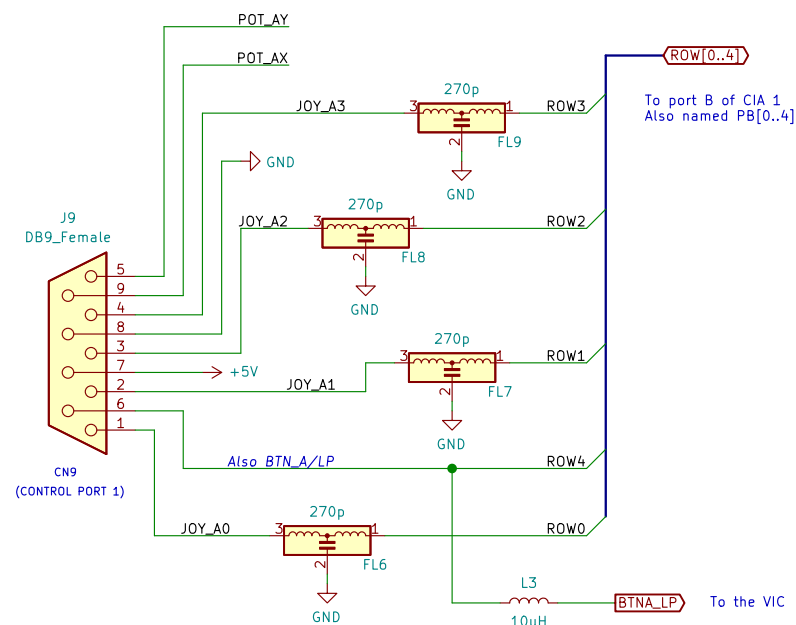
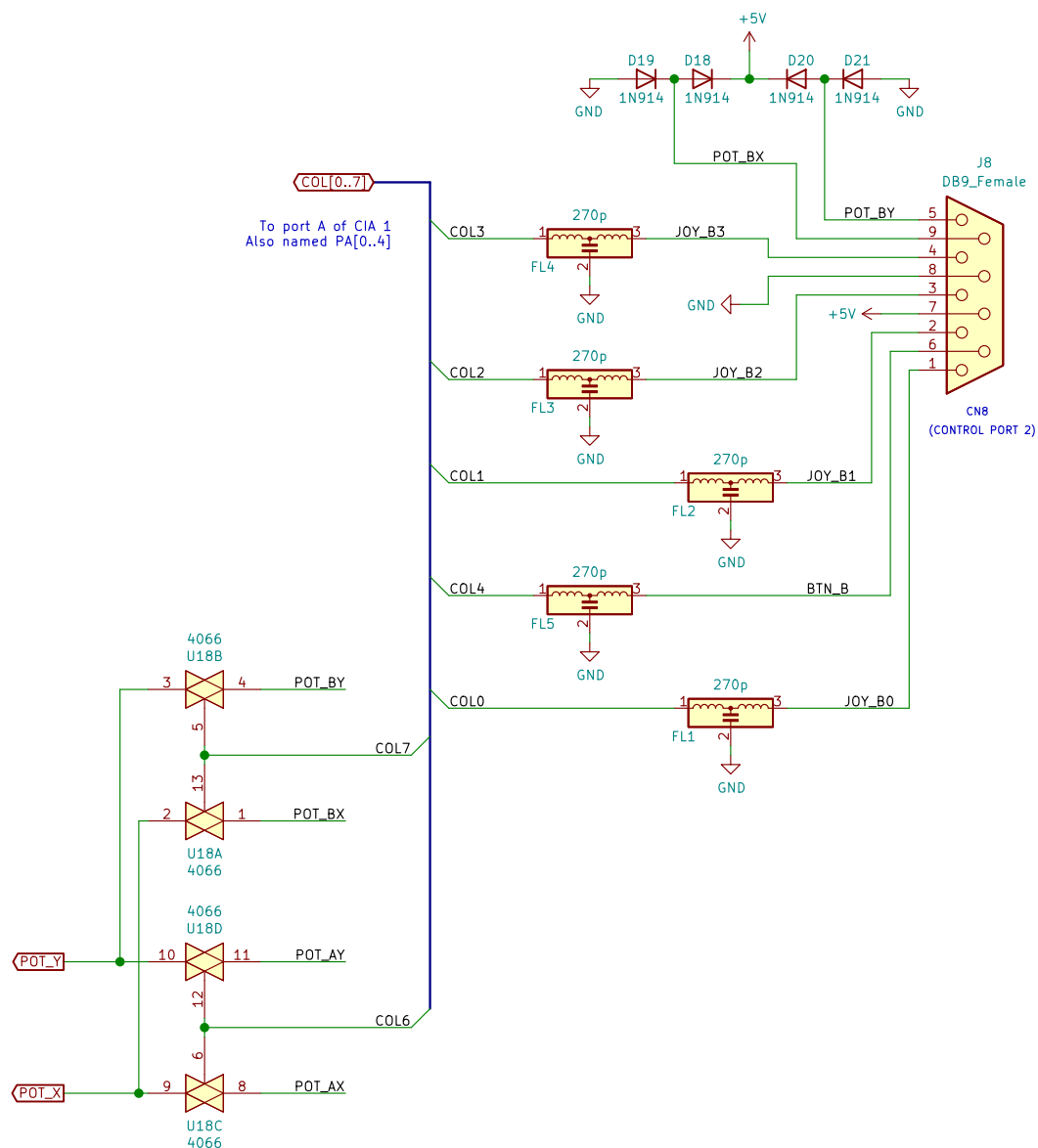
**Title: Commodore C64C - Assy 250469-01 Rev. A**

Size: A4 Date: 2020-04-15

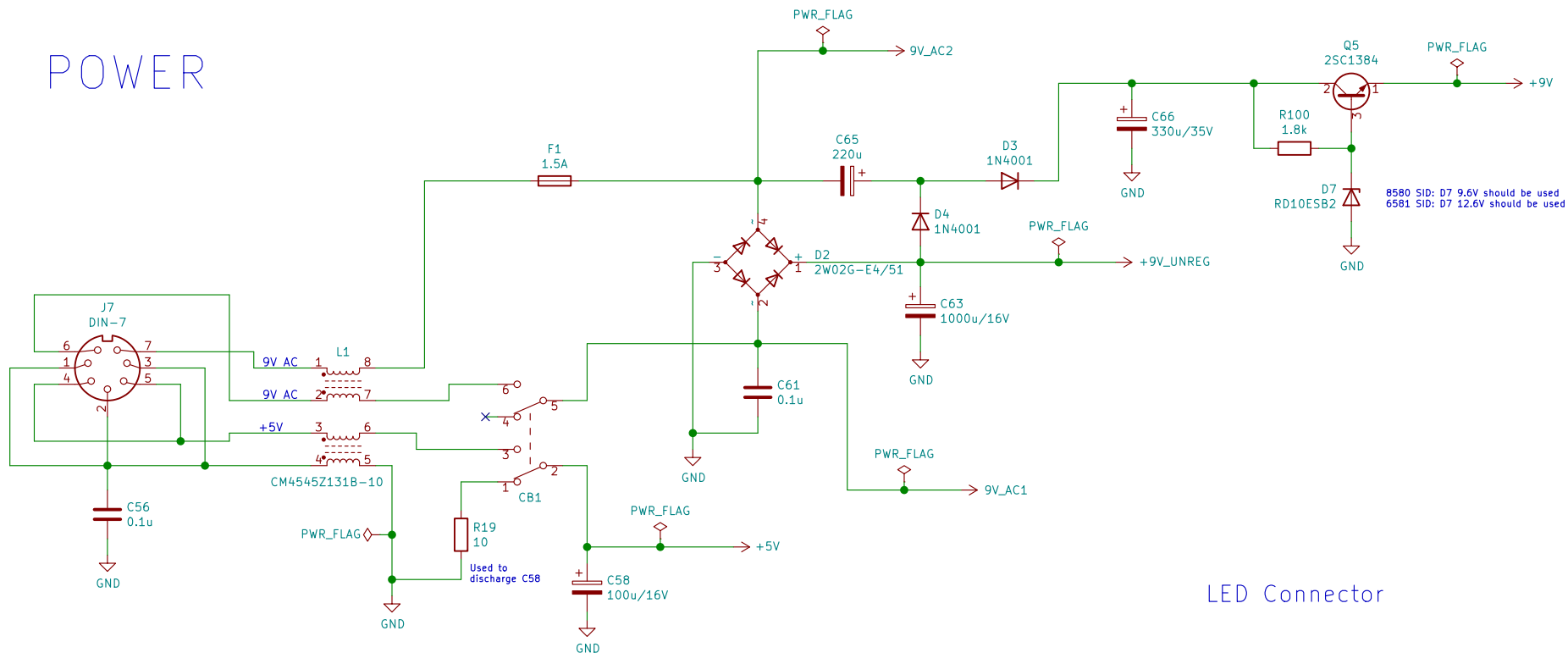
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Rev: 1

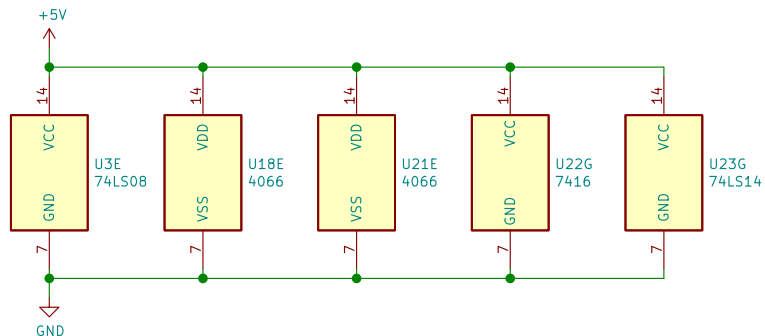
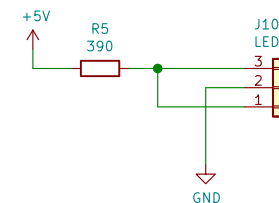
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# POWER



## LED Connector



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Sheet: /Power/  
 File: power.sch

**Title: Commodore C64C – Assy 250469-01 Rev. A**

Size: A4 Date: 2020-04-15

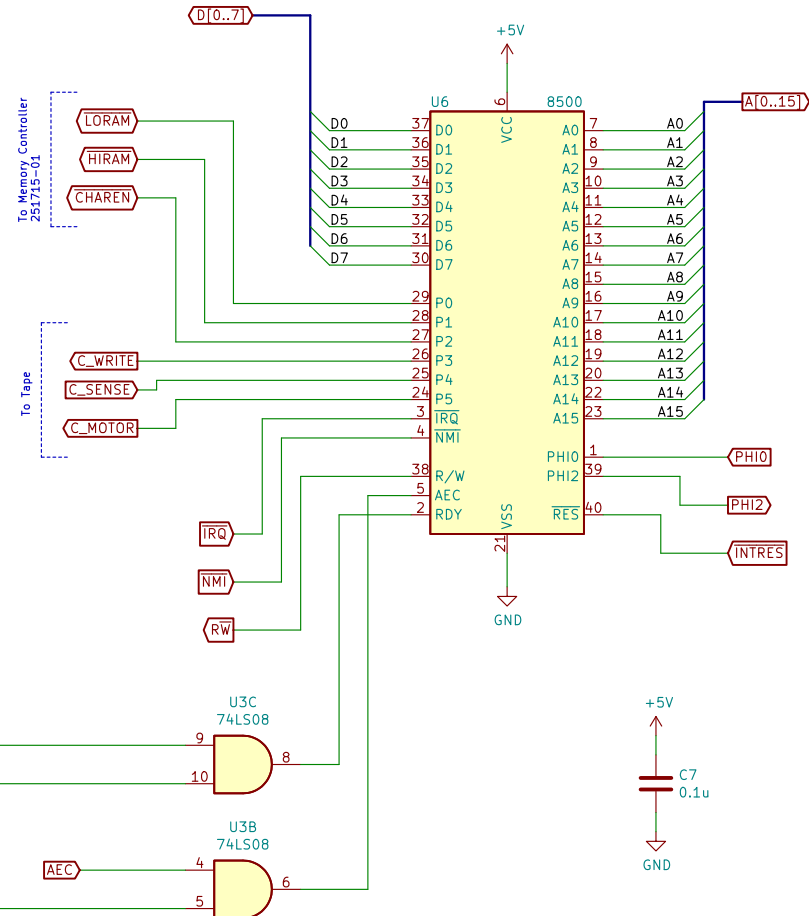
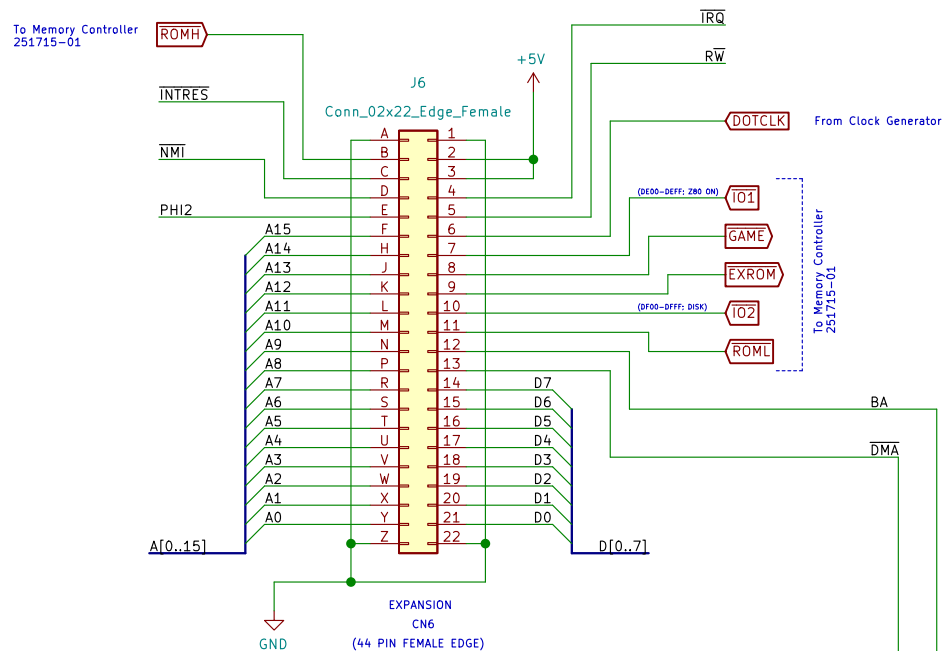
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Rev: 1

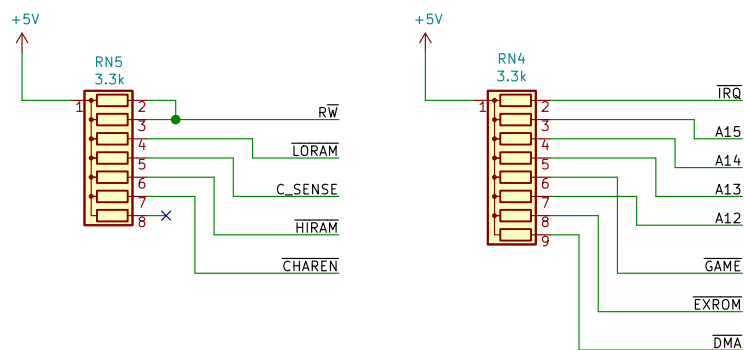
Id: 5/11

# EXPANSION PORT

# CPU



## Pull-up resistors



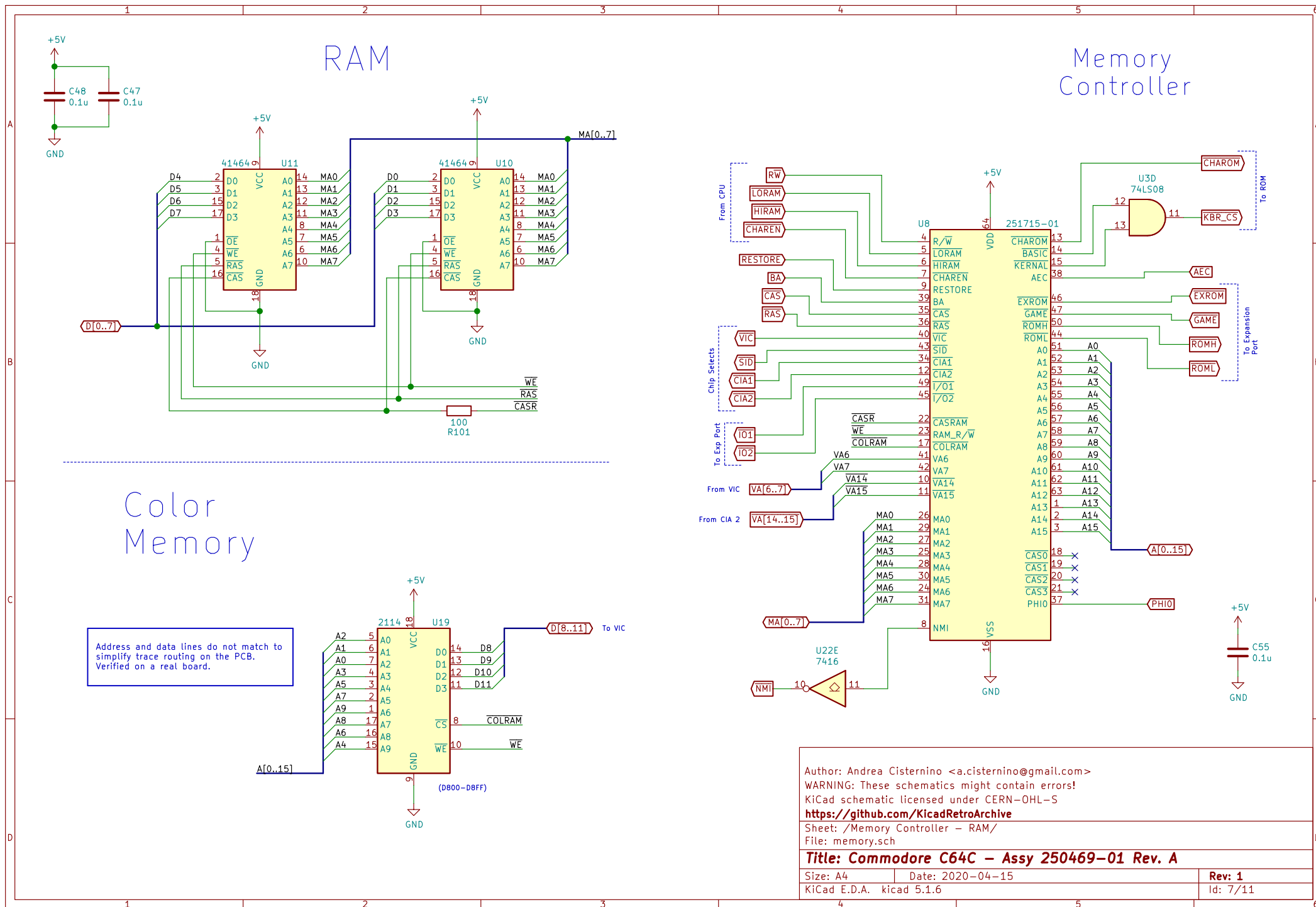
Author: Andrea Cisternino <a.cisternino@gmail.com>  
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Sheet: /CPU - Expansion Port/  
 File: cpu.sch

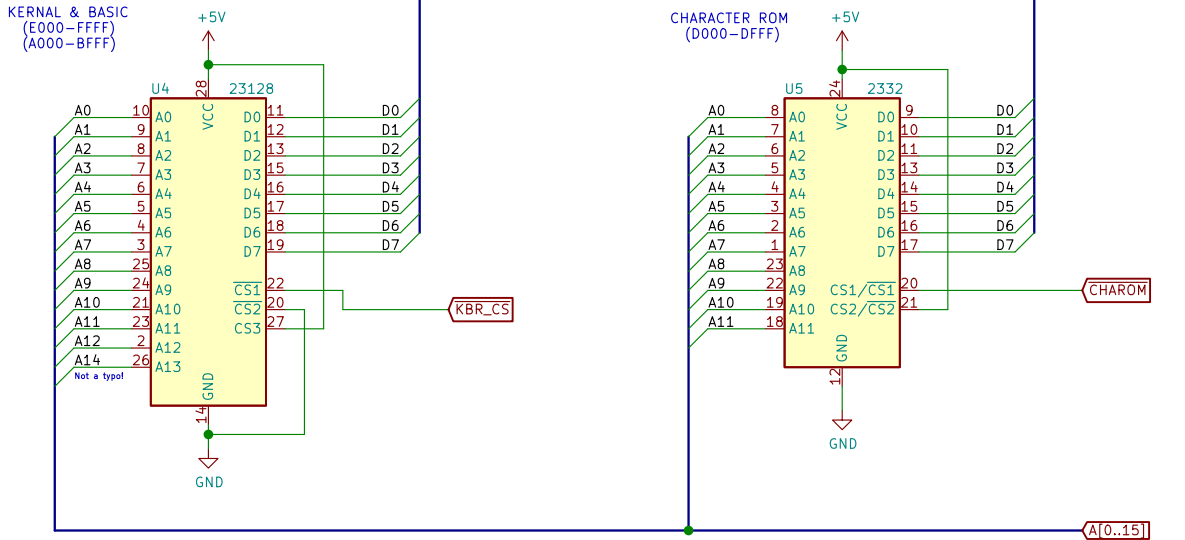
**Title: Commodore C64C - Assy 250469-01 Rev. A**

Size: A4 Date: 2020-04-15  
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 Id: 6/11



## ROM



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Sheet: /ROM/

File: rom.sch

**Title: Commodore C64C – Assy 250469-01 Rev. A**

|          |                  |
|----------|------------------|
| Size: A4 | Date: 2020-04-15 |
|----------|------------------|

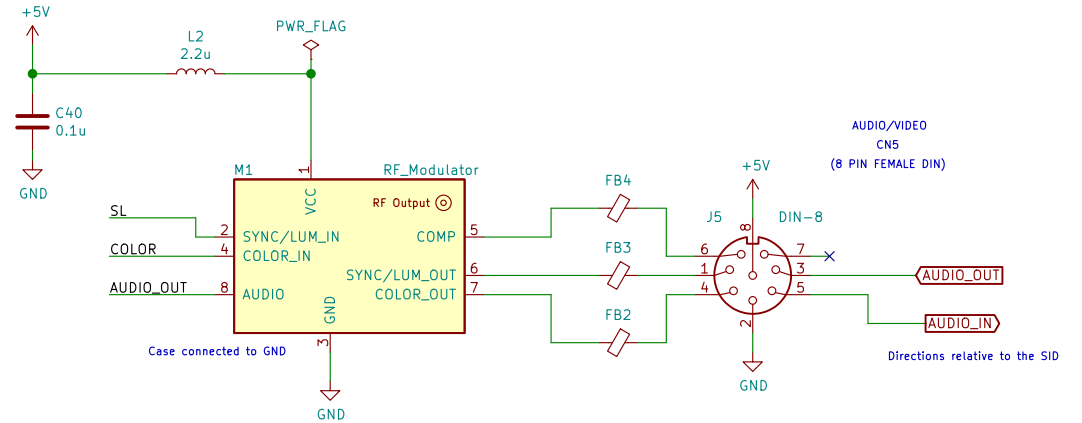
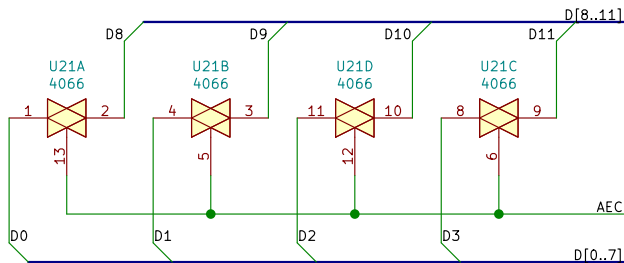
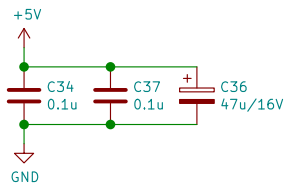
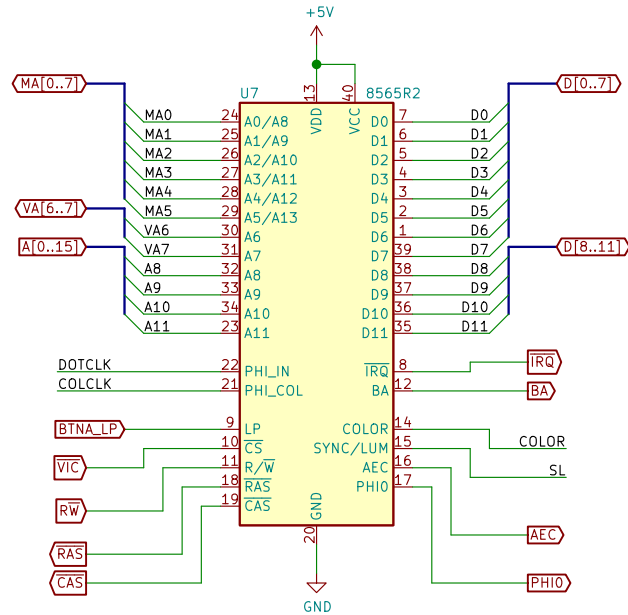
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|--------------|-------------|
| KiCad E.D.A. | kicad 5.1.6 |
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Rev: 1

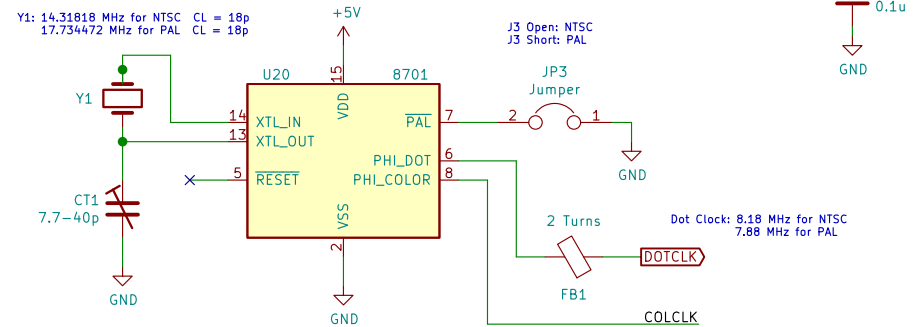
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## VIC 2



## CLOCK GEN



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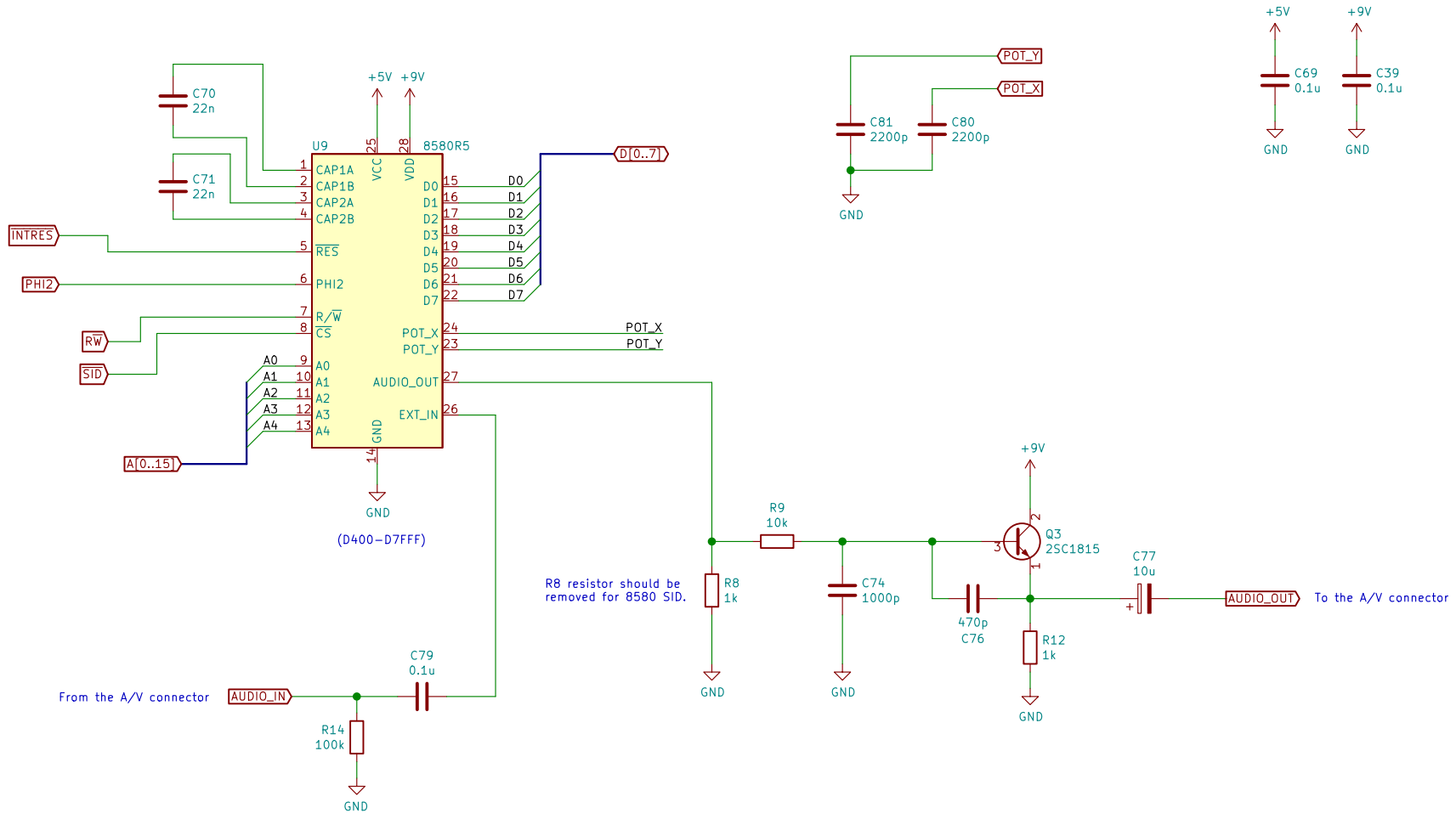
Sheet: /Video - ClockGen/  
 File: video.sch

**Title: Commodore C64C - Assy 250469-01 Rev. A**

Size: A4 Date: 2020-04-15  
 KiCad E.D.A. kicad 5.1.6

Rev: 1  
 Id: 9/11

# SID



Author: Andrea Cisternino <a.cisternino@gmail.com>  
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Sheet: /Audio/  
 File: audio.sch

**Title: Commodore 64C – Assy 250469–01 Rev. A**

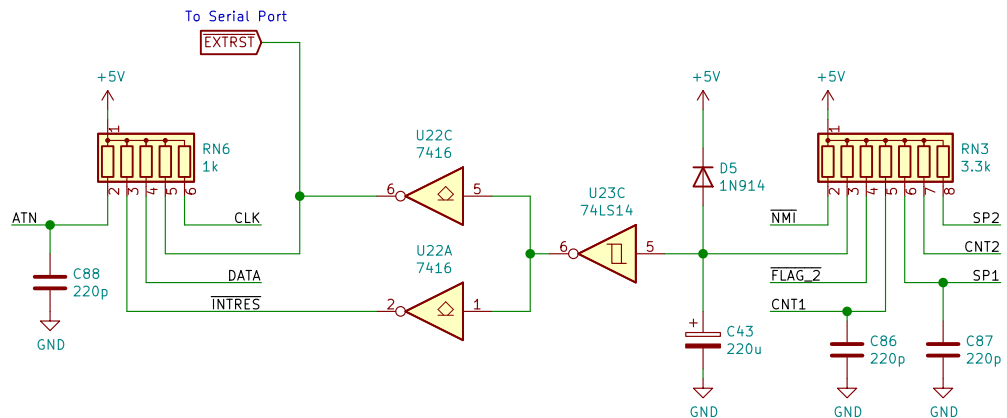
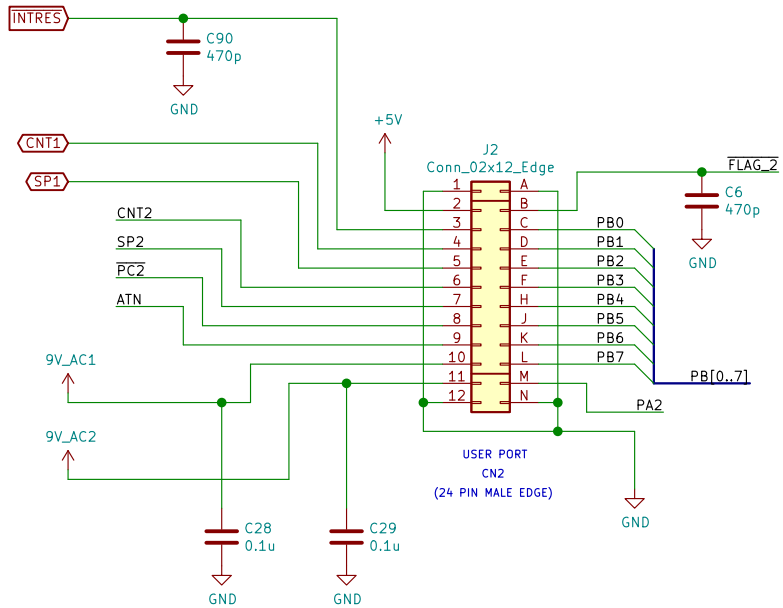
Size: A4 Date: 2020–04–15

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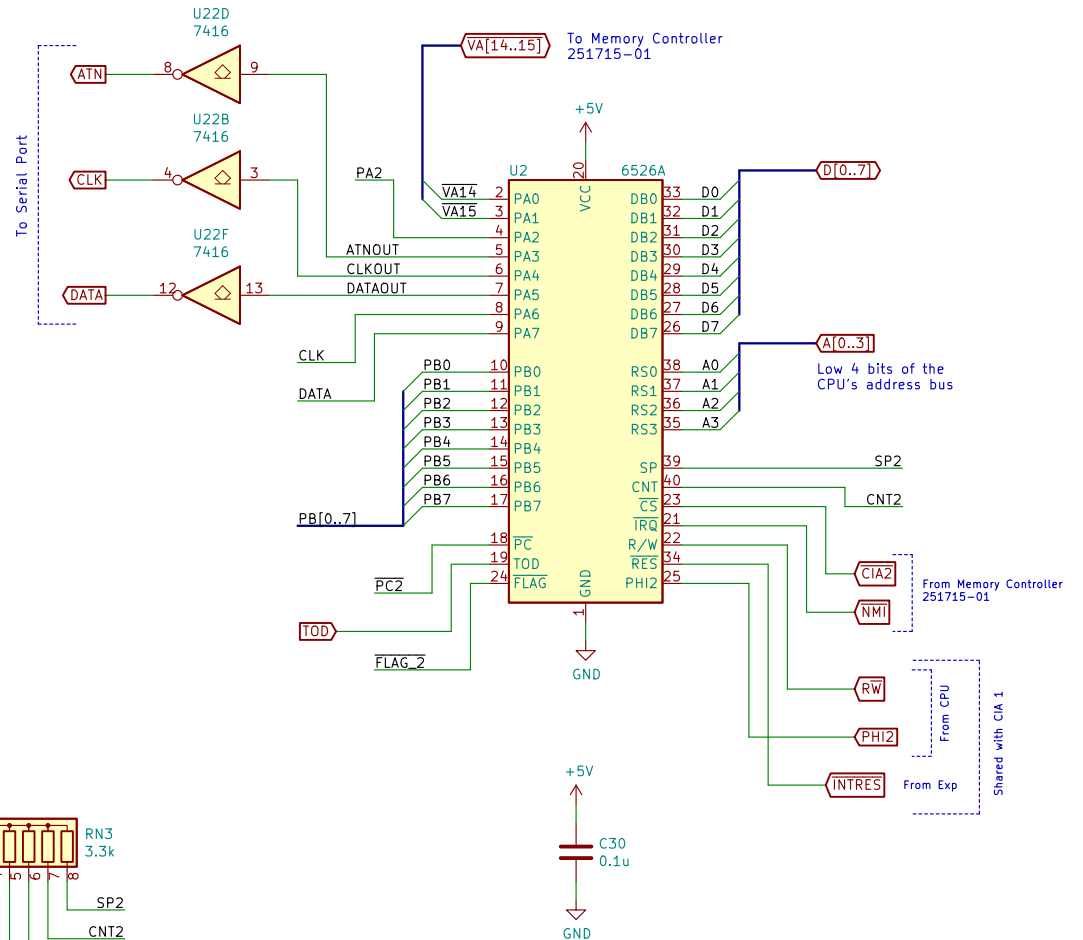
Rev: 1

Id: 10/11

# USER PORT



CIA 2  
(DD00-DDFF)



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Sheet: /User Port – CIA 2/  
File: user\_port.sch

**Title: Commodore C64C - Assy 250469-01 Rev. A**

|          |                  |
|----------|------------------|
| Size: A4 | Date: 2020-04-15 |
|----------|------------------|

KiCad E.D.A. kicad 5.1.6

Rev: 1

Id: 11/11