

FRABUSD9

—DABUS[15..0]

♦DBUS[7..0]

→DFRABUSD[9..0]

ABUS0

ABUS2

ABUS4

ABUS5

ABUS6

ABUS7

ABUS8

ABUS9

ABUS10

ABUS11

ABUS12

ABUS13

ABUS14

ABUS15

DBUS0

DBUS1

DBUS2

DBUS3

DBUS4

DBUS5

DBUS7

FRABUSDO

FRABUSD1

FRABUSD2

FRABUSD3 FRABUSD4 FRABUSD5

FRABUSD6

FRABUSD7

DBUS6

Not sure what's going on with UD1. Is that chip shared on another page?

ROM3 / 1 Mb llgs Redrawn by james@baldengineer

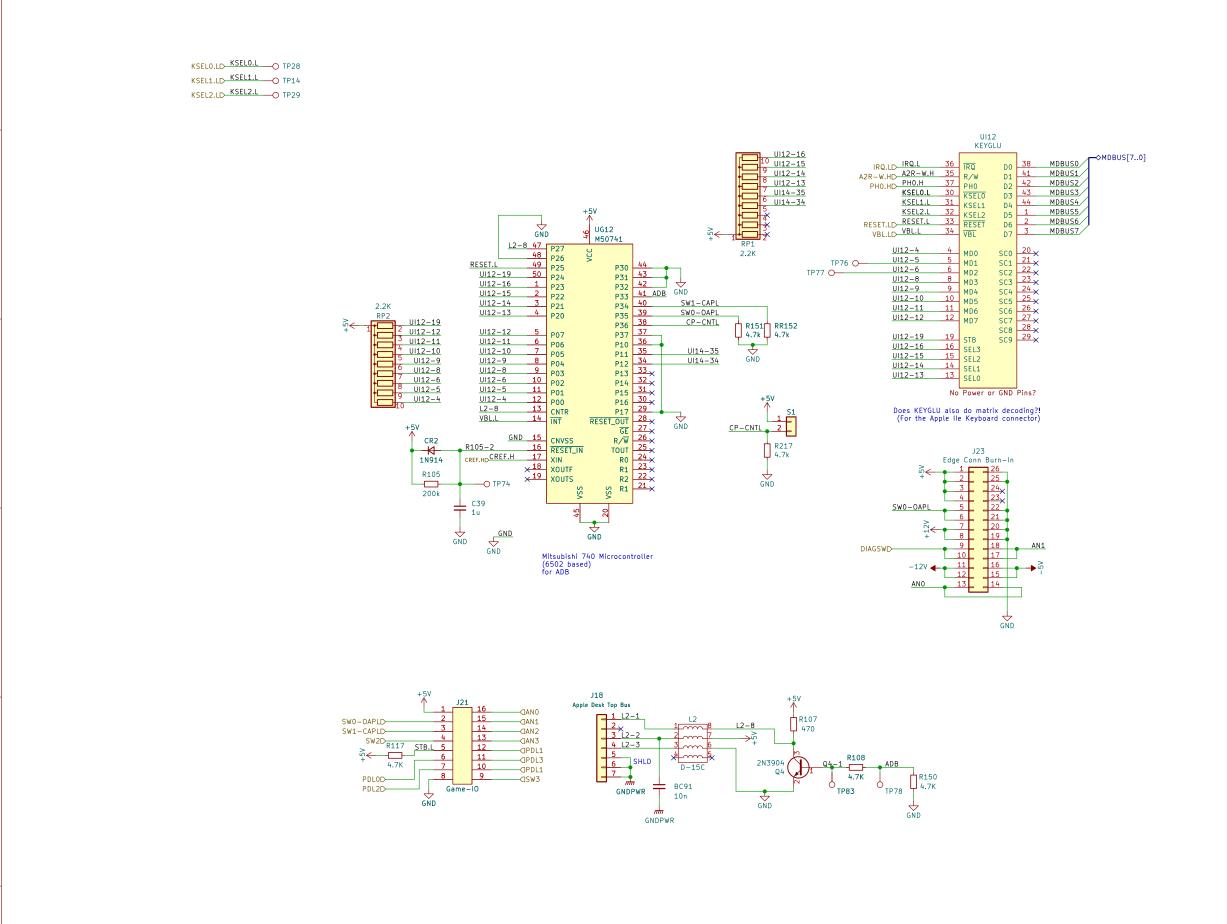
bald.ee/bitpreserve Sheet: /CYA-Mem-1/

Title: CPU and Fast Memory Controlle per source hardware

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Size: A3 Date: 2023-01-28 Rev: 2 KiCad E.D.A. kicad (6.0.11)



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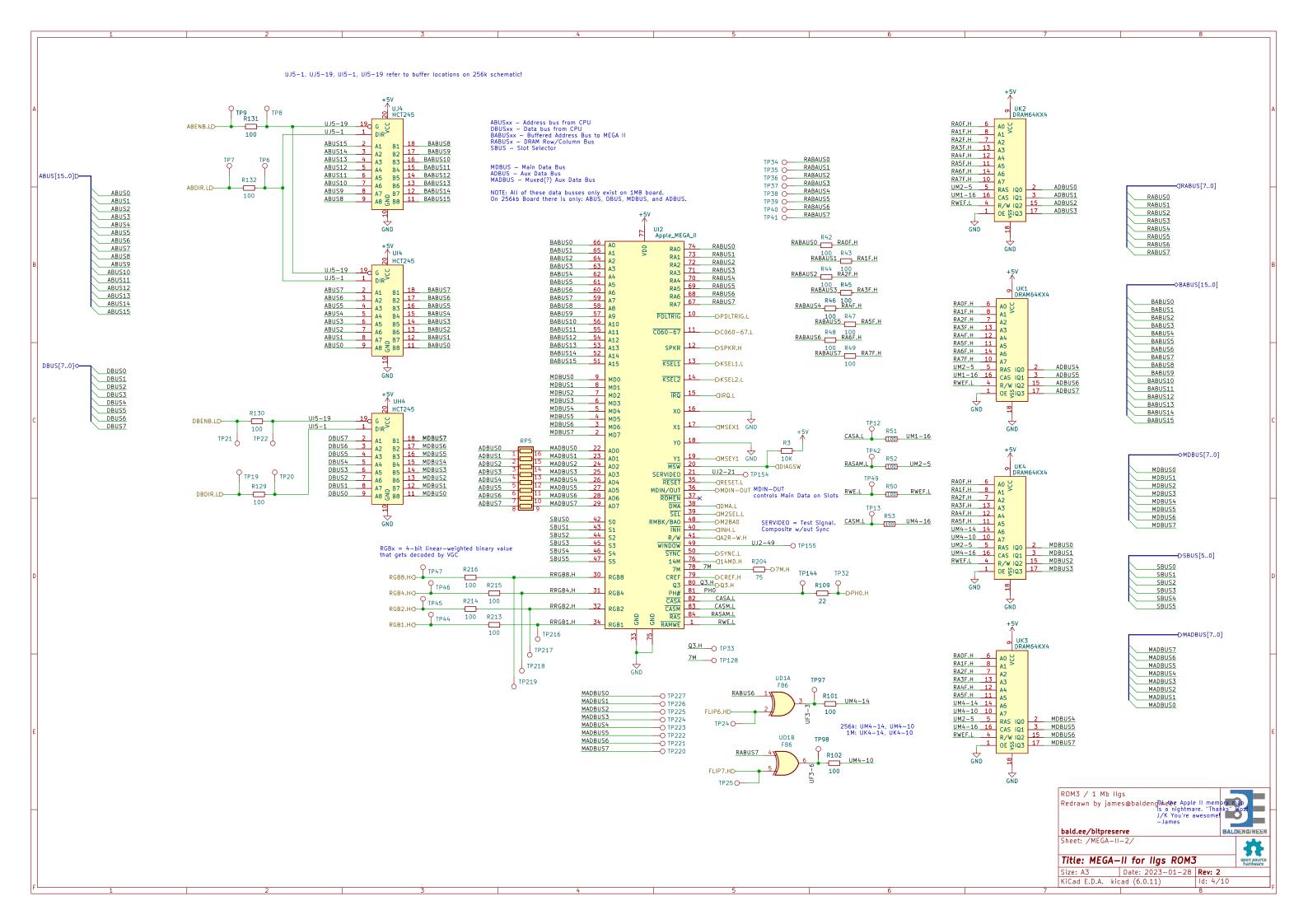
Sheet: /ABD, KEYGLU, Burn Edge, Gameport/

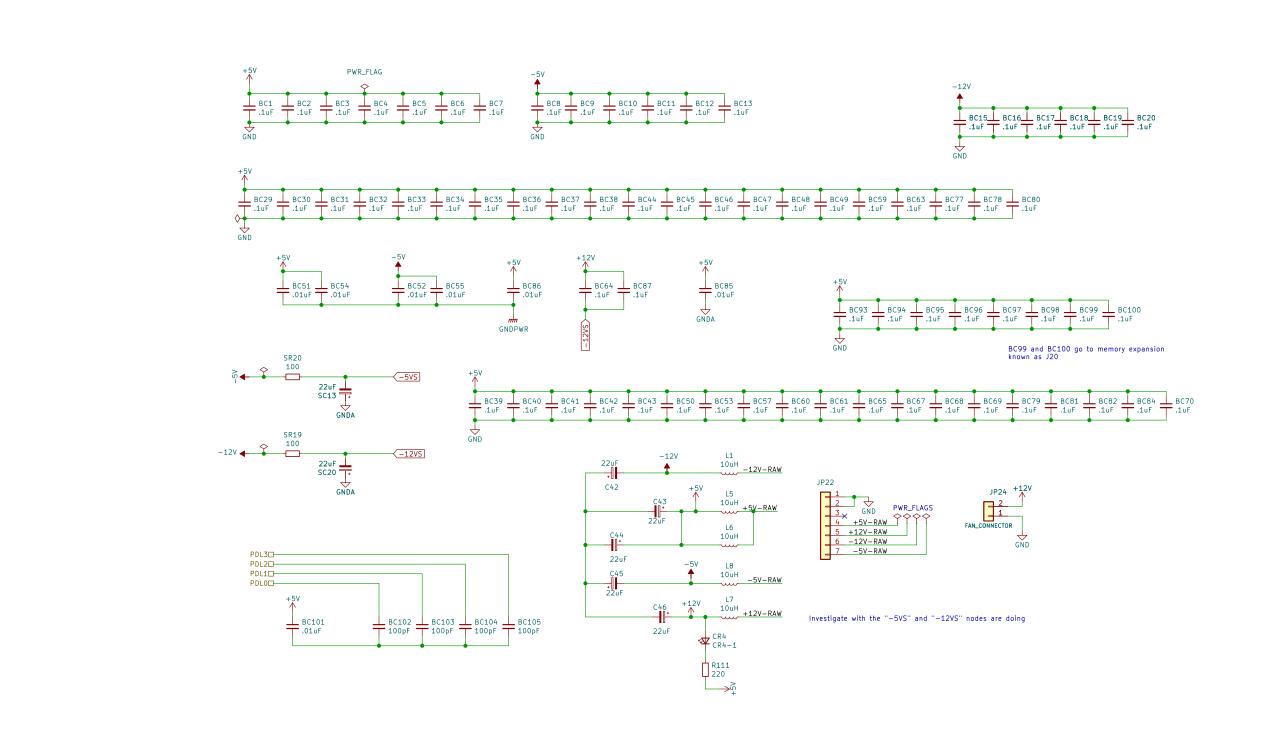
Title: UG12, Keyboard GLU, Burn-In, ppen source port

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Sheet: /Capacitors-6/

Title: Capacitors (and Power Connecton per hardware

3

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