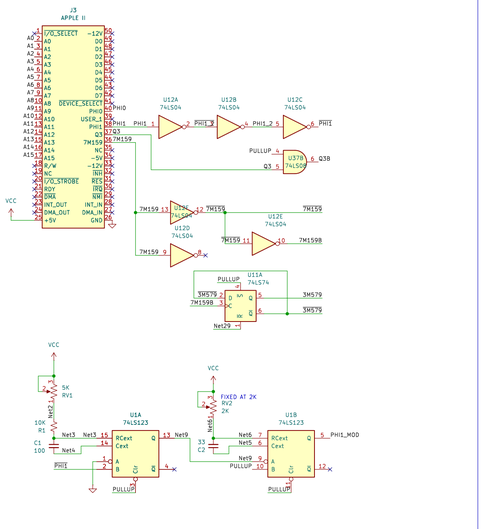
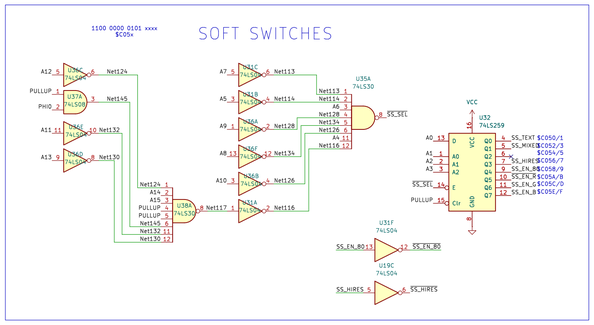
BUS CONNECTOR AND CLOCK GENERATION

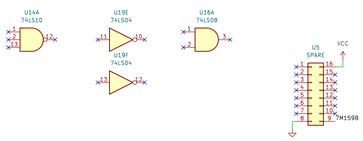


SOFT SWITCHES

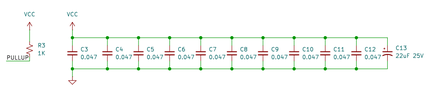
1100 0000 0101 xxxx
$C05x



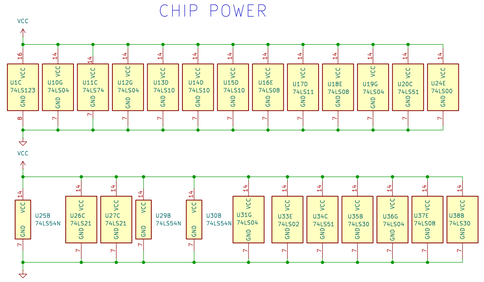
UNUSED GATES



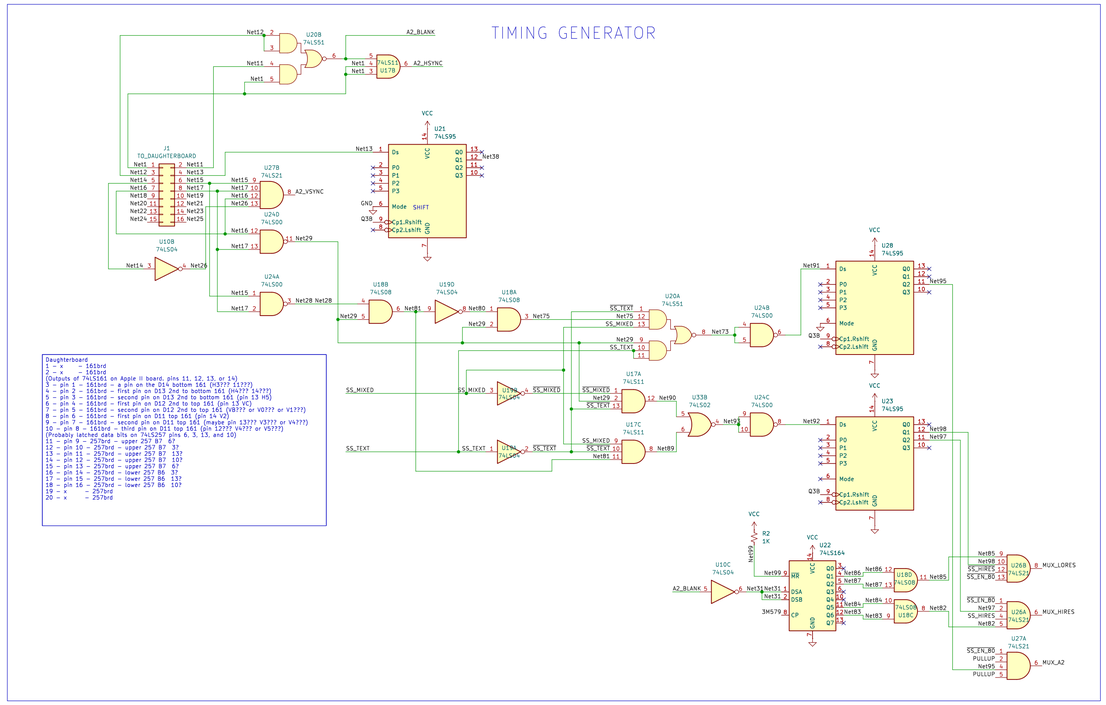
BYPASS CAPS



CHIP POWER

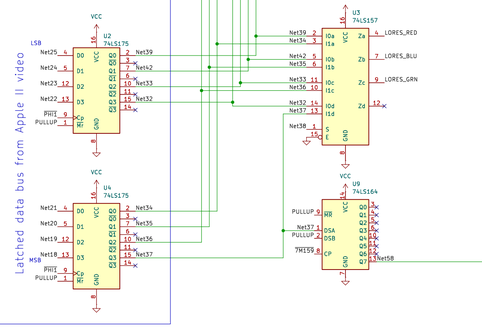


TIMING GENERATOR

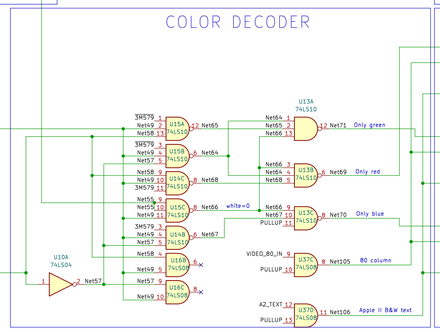


Daughterboard
1 – x – 161brd
2 – x – 161brd
(Outputs of 74LS161 on Apple II board, pins 11, 12, 13, or 14)
3 – pin 1 – 161brd – a pin on the D14 bottom 161 (HS??? (1???)
4 – pin 2 – 161brd – first pin on D13 2nd to bottom 161 (H4??? 14???)
5 – pin 3 – 161brd – second pin on D13 2nd to bottom 161 (pin 13 H5)
6 – pin 4 – 161brd – first pin on D12 2nd to top 161 (V8??)
7 – pin 5 – 161brd – second pin on D12 2nd to top 161 (V8??? or V3??? or V1???)
8 – pin 6 – 161brd – first pin on D11 top 161 (pin 14 V2)
9 – pin 7 – 161brd – second pin on D11 top 161 (maybe pin 13??? V3??? or V4???)
10 – pin 8 – 161brd – third pin on D11 top 161 (pin 12??? V4??? or V5???)
(Probably latched data bits on 74LS257 pins 6, 3, 13, and 10)
11 – pin 9 – 257brd – upper 257 B7 67
12 – pin 10 – 257brd – upper 257 B7 37
13 – pin 11 – 257brd – upper 257 B7 127
14 – pin 12 – 257brd – upper 257 B7 107
15 – pin 13 – 257brd – upper 257 B7 67
16 – pin 14 – 257brd – lower 257 B6 37
17 – pin 15 – 257brd – lower 257 B6 137
18 – pin 16 – 257brd – lower 257 B6 107
19 – x – 257brd
20 – x – 257brd

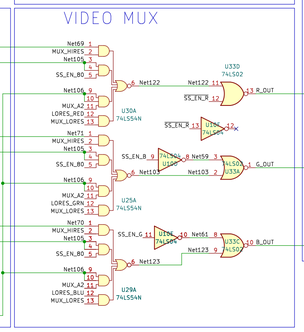
VIDEO DATA LATCH



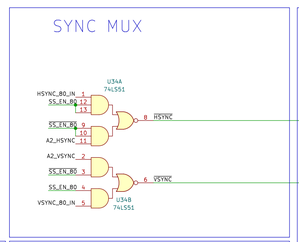
COLOR DECODER



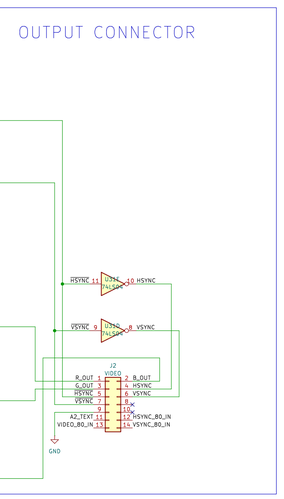
VIDEO MUX



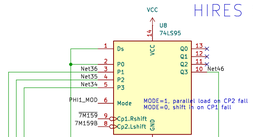
SYNC MUX



OUTPUT CONNECTOR



HIRES SHIFT REG



AMDEK DVM CARD
Reversed by Tube Time
Sheet: /
File: AmdekDVM.kicad_sch
Title:
Size: A1
Date:
Rev:
KiCad E.D.A. 9.0.1
Id: 1/1