

clk
reset
pc[31:0]
instr[31:0]
aluout[31:0]

writedata[31:0]

™ memwrite
 readdata[31:0]

	40,000 ps	45,000 ps	50,000 ps	55,000 ps	60,000 ps	65,000 ps	70,000 ps	75,000 ps	80
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	85,000 ps	90,000 ps	95,000 ps	100,000 ps	105,000 ps	110,000 ps	115,000 ps	120,000 ps	1:
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	00005		0007	0000	0004		000b	00000008	\perp
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_1 1 1	130,000 ps	135,000 ps	140,000 ps	145,000 ps	150,000 ps	155,000 ps	160,000 ps	165,000 ps 1
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00000003	0000	0007		0000	0000		0000000ь	
				2000000				
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	175,000 ps	180,000 ps	185,000 ps	190,000 ps	195,000 ps	200,000 ps	205,000 ps	210,000 ps
0000	0007	0000	0050	0000	0058	0000	0050	0000005a
0000	0005	0000	0007		efe0	0000	X	000000c
	XXXX	XXXX		XXXX	XXXX	0000	0007	efe00000

	220,000 ps	225,000 ps	230,000 ps	235,000 ps	240,000 ps	245,000 ps	250,000 ps	255,000 ps
0000005a	X		0000	001a			0000	0050
000000c	X		0000	0000			0000	0007
efe00000	X		XXXX	XXXX			0000	0007

	265,000 ps	270,000 ps	275,000 ps	280,000 ps	285,000 ps	290,000 ps	295,000 ps	300,000 ps
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				7000000				
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