Device 0xF11401, Konfig Ch1 Offset 0, Address 11111111

14:49:57.989 -> -> 10 3E A0 01 FFD155 F11401 01 05 00 00 00 00 01 - 35479

14:49:58.131 -> <- 0A 3E 80 02 F11401 FFD155 00 - 35594

14:49:58.178 -> -> 19 47 A0 01 FFD155 F11401 01 08 02 31 03 31 04 31 05 31 06 31 07 31 08 31 - 35639

01: 8 ??, 02 … 08: 7 \* 0x31 = „1“

14:49:58.274 -> <- 0A 47 80 02 F11401 FFD155 00 - 35756

14:49:58.320 -> -> 0D 50 A0 01 FFD155 F11401 01 08 09 31 - 35784

02 : 1 \* 0x31 = „1“

14:49:58.416 -> <- 0A 50 80 02 F11401 FFD155 00 - 35905

14:49:58.462 -> -> 0B 59 A0 01 FFD155 F11401 01 06 - 35934

14:49:58.557 -> <- 0A 59 82 02 F11401 FFD155 00 - 36055

Konfig Ch1 Offset 3.3 (=> 0x21), Address 12345678

Konfig Ch2 Offset 4.4 (=> 0x2C), Address 0ABCDEF1

15:27:11.351 -> -> 10 66 A0 01 FFD155 F11401 02 05 00 00 00 00 01 - 102156

15:27:11.492 -> <- 0A 66 80 02 F11401 FFD155 00 - 102273

15:27:11.539 -> -> 19 6F A0 01 FFD155 F11401 02 08 01 2C 02 30 03 41 04 42 05 43 06 44 07 45 - 102318

15:27:11.633 -> <- 0A 6F 80 02 F11401 FFD155 00 - 102434

15:27:11.681 -> -> 0F 78 A0 01 FFD155 F11401 02 08 08 46 09 31 - 102465

15:27:11.821 -> <- 0A 78 80 02 F11401 FFD155 00 - 102584

15:27:11.821 -> -> 0B 01 A0 01 FFD155 F11401 02 06 - 102610

15:27:11.962 -> <- 0A 01 82 02 F11401 FFD155 00 - 102733

15:27:11.962 -> -> 10 0A A0 01 FFD155 F11401 01 05 00 00 00 00 01 - 102768

15:27:12.104 -> <- 0A 0A 80 02 F11401 FFD155 00 - 102883

15:27:12.104 -> -> 0D 13 A0 01 FFD155 F11401 01 08 01 21 - 102914

15:27:12.245 -> <- 0A 13 80 02 F11401 FFD155 00 - 103032

15:27:12.293 -> -> 0B 1C A0 01 FFD155 F11401 01 06 - 103061

15:27:12.435 -> <- 0A 1C 82 02 F11401 FFD155 00 - 103184