V&V-Plan: OBED.ui

Version: V2.4
Datum: 2026-02-27
Erstellt für: Quality Gate / PEP Process

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1. Requirements Traceability Matrix

Requirements

Code	Name	State	Tags	Bemerkungen
REQ-00094377	Unterstützung DBET(E,A)-7x mit HVP-3.x	released	-	-
REQ-00095698	Neue Anzeige-Parameter für Anteile des Druckreglers	released	-	-
REQ-00096011	Druckregler nicht einstellbar wenn nicht aktiv	released	-	-
REQ-00096012	App Publishing from three accounts worldwide	released	Legal ✓	-
REQ-00096180	Publishing von easy2connect in India	released	-	-

Features

Code	Name	State	Bemerkungen
FEAT-00097119	Valve screen "Pressure Controller"	released	-
FEAT-00097120	Valve screen "Pressure Command Value"	released	-
FEAT-00097121	Valve screen "Pressure Sensor"	released	-
FEAT-00096845	New display parameters for pressure controller components	released	-
FEAT-00096828	Pressure controller parameterization should always be possible	released	-
FEAT-00096736	country-dependent legal screens	released	-
FEAT-00096814	Country specific legal texts for India	released	-

Test Specifications

Code	Name	State	Bemerkungen
TST-00097216	FEAT-00097119: Valve screen "Pressure Controller"	released	-
TST-00097217	FEAT-00097120: Valve screen "Pressure Command Value"	released	-
TST-00097218	FEAT-00097121: Valve screen "Pressure Sensor"	released	-

Code	Name	State	Bemerkungen
TST-00097192	New display parameters for pressure controller components	released	-
TST-00097193	Pressure controller parameterization should always be possible	released	-
TST-00096889	country-dependent legal screens	released	-
TST-00097206	Country specific legal texts for India	released	-

2. Vorgesehener Testumfang

Basis: 08.03.05 - Verification & Validation (V&V) - HydraulicDrive X - Docupedia

Level	V&V Measure / Scope	Geplanter Testumfang	Durchführung	Ergebnis	Nachweise	Anmerkung
I. Requirement Elicitation / System Validation	Acceptance Test	Kunden-/ Abnahmetest der Applikation	*TODO*	*TODO*	*TODO*	*TODO*
II. System Architecture / System Integration & Verification	Tests im Systemversuch	Tests mit realer Hardware im Systemversuch	*TODO*	*TODO*	*TODO*	*TODO*
III. Subsystem Architecture / Subsystem Integration & Verification	Software Function Test	Manuelle Tests nach Test-Spezifikationen	*TODO*	*TODO*	*TODO*	*TODO*
IV. Component Design and Implementation	Unit Test	Automatische Unit-Tests	*TODO*	*TODO*	*TODO*	*TODO*
IV. Component Design and Implementation	Static Code Analysis	Automatische Code-Analyse (z.B. SonarQube)	*TODO*	*TODO*	*TODO*	*TODO*
IV. Component Design and Implementation	Integration Test	Komponenten-Integrationstests	*TODO*	*TODO*	*TODO*	*TODO*
IV. Component Design and Implementation	E2E Test	End-to-End Testszenarien	*TODO*	*TODO*	*TODO*	*TODO*

3. Zusammenfassung

Requirements Coverage

- **Gesamt**: 5 Requirements, 7 Features, 7 Test-Spezifikationen
- released: 5

Kritische Requirements

- Legal: 1
- Security: 0
- Safety: 0

Test Execution

- **Geplante Tests**: 7
- **Durchgeführte Tests**: *TODO*
- **Bestandene Tests** (✓): *TODO*
- **Tests mit Befunden** (⚠): *TODO*
- **Fehlgeschlagene Tests** (✗): *TODO*

Quality Gate Status

⚪ **AUSSTEHEND** - Test-Ergebnisse müssen noch eingetragen werden

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Freigaben

Rolle	Name	Unterschrift	Datum
Gruppenleiter	_____	_____	___
QMM/EPQ	_____	_____	___