Network Working Group
Request for Comments: 12 REVISED

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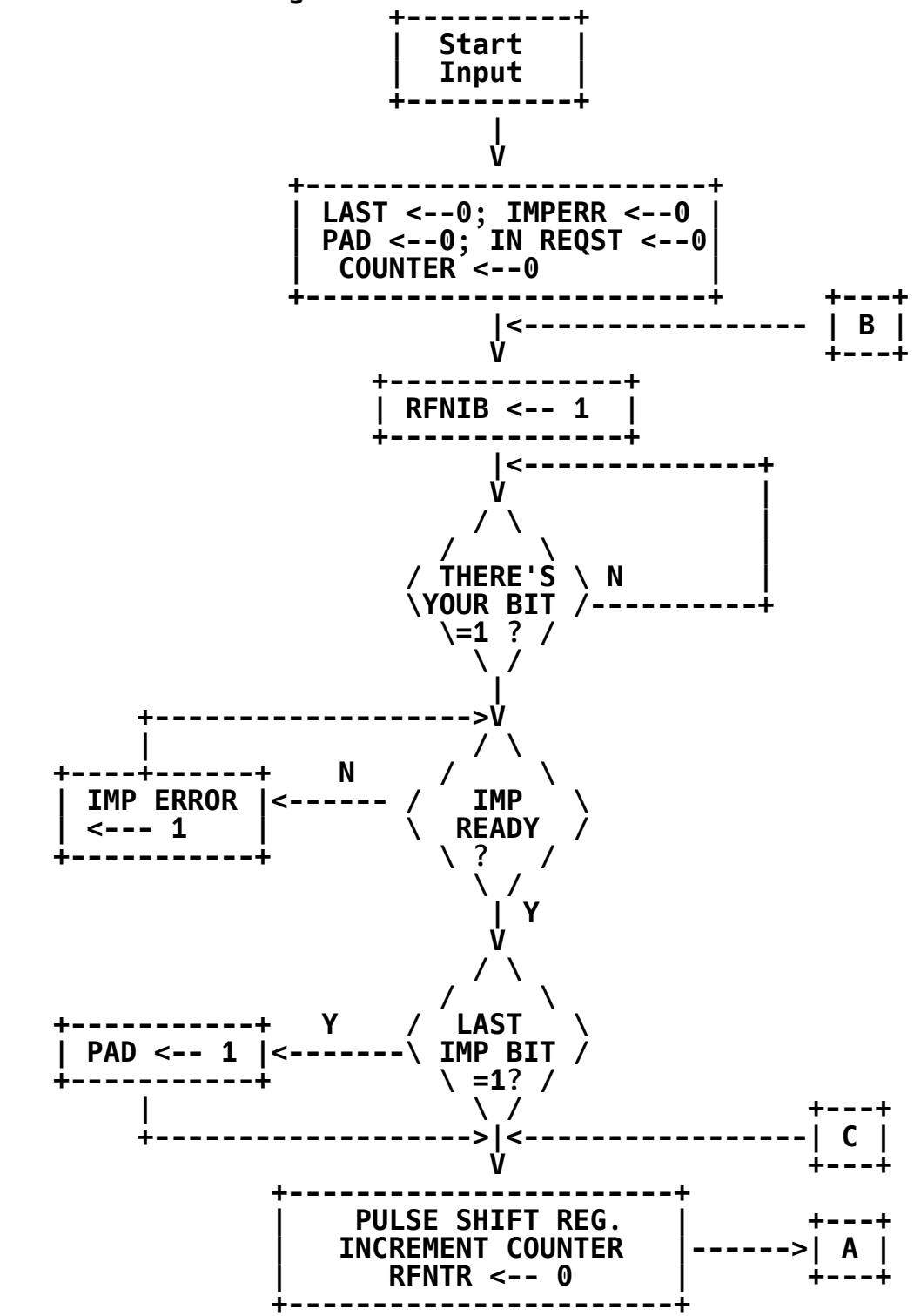
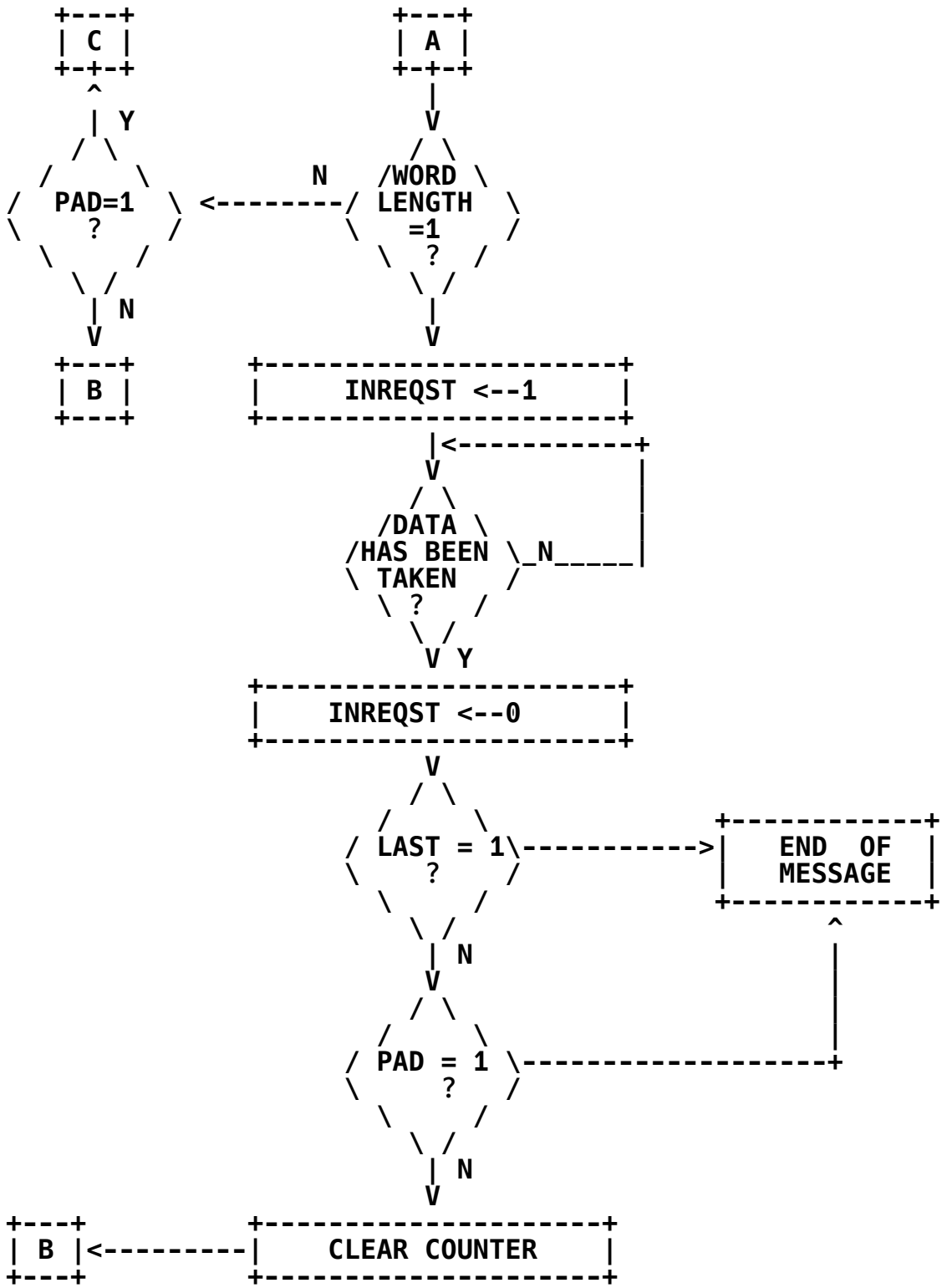
26 August 1969

IMP-HOST INTERFACE FLOW DIAGRAMS

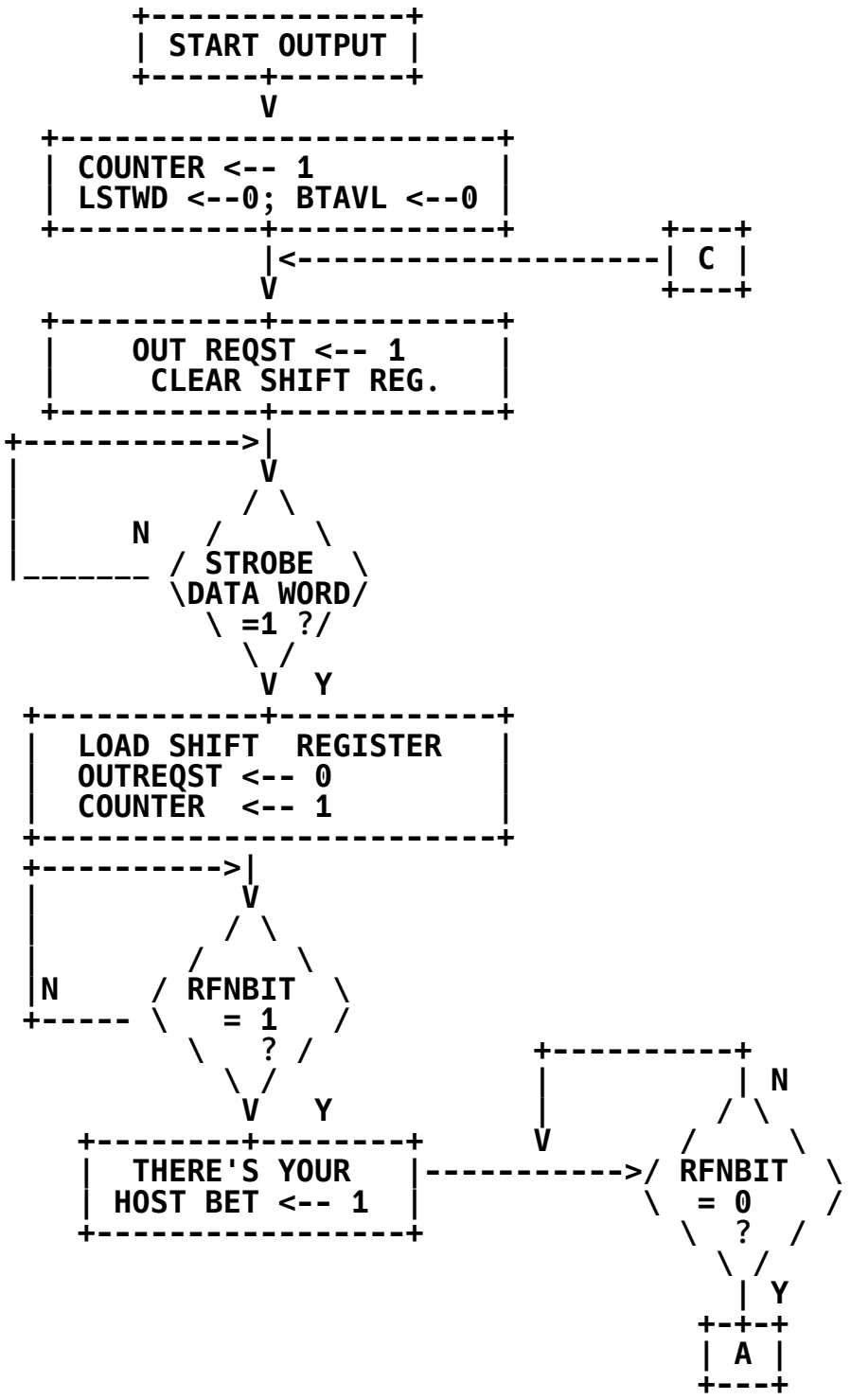
The following flow diagrams were extracted from the logic diagrams provided in Appendix B of BBN Report No. 1822. These diagrams indicate the logical sequence of hardware operations which occur within the IMP-HOST interface. The logic names appearing in the blocks correspond to the logic elements found in Appendix B.

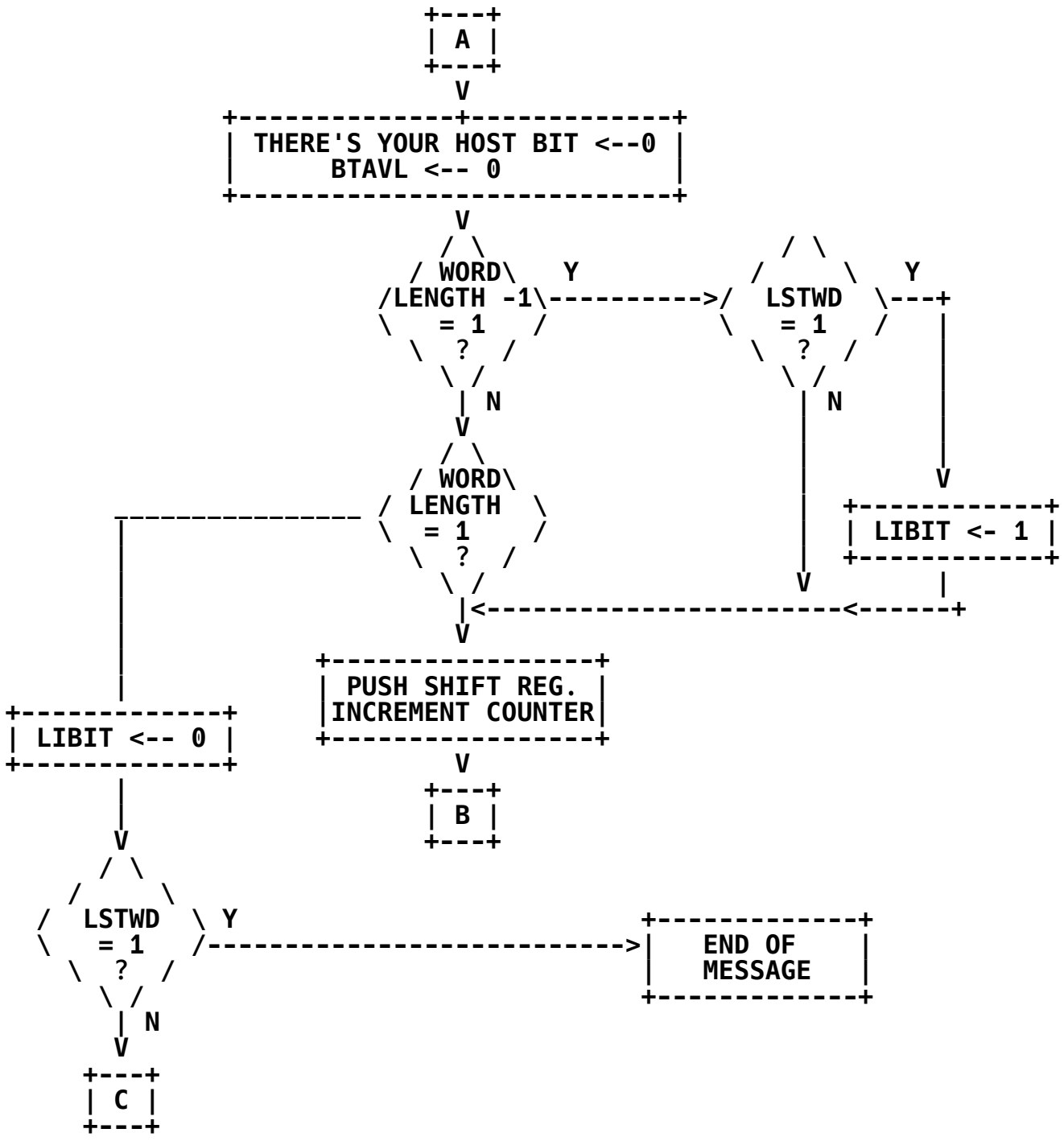
IMP to HOST Message




C
A
Y
N
PAD=1 ?
WORD LENGTH =1 ?
N
B
INREQST <--1
INREQST <--0
DATA HAS BEEN TAKEN ?
Y
LAST = 1 ?
END OF MESSAGE
N
PAD = 1 ?
N
B
CLEAR COUNTER

HOST TO IMP MESSAGE





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