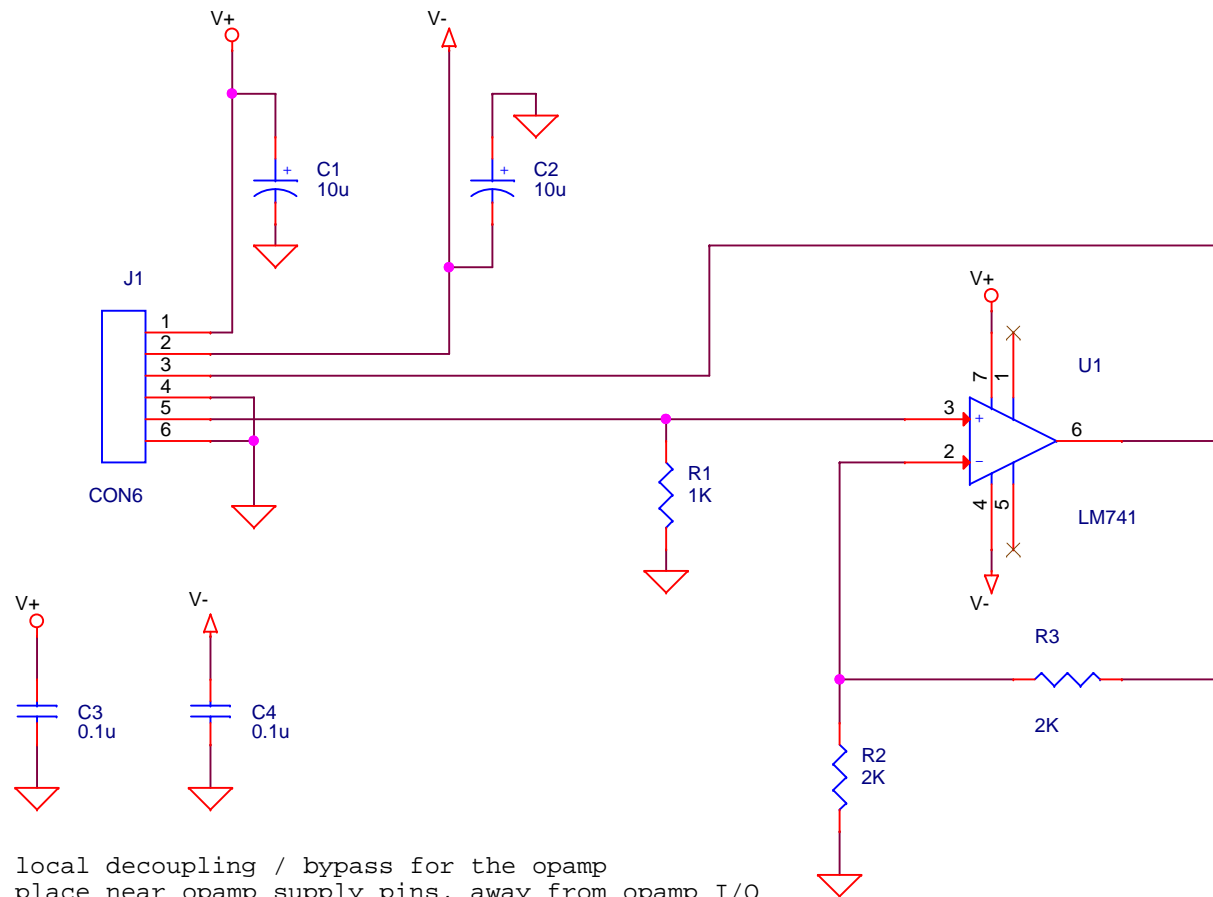


reservoir caps for the whole board
place near connector & away from opamp I/O



local decoupling / bypass for the opamp
place near opamp supply pins, away from opamp I/O

Notes on connector J1:

1. +12V for V+ rail
2. -12V for V- rail
3. opamp Vout, $= 2 \cdot V_{in}$
4. GND
5. opamp Vin, Vin needs to be within $\{-5.5V, 5.5V\}$
6. GND

Title		
Analog_Example1.dsn		
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