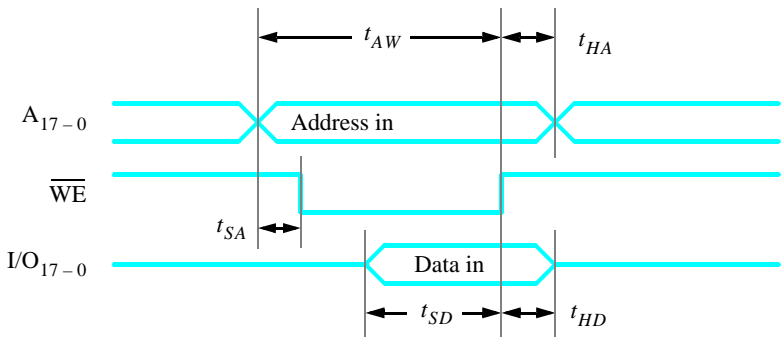


(a) SRAM read cycle timing



(b) SRAM write cycle timing