CPSC 305 Memory Simulator File 2

- Stefano C. Coronado, Melody Sepehrar
  - 7th October - 14th October – sporadically (both)
  - Week of 21st October - 28th October – sporadically (both)
  - 2019-11-01 – 1700 – 2100hrs – (both)

- We did not have the time to swap the simulator with another team

- Description of success on the computer simulation

  - We have completed every portion of the simulator itself… However, there are few caveats that keep this from working properly.

  - Stefano worked on the fetch-decode-execute portion aka step() by asking Gusty for the best way to comprehend how does the CPU module fetch from the overall memory_system module.

    - The shortfalls or consequences that occured is that for some reason, the opcode is not properly being detected by the fetched instruction … and therefore keeps the overall switch statement from properly running. We have tried debugging this by printing out the detected opcode as a standard int, trying to find out if any of the execute steps (LDI, MOV, etc) are going to run.

  - Melody assisted with writing the test cases that we hoped to run on the Simulator.

    - The shortfalls of this is that since we can't properly detect the opcodes, we do not really know if they work well.

  - Melody wrote much of the initial code for the Memory Simulator that was eventually improved upon by the both of us.

  - We think we should get an 80 – because we understand the concepts necessary to create the simulator, and except for a few bugs, we believe this is a fair assessment of our comprehension and abilities.