

# Jagan Prasath

Sr. Electrical Engineer

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R&D engineer well-known for creating positive workplace culture and high performing teams. Demonstrated service & quality expertise, including complex problem solving and Real time testing



## Work History

2023-06 -  
Current

### Sr. Electrical Engineer

Pravaig Dynamics, Bangalore

- Design PDU for battery packs with high voltage architecture consider safety factors including AIS standards.
- Selecting the right cell chemistry for the battery pack and creating a test model based on the use case of the vehicle.
- Component selection for the battery pack which includes Contactor's, Fuse, Connectors, etc.,.
- Communicating with BMS team and deciding the workcase scenario which will help in BMS development for battery pack.
- Assigning and managing the work with team members in an efficient way.
- Design modeling and verification with the mechanical team regarding the 3D models.
- Creating documents based on ISO standards and organizing them.
- Creating SOP for prototyping and verification of the process to improve the SOP in future.
- Getting the feedback from testing team to understand the shortcomings and overcome them in further design process.

2018-06 -  
2023-05

### R&D Engineer

Hulikkal Electro India Pvt Ltd, Coimbatore, India

- Collaborate with development team to develop battery, controller and Motor.
- Controller programming based on speed and control parameters.
- Test motor and controller current consumption with various load.
- Calculate the required power to obtain the desired range.
- Based on the range requirement battery pack capacity is calculated.
- After final product design based on size requirement and current requirement battery pack will be designed.
- After identifying maximum current and voltage appropriate BMS will be selected and failure test will be carried out.

- Test the E-Bike and troubleshoot, It is repeated until the final results.
- Documenting the entire process.



## Skills

High Voltage Architecture  
PDU Design  
Component selection for PDU design  
Cell selection  
HV Battery Pack Modeling  
MATLAB Simulation for Battery Pack  
PCAN



## Education

2014-06 - **B.E.: Electrical And Electronics Engineering**  
2018-05 Jai ShriRam Engineering College - Tiruppur