

Features

- · Excellent Stability and Uniformity
- High Dense Cell Design for Extremely Low RDS(ON)
- · Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

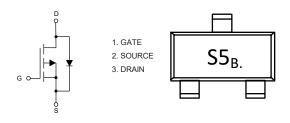
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance:90°C/W Junction to Ambient(Steady-State)(2)

Parameter	Symbol	Rating	Unit		
Drain-Source Voltage	V _{DS}	-20	V		
Gate-Source Volltage		V _{GS}	±10	V	
Continuous Drain Current	T _A =25°C		-4.2	Α	
	T _A =100°C	· I _D	-2.7		
Pulsed Drain Current ⁽³⁾	•	I _{DM}	-21	Α	
Total Power Dissipation ⁽⁴⁾		P_{D}	1.4	W	

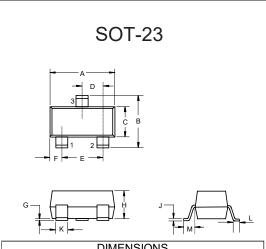
Note:

- Halogen free "Green" products are defined as those which contain <900ppm bromine,
 <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of $R_{\theta JA}$ is measured with the device mounted on $1in^2$ FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C. The Power dissipation P_{DSM} is based on $R_{\theta JA}$ t≤ 10s and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4. P_D is based on max. junction temperature, using junction to ambient thermal resistance.

Internal Structure and Marking Code

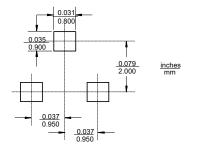


P-CHANNEL MOSFET



DIMENSIONS					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	NOTE
Α	0.110	0.120	2.80	3.04	
В	0.083	0.104	2.10	2.64	
С	0.047	0.055	1.20	1.40	
D	0.034	0.041	0.85	1.05	
Е	0.067	0.083	1.70	2.10	
F	0.018	0.024	0.45	0.60	
G	0.0004	0.006	0.01	0.15	
Н	0.035	0.043	0.90	1.10	
J	0.003	0.007	0.08	0.18	
K	0.012	0.020	0.30	0.51	
L	0.007	0.020	0.20	0.50	
М	0.022 REF		0.55	REF	

Suggested Solder Pad Layout



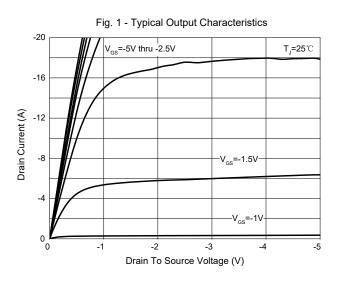


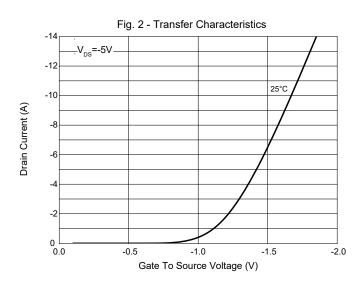
Electrical Characteristics @ 25°C (Unless Otherwise Specified)

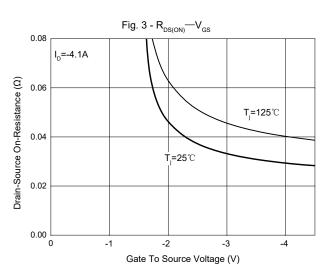
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics				1			
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250μA	-20			V	
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±10V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V			-1	μA	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-0.5		-0.9	V	
Drain-Source On-Resistance	R _{DS(on)}	V_{GS} =-4.5V, I_D =-4A		30	39	39 49 63	
		V _{GS} =-2.5V, I _D =-3A		38	49		
		V _{GS} =-1.8V, I _D =-2A		51	63		
Gate Resistance	R _g	F=1 MHz, Open drain		14.1		Ω	
Diode Characteristics							
Continuous Body Diode Current	Is				-4.2	Α	
Pulse Diode Forward Current	I _{SM}				-10		
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =-3.3A		-0.8	-1.2	V	
Reverse Recovery Time	t _{rr}	I _F =-4.1A, dI _F /dt=100A/μs		38		ns	
Reverse Recovery Charge	Q _{rr}	- 1 _F 4.1Λ, α1 _F /αι-100Λ/μ5		22		nC	
Dynamic Characteristics							
Input Capacitance	C _{iss}			821			
Output Capacitance	C _{oss}	V_{DS} =-4V, V_{GS} =0V,f=1MHz		149		pF	
Reverse Transfer Capacitance	C _{rss}			126			
Total Gate Charge	Q _g	V _{DS} =-4V,V _{GS} =-4.5V,I _D =-4.1A		8.58			
	Q _g	V _{DS} =-4V,V _{GS} =-2.5V,I _D =-4.1A		4.7			
Gate-Source Charge	Q _{gs}	V _{DS} =-4V,V _{GS} =-2.5V,I _D =-4.1A		1.2		nC	
Gate-Drain Charge	Q_{gd}	V _{DS} =-4V,V _{GS} =-2.5V,I _D =-4.1A		1.4			
Turn-On Delay Time	t _{d(on)}			8.6			
Turn-On Rise Time	t _r	V _{DD} =-4V, V _{GS} =-4.5V,		12			
Turn-Off Delay Time	t _{d(off)}	R_{GEN} =-4.5 Ω , I_D = -3.3 A		61		ns	
Turn-Off Fall Time	t _f			26		l	

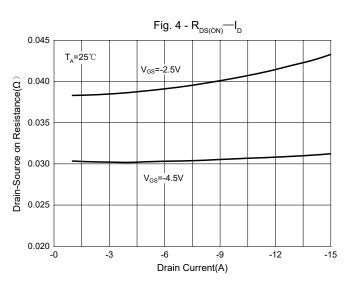


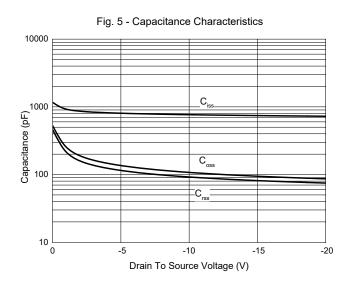
Curve Characteristics

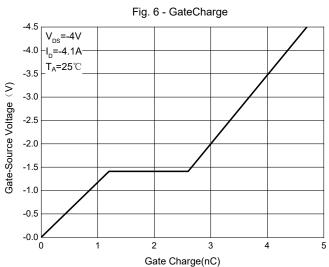






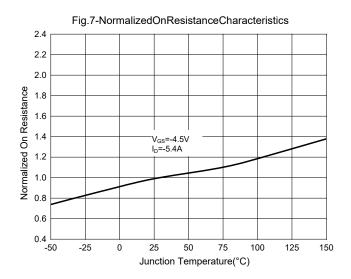


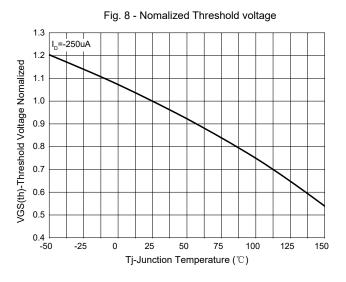


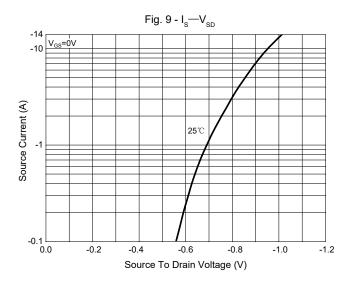


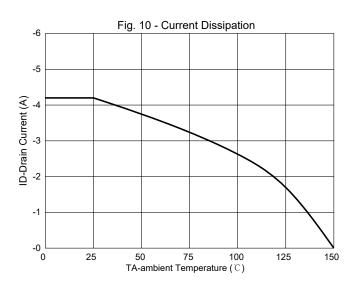


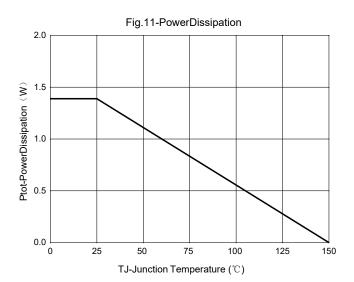
Curve Characteristics





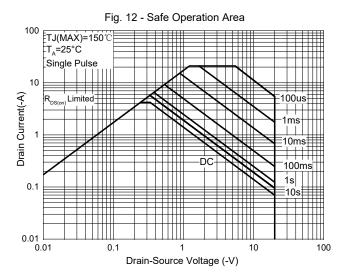








Curve Characteristics



10 D=T_{on}/T In descending order
D=0.5, 0.3, 0.1, 0.05, 0.02, 0.01, single pulse T_{J,PK}=T_A+P_{DM}·Z_{θJA}·R_{θJA} R_{θJA}=90°C/W Zth(J-A) Normalized Transient Thermal Resistance 0.1 0.01 1E-3 1E-4 1 111111 1E-6 1E-5 1E-4 1E-3 0.01 0.1 10 100 1000 Pulse Width (s)

Fig. 13 -Normalized Transient Thermal Impedance



Ordering Information

Device	Packing	
SI2305B-TP	Tape&Reel: 3Kpcs/Reel	
SI2305B-13P	Tape&Reel: 10Kpcs/Reel	

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