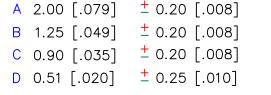
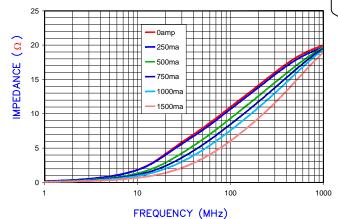
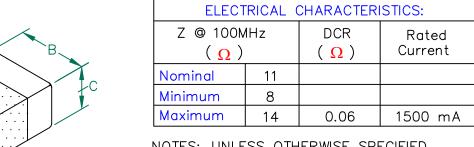
## MI0805K110R-10

#### PHYSICAL DIMENSIONS:



#### Z vs FREQUENCY IMPEDANCE UNDER DC BIAS





#### NOTES: UNLESS OTHERWISE SPECIFIED

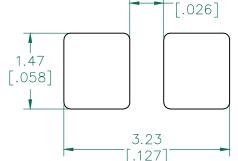
- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER TAPE.
- 2. TERMINATION FINISH IS 100% TIN.
- 3. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 4. OPERATING TEMP. RANGE: -40°C~+125°C. (INCLUDING SELF-HEATING)

#### LAND PATTERNS FOR REFLOW SOLDERING

# |Z| , R, AND X vs. FREQUENCY MPEDANCE

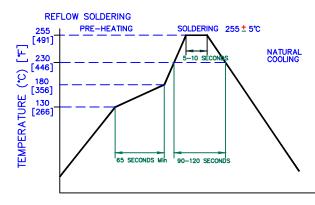
FREQUENCY (MHz)

AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 3227



(For wave soldering, add 0.762 [.030] to this dimension)

#### RECOMMENDED SOLDERING CONDITIONS



DIMENSIONS ARE IN mm [INCHES].				This print is the property of Laird					
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				copies shall be made without the					
				written consent of Laird Tech. A					
				rights to design or invention are					
Ε	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	reserved.					
				PROJECT/PART NUMBER:	П	REV	PART TO	PE:	DRAWN BY:
D	CHANGE TO PAPER TAPE	03/04/10	JUN	MI0805K110R-10	- 1	F	co-	-FIRE	тмв
С	UPDATE COMPANY LOGO	08/06/08	JRK		- 1	-	00	· iik	IMID
В	ADD ROHS & UPDATE COMPANY LOGO	08/28/07	JRK		SCAL	E:	_	SHEET:	•
Α	ORIGINAL DRAFT	04/05/04	ТМВ	lan #	TOOL	_		1	of 1
REV	DESCRIPTION	DATE	INT	MI0805K110R-10-E			-	•	0. 1

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