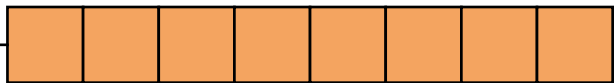
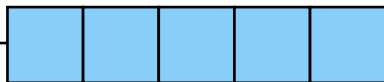


Processor x



Processor y



Processor x

