Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFC0	
FFFF FFC4	
FFFF FFC8	
FFFF FFCC	
FFFF FFD0	
FFFF FFD4	
FFFF FFD8	
FFFF FFDC	
FFFF FFEO	
FFFF FFE4	
FFFF FFE8	
FFFF FFEC	
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	
FFFF FFFC	

Instruction: 92 mov sp, #0
The address that **SP** points to

	\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \	
Address	Value	
FFFF FFA0		
FFFF FFA4		
FFFF FFA8		
FFFF FFAC		
FFFF FFB0		
FFFF FFB4		
FFFF FFB8		
FFFF FFBC		
FFFF FFC0		
FFFF FFC4		
FFFF FFC8		
FFFF FFCC		
FFFF FFD0		
FFFF FFD4		
FFFF FFD8		
FFFF FFDC		
FFFF FFE0		
FFFF FFE4		
FFFF FFE8		
FFFF FFEC		
FFFF FFFO		
FFFF FFF4		
FFFF FFF8	0xFFFFFFC0	
FFFF FFFC	148	

Instruction: 104 push {fp}

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFC0	
FFFF FFC4	
FFFF FFC8	
FFFF FFCC	
FFFF FFD0	
FFFF FFD4	
FFFF FFD8	
FFFF FFDC	
FFFF FFEO	
FFFF FFE4	
FFFF FFE8	
FFFF FFEC	
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	
FFFF FFFC	

Instruction: 96 mov fp, 0xfffffc0

## The address that **FP** points to

	_
Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFC0	
FFFF FFC4	
FFFF FFC8	
FFFF FFCC	
FFFF FFD0	
FFFF FFD4	
FFFF FFD8	
FFFF FFDC	
FFFF FFEO	
FFFF FFE4	
FFFF FFE8	
FFFF FFEC	
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148

Instruction: 108 add fp, sp, #4

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFC0	
FFFF FFC4	
FFFF FFC8	
FFFF FFCC	
FFFF FFD0	
FFFF FFD4	
FFFF FFD8	
FFFF FFDC	
FFFF FFEO	
FFFF FFE4	
FFFF FFE8	
FFFF FFEC	
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	
FFFF FFFC	148
: 40	0 (1)

Instruction: 100 push {Ir}

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFC0	
FFFF FFC4	
FFFF FFC8	
FFFF FFCC	
FFFF FFD0	
FFFF FFD4	
FFFF FFD8	
FFFF FFDC	
FFFF FFE0	
FFFF FFE4	
FFFF FFE8	
FFFF FFEC	
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148
I.a. a.t a.t.! aa 4.4	2   "

Instruction: 112 sub sp, sp, #8

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFC0	
FFFF FFC4	
FFFF FFC8	
FFFF FFCC	
FFFF FFD0	
FFFF FFD4	
FFFF FFD8	
FFFF FFDC	
FFFF FFE0	
FFFF FFE4	
FFFF FFE8	
FFFF FFEC	124
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148

Instruction: 4 push {Ir}

\ ddrocc	Value	
Address	Value	
FFFF FFA0		
FFFF FFA4		
FFFF FFA8		
FFFF FFAC		
FFFF FFB0		
FFFF FFB4		
FFFF FFB8		
FFFF FFBC		
FFFF FFC0		
FFFF FFC4		
FFFF FFC8		
FFFF FFCC		
FFFF FFD0		
FFFF FFD4		
FFFF FFD8		
FFFF FFDC		
FFFF FFEO		
FFFF FFE4		
FFFF FFE8	FFFF FFFC	
FFFF FFEC	124	
FFFF FFFO		
FFFF FFF4		
FFFF FFF8	0xFFFFFFC0	
FFFF FFFC	148	

Instruction: 16 sub sp, sp, #8

	T		
Address	Value		
FFFF FFAO			
FFFF FFA4			
FFFF FFA8			
FFFF FFAC			
FFFF FFB0			
FFFF FFB4			
FFFF FFB8			
FFFF FFBC			
FFFF FFC0			
FFFF FFC4			
FFFF FFC8			
FFFF FFCC			
FFFF FFD0			
FFFF FFD4			
FFFF FFD8			
FFFF FFDC			
FFFF FFEO			
FFFF FFE4			
FFFF FFE8	FFFF FFFC		
FFFF FFEC	124		
FFFF FFF0			
FFFF FFF4			
FFFF FFF8	0xFFFFFFC0		
FFFF FFFC	148		
Instruction: 9 puch (fp)			

Instruction: 8 push {fp}

	1	
Address	Value	
FFFF FFAO		
FFFF FFA4		
FFFF FFA8		
FFFF FFAC		
FFFF FFB0		
FFFF FFB4		
FFFF FFB8		
FFFF FFBC		
FFFF FFC0		
FFFF FFC4		
FFFF FFC8		
FFFF FFCC		
FFFF FFD0		
FFFF FFD4		
FFFF FFD8		
FFFF FFDC		
FFFF FFEO		
FFFF FFE4	4	
FFFF FFE8	FFFF FFFC	
FFFF FFEC	124	
FFFF FFFO		
FFFF FFF4		
FFFF FFF8	0xFFFFFFC0	
FFFF FFFC	148	

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFC0	
FFFF FFC4	
FFFF FFC8	
FFFF FFCC	
FFFF FFD0	
FFFF FFD4	
FFFF FFD8	
FFFF FFDC	
FFFF FFEO	
FFFF FFE4	
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFF0	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148
In atminations, 12	addfo oo HA

Instruction: 12 add fp, sp, #4

Address	Value	Addr	ess Value	
FFFF FFA0		FFFF F	FFFF FFAO	
FFFF FFA4		FFFF F	FFFF FFA4	
FFFF FFA8		FFFF F	FFFF FFA8	
FFFF FFAC		FFFF FFAC		
FFFF FFB0		FFFF F	FFFF FFBO	
FFFF FFB4		FFFF F	FB4	
FFFF FFB8		FFFF F	FB8	
FFFF FFBC		FFFF F	FBC	
FFFF FFC0		FFFF F	FC0	
FFFF FFC4		FFFF F	FFFF FFC4	
FFFF FFC8		FFFF F	FFFF FFC8	
FFFF FFCC		FFFF F	FFFF FFCC	
FFFF FFD0		FFFF F	FD0	
FFFF FFD4		FFFF F	FD4	
FFFF FFD8		FFFF F	FD8	
FFFF FFDC		FFFF F	FDC 60	
FFFF FFEO		FFFF I	FE0	
FFFF FFE4	4	FFFF I	FE4 4	
FFFF FFE8	FFFF FFFC	FFFF I	FFE8 FFFF FFFC	
FFFF FFEC	124	FFFF F	FEC 124	
FFFF FFFO		FFFF I	FFO	
FFFF FFF4		FFFF I	FF4	
FFFF FFF8	0xFFFFFFC0	FFFF I	FFF8 OxFFFFFFCC	
FFFF FFFC	148	FFFF I	FFC 148	
Instruction: 20	str r0, [fp, #-8]	Instruct	ion: 4 push {lr}	

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFC0	
FFFF FFC4	
FFFF FFC8	
FFFF FFCC	
FFFF FFD0	
FFFF FFD4	
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148

Instruction: 8 push {fp}

Address	Value
FFFF FFAO	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFC0	
FFFF FFC4	
FFFF FFC8	
FFFF FFCC	
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148

Instruction: 20 str r0, [fp, #-8]

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFC0	
FFFF FFC4	
FFFF FFC8	
FFFF FFCC	
FFFF FFD0	
FFFF FFD4	
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148
Instruction: 12	add fn. sn. #4

Instruction: 12 add fp, sp, #4

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFC0	
FFFF FFC4	
FFFF FFC8	
FFFF FFCC	60
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148

Instruction: 4 push {Ir}

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFC0	
FFFF FFC4	
FFFF FFC8	
FFFF FFCC	
FFFF FFD0	
FFFF FFD4	
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148

Instruction: 16 sub sp, sp, #8

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFC0	
FFFF FFC4	
FFFF FFC8	FFFF FFDC
FFFF FFCC	60
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148
Instruction, 0 r	auch (fm)

Instruction: 8 push {fp}

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFC0	
FFFF FFC4	
FFFF FFC8	FFFF FFDC
FFFF FFCC	60
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFF0	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148

Instruction: 12 add fp, sp, #4

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	60
FFFF FFCC FFFF FFD0	60
	3
FFFF FFD0	
FFFF FFD0 FFFF FFD4	3
FFFF FFD4 FFFF FFD8	3 FFFF FFEC
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC	3 FFFF FFEC
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC FFFF FFE0	3 FFFF FFEC 60
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC FFFF FFE0 FFFF FFE4	3 FFFF FFEC 60
FFFF FFD0 FFFF FFD4 FFFF FFDC FFFF FFE0 FFFF FFE4 FFFF FFE8	3 FFFF FFEC 60 4 FFFF FFFC
FFFF FFEO	3 FFFF FFEC 60 4 FFFF FFFC
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC FFFF FFE0 FFFF FFE4 FFFF FFE8 FFFF FFEC FFFF FFFC	3 FFFF FFEC 60 4 FFFF FFFC

Instruction: 4 push {Ir}

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFCO	
FFFF FFC4	
FFFF FFC8	FFFF FFDC
FFFF FFCC	60
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFE0	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFF0	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148
Instruction: 16	cub cn cn #0

Instruction: 16 sub sp, sp, #8

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	FFFF FFCC
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	60
	00
FFFF FFD0	00
	3
FFFF FFD0	
FFFF FFD0 FFFF FFD4	3
FFFF FFD4 FFFF FFD8	3 FFFF FFEC
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC	3 FFFF FFEC
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC FFFF FFE0	3 FFFF FFEC 60
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC FFFF FFE0 FFFF FFE4	3 FFFF FFEC 60
FFFF FFD0 FFFF FFD8 FFFF FFDC FFFF FFE0 FFFF FFE4 FFFF FFE8	3 FFFF FFEC 60 4 FFFF FFFC
FFFF FFEO	3 FFFF FFEC 60 4 FFFF FFFC
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC FFFF FFE0 FFFF FFE4 FFFF FFE8 FFFF FFEC FFFF FFFC	3 FFFF FFEC 60 4 FFFF FFFC
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC FFFF FFE0 FFFF FFE4 FFFF FFE8 FFFF FFEC FFFF FFFC FFFF FFFF	3 FFFF FFEC 60 4 FFFF FFFC 124

Instruction: 8 push {fp}

	1
Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	
FFFF FFBC	
FFFF FFCO	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	60
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148

Instruction: 20 str r0, [fp, #-8]

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	FFFF FFCC
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	60
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148
Instruction: 12	add fn cn #1

Instruction: 12 add fp, sp, #4

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	
FFFF FFB8	FFFF FFCC
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	60
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148

Instruction: 16 sub sp, sp, #8

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	1
FFFF FFB8	FFFF FFCC
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
o	
FFFF FFCC	60
FFFF FFCC	60
	3
FFFF FFD0	
FFFF FFD0 FFFF FFD4	3
FFFF FFD4 FFFF FFD8	3 FFFF FFEC
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC	3 FFFF FFEC
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC FFFF FFE0	3 FFFF FFEC 60
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC FFFF FFE0 FFFF FFE4	3 FFFF FFEC 60 4
FFFF FFE4 FFFF FFE8	3 FFFF FFEC 60 4 FFFF FFFC
FFFF FFE0 FFFF FFE4 FFFF FFE8 FFFF FFEC	3 FFFF FFEC 60 4 FFFF FFFC
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC FFFF FFE0 FFFF FFE4 FFFF FFE8 FFFF FFEC FFFF FFFC	3 FFFF FFEC 60 4 FFFF FFFC

Instruction: 80 pop {fp}

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFBO	
FFFF FFB4	1
FFFF FFB8	FFFF FFCC
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	60
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFF0	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148
Instruction: 20	) str r0. [fp. #-8

Instruction: 20 str r0, [fp, #-8]

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	1
FFFF FFB8	FFFF FFCC
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	
-111100	60
FFFF FFD0	60
	3
FFFF FFD0	
FFFF FFD0 FFFF FFD4	3
FFFF FFD0 FFFF FFD4 FFFF FFD8	3 FFFF FFEC
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC	3 FFFF FFEC
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC FFFF FFE0	3 FFFF FFEC 60
FFFF FFD0 FFFF FFD4 FFFF FFD8 FFFF FFDC FFFF FFE0 FFFF FFE4	3 FFFF FFEC 60
FFFF FFD0 FFFF FFD4 FFFF FFDC FFFF FFE0 FFFF FFE4 FFFF FFE8	3 FFFF FFEC 60 4 FFFF FFFC
FFFF FFD0 FFFF FFD4 FFFF FFDC FFFF FFE0 FFFF FFE4 FFFF FFE8 FFFF FFEC	3 FFFF FFEC 60 4 FFFF FFFC
FFFF FFD0 FFFF FFD4 FFFF FFDC FFFF FFE0 FFFF FFE4 FFFF FFE8 FFFF FFEC FFFF FFFC	3 FFFF FFEC 60 4 FFFF FFFC
FFFF FFD0 FFFF FFD4 FFFF FFDC FFFF FFE0 FFFF FFE4 FFFF FFE8 FFFF FFEC FFFF FFFC FFFF FFFC	3 FFFF FFEC 60 4 FFFF FFFC 124

Instruction: 84 pop {pc}

Address	Value
FFFF FFAO	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	1
FFFF FFB8	FFFF FFCC
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	60
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148

Instruction: 76 sub sp, fp, #4

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	1
FFFF FFB8	FFFF FFCC
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	60
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFF0	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148
Instruction, 76	subsp fp #1

Instruction: 76 sub sp, fp, #4

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	1
FFFF FFB8	FFFF FFCC
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	60
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFFO	·
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148
	""

Instruction: 80 pop {fp}

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	1
FFFF FFB8	FFFF FFCC
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	120
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFF0	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148

Instruction: 80 pop {fp}

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	1
FFFF FFB8	FFFF FFCC
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	60
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFE0	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148
Instruction: 8/	non Incl

Instruction: 84 pop {pc}

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	1
FFFF FFB8	FFFF FFCC
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	120
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFF0	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148

Instruction: 84 pop {pc}

Address	Value
FFFF FFAO	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	1
FFFF FFB8	FFFF FFCC
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	60
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148

Instruction: 76 sub sp, fp, #4

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	1
FFFF FFB8	FFFF FFCC
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	120
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148
	l f 44

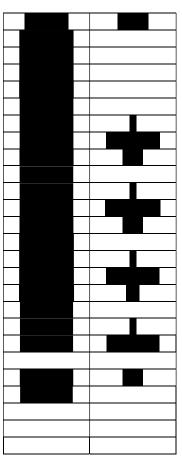
Instruction: 76 sub sp, fp, #4

Address	Value						
FFFF FFA0							
FFFF FFA4							
FFFF FFA8							
FFFF FFAC							
FFFF FFB0							
FFFF FFB4	1						
FFFF FFB8	FFFF FFCC						
FFFF FFBC	60						
FFFF FFC0							
FFFF FFC4	2						
FFFF FFC8	FFFF FFDC						
FFFF FFCC	60						
FFFF FFD0							
FFFF FFD4	3						
FFFF FFD8	FFFF FFEC						
FFFF FFDC	60						
FFFF FFEO							
FFFF FFE4	4						
FFFF FFE8	FFFF FFFC						
FFFF FFEC	124						
FFFF FFFO							
FFFF FFF4							
FFFF FFF8	0xFFFFFFC0						
FFFF FFFC	148						

Instruction 80 pop {fp}

Address	Value						
FFFF FFA0							
FFFF FFA4							
FFFF FFA8							
FFFF FFAC							
FFFF FFB0							
FFFF FFB4	1						
FFFF FFB8	FFFF FFCC						
FFFF FFBC	60						
FFFF FFC0							
FFFF FFC4	2						
FFFF FFC8	FFFF FFDC						
FFFF FFCC	60						
FFFF FFD0							
FFFF FFD4	3						
FFFF FFD8	FFFF FFEC						
FFFF FFDC	60						
FFFF FFEO							
FFFF FFE4	4						
FFFF FFE8	FFFF FFFC						
FFFF FFEC	124						
FFFF FFFO							
FFFF FFF4							
FFFF FFF8	0xFFFFFFC0						

Instruction: 136 sub sp, fp, #4



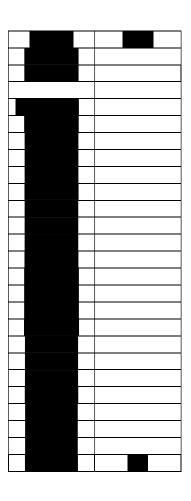
Nothing here..

Address	Value							
FFFF FFA0								
FFFF FFA4								
FFFF FFA8								
FFFF FFAC								
FFFF FFB0								
FFFF FFB4	1							
FFFF FFB8	FFFF FFCC							
FFFF FFBC	60							
FFFF FFC0								
FFFF FFC4	2							
FFFF FFC8	FFFF FFDC							
FFFF FFCC	60							
FFFF FFD0								
FFFF FFD4	3							
FFFF FFD8	FFFF FFEC							
FFFF FFDC	60							
FFFF FFEO								
FFFF FFE4	4							
FFFF FFE8	FFFF FFFC							
FFFF FFEC	124							
FFFF FFFO								
FFFF FFF4								
FFFF FFF8	0xFFFFFFC0							
FFFF FFFC	148							
Instructions 140 non (fm)								

Instruction: 140 pop {fp}

Address	Value
FFFF FFA0	
FFFF FFA4	
FFFF FFA8	
FFFF FFAC	
FFFF FFB0	
FFFF FFB4	1
FFFF FFB8	FFFF FFCC
FFFF FFBC	60
FFFF FFC0	
FFFF FFC4	2
FFFF FFC8	FFFF FFDC
FFFF FFCC	60
FFFF FFD0	
FFFF FFD4	3
FFFF FFD8	FFFF FFEC
FFFF FFDC	60
FFFF FFEO	
FFFF FFE4	4
FFFF FFE8	FFFF FFFC
FFFF FFEC	124
FFFF FFFO	
FFFF FFF4	
FFFF FFF8	0xFFFFFFC0
FFFF FFFC	148

Instruction: 124 str r0, [fp, #-8]



## **Register Contents**

r0	4	3	2	1	3	6	10	0											
r2	1	3	6																
r3	4	3	2	1	2	3	6	4	10	0									
lr	148	124	60	60	60	60													

sp	0x0000 0000	0xFFFF FFFC	0xFFFF FFF8	0xFFFF FFF0	0xFFFF FFEC	0xFFFF FFE8
sp	0xFFFF FFE0	0xFFFF FFDC	0xFFFF FFD8	0xFFFF FFD0	0xFFFF FFCC	0xFFFF FFC8
sp	0xFFFF FFC0	0xFFFF FFBC	0xFFFF FFB8	0xFFFF FFB0	0xFFFF FFB8	0xFFFF FFBC
sp	0xFFFF FFC0	0xFFFF FFC8	0xFFFF FFCC	0xFFFF FFD0	0xFFFF FFD8	0xFFFF FFDC
sp	0xFFFF FFE0	0xFFFF FFE8	0xFFFF FFEC	0xFFFF FFF0	0xFFFF FFF8	0xFFFF FFFC
sp	0x0000 0000					
sp						

fp	0x0000 C000	0xFFFF FFFC	0xFFFF FFEC	0xFFFF FFDC	0xFFFF FFCC	0xFFFF FFBC
fp	0xFFFF FFCC	0xFFFF FFDC	0xFFFF FFEC	0xFFFF FFFC		
fp						
fp						
fp						
fp						
fp						