EX: D. 8 lum fabrication proces

$$N mos$$
 transistor
 $tox = 8nm$
 $un = 450 cm^2$
 $v-s$
 $v+ = 0.7v$

a)
$$C_{0x} = \frac{\epsilon_r \epsilon_o}{\epsilon_o} = \frac{(3.9)(8.85 \times 10^{-12} \text{ F/m})}{8 \times 10^{-9} \text{ m}}$$

$$Cox = 4.31 \times 10^{-3} F$$
 m^2

$$= 450 \, \text{cm}^2 \left[4.31 \times 10^3 \, \text{F} \right] \, 1 \, \text{m}^2$$

$$V-5 \, \left[m^2 \, 100^2 \, \text{cm}^2 \right]$$

$$= 194.15 \times 10^{-6} = \frac{F}{V-S}$$

$$9 = CV \qquad C = 9 = C$$

$$[F] \qquad V$$

b) cond

$$k \mid_{n} = 194.15 \times 10^{-6} \frac{C/V}{V-S} = \frac{C}{S} \cdot \frac{1}{V^{2}}$$
 $k \mid_{n} = 194.15 \times 10^{-6} \frac{A}{V^{2}}$
 $k \mid_{n} = 194.15 \times 10^{-6} \frac{A}{V^{2}$

$$C_D = \frac{1}{2} R_D \frac{\omega}{L} (1065 - V_L) = 100 MFI$$

$$i_D = \frac{1}{2} \left(\frac{194.15 \text{ uA}}{\text{V2}} \right) \left(\frac{8 \text{ um}}{8 \text{ um}} \right) \left(\frac{1000 \text{ m}}{1000 \text{ m}} \right) \left(\frac{1000 \text{ m}}{100$$

$$100 = \frac{1}{2}(194.15)(10)(56-14)^{2}$$

$$565 = 1.02$$

$$V_{DS} = V_{6S} - V_{t}$$

$$= 1.02 - 0.7$$

$$V_{DS} = 0.321 V$$

$$(\sqrt{50}) = \sqrt{55} - \sqrt{4}$$

$$\sqrt{60} = \sqrt{65} - \sqrt{4}$$

$$V_{GS}$$
 or $V_{D} => ac + dc$ components

 V_{GS} or $V_{D} => dc$ only

vgs er i = ac only (small signal)

NMOS (15 in triade EX 2 Vos is Very Small Nmos device acts like 1000 r resistor. What is UGS? rds = = 1000 (194.15×10-6)(10)(1565-0.7) 1 VGS = 0.915 V