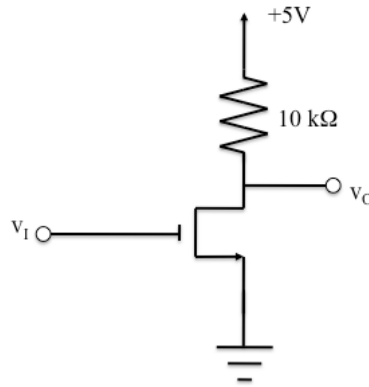


## Module 6: Homework

EE 315

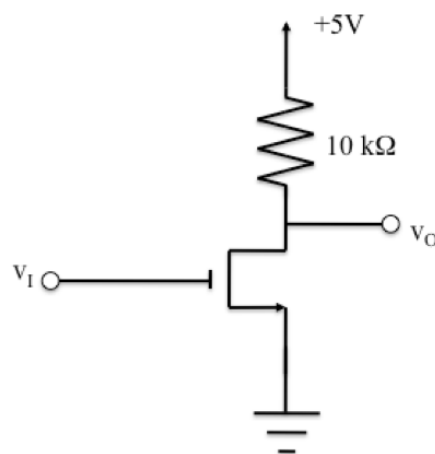
1. Consider the following common source amplifier, where  $V_t = 1.5\text{V}$ ,  $k'_n W/L = .2 \text{ mA/V}^2$ .



- Sketch the voltage transfer characteristic, clearly labeling the transition points, A, B and C.
  - The device is biased for a  $0.15 \text{ mA}$  drain current. Find the Q-point.
  - Find the voltage gain at this bias point.
2. A common source amplifier uses an NMOS transistor with  $k'_n = 0.4 \text{ mA/V}^2$ ,  $W/L = 10$ ,  $V_t = 0.4\text{V}$ ,  $V_{DD} = 2.5 \text{ V}$  and  $V_A = 10\text{V}$ . The amplifier Q-point is at  $I_{DQ} = 0.2 \text{ mA}$  and uses a drain resistor of  $6.2 \text{ k}\Omega$ .
- Find  $V_{GSQ}$  and  $V_{DSQ}$ .
  - Draw the small signal model and find  $g_m$ ,  $R_{in}$ ,  $A_{vo}$ , and  $R_o$ .
  - If a load resistor is connected to the drain where  $R_L = 15 \text{ k}\Omega$ , what is the gain,  $A_v$ . Update your small signal model.
  - If a source signal,  $v_{sig}$  in series with a resistance of  $R_{sig} = 300 \text{ k}\Omega$  is connected to the gate, what is the gain,  $G_v$ .
3. A common gate amplifier uses an NMOS transistor with  $g_m = 4 \text{ mA/V}$  and a drain resistor of  $5 \text{ k}\Omega$  and a load resistor of  $7.5 \text{ k}\Omega$ . The amplifier is driven by a source,  $v_{sig}$ , that has  $R_{sig} = 500 \text{ ohms}$ .
- Find the input resistance ( $R_{in}$ ) and the overall voltage gain,  $G_v$ . Draw the small signal model.
  - Suppose we want the input resistance to equal the signal resistance at the Q-point,  $I_{DQ}$ . What would the drain current Q-point need to change to for this to happen?

4. A common drain amplifier has the following characteristics:  $k'_n = 0.1 \text{ mA/V}^2$  and  $V_t = 0.6 \text{ V}$ . The operating point is  $V_{GSQ} = 0.85 \text{ V}$ .
- What is the  $W/L$  ratio for an output resistance of 300 ohms?
  - What is the drain current at the operating point?
  - This amplifier is connected to a 10k ohm potentiometer as the load. What is the range of possible overall voltage gain?

1. Consider the following common source amplifier, where  $V_t = 1.5 \text{ V}$ ,  $k'_n W/L = .2 \text{ mA/V}^2$ .



- a. Sketch the voltage transfer characteristic, clearly labeling the transition points, A, B and C.

$$V_t = 1.5 \text{ V}$$

$$k'_n \frac{W}{L} = 0.2 \text{ mA/V}^2$$

Point A:

Cutoff

$$V_I = V_t$$

$$V_O = V_{DD}$$

$$(5, 1.5)$$

Point B:

Edge of Saturation

$$V_O = V_I - V_t \quad V_I = V_{GS} = V_t +$$

$\uparrow$        $\uparrow$   
 $V_{DS}$     $V_{GS}$

$$V_I = V_{GS} = V_t + \frac{\sqrt{1 + 2(R_D k'_n \frac{W}{L} V_{DD})} - 1}{R_D k'_n \frac{W}{L}}$$

$$V_I = 1.5 + \frac{\sqrt{1 + 2(10)(0.2)(5)} - 1}{10 \times 2} \Rightarrow V_I = 3.29V$$

$$V_o = V_{os} = V_I - V_t$$

$$V_o = 3.29 - 1.5 = 1.79V \quad (1.79, 3.29)$$

Point C:

Triode

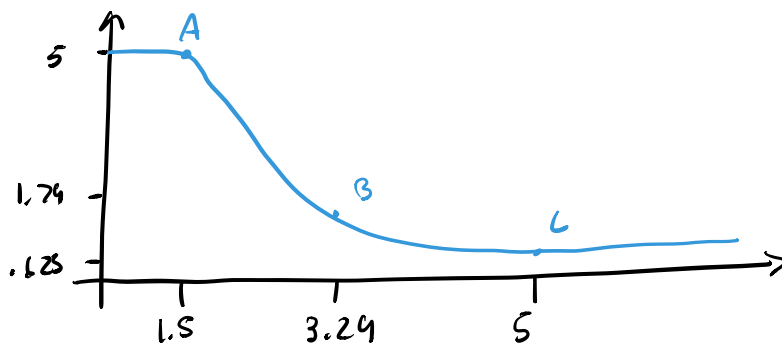
$$V_{GS} = V_I = V_{DD} = 5V$$

$$V_{os} = V_o = \frac{V_{GS}}{1 + R_o k'_n \frac{W}{L} (V_{GS} - V_t)}$$

$$V_o = \frac{5}{1 + (1.2)(5 - 1.5)(10)}$$

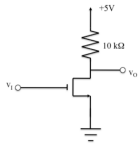
$$V_o = 0.625V$$

Point	$V_o$	$V_I$
A	5	1.5
B	1.79	3.29
C	0.625	5



$$V_t = 1.5\text{V}$$

$$k'_n \frac{W}{L} = 0.2 \frac{\text{mA}}{\text{V}^2}$$



b. The device is biased for a 0.15 mA drain current. Find the Q-point.

$$I_{DQ} = 0.15 \text{ mA}$$

$$V_{DQ} = V_{DSQ} = V_{DD} - I_{DQ} R_D$$

$$V_{DSQ} = (5) - (0.15)(10)$$

$$V_{DQ} = 3.5\text{V}$$

$$V_{IQ} = V_{GSQ}$$

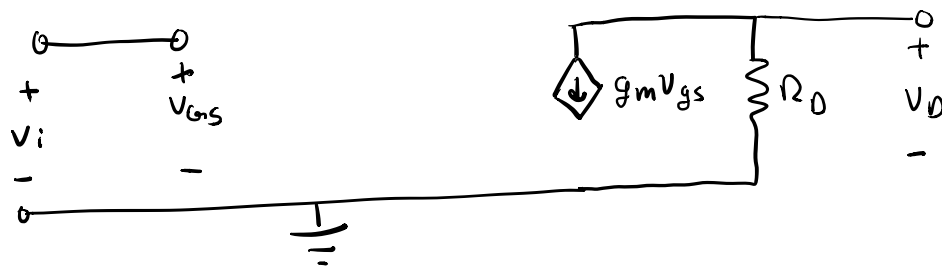
$$I_{DQ} = \frac{1}{2} k'_n \frac{W}{L} (V_{IQ} - V_t)^2$$

$$0.15 = \frac{1}{2} (0.2) (V_{IQ} - 1.5)^2$$

$$1.5 = (V_{IQ} - 1.5)^2 \quad \sqrt{1.5} + 1.5 = V_{IQ}$$

$$V_{IQ} = 2.72\text{V}$$

c. Find the voltage gain at this bias point.



$$A_{v0} = \frac{v_o}{v_i} = -g_m R_D$$

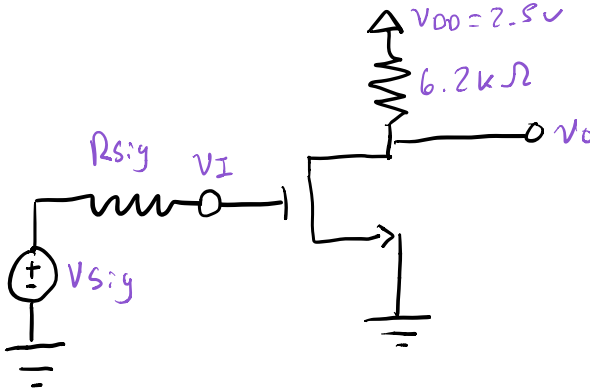
$$= -\left(k'_n \frac{W}{L} (V_{IQ} - V_t)\right) R_D$$

$$A_{v0} = -(0.2)(2.72 - 1.5)(10)$$

$$A_{v0} = -2.45\text{V/V}$$

2. A common source amplifier uses an NMOS transistor with  $k'_n = 0.4 \text{ mA/V}^2$ ,  $W/L = 10$ ,  $V_t = 0.4 \text{ V}$ ,  $V_{DD} = 2.5 \text{ V}$  and  $V_A = 10 \text{ V}$ . The amplifier Q-point is at  $I_{DQ} = 0.2 \text{ mA}$  and uses a drain resistor of  $6.2 \text{ k}\Omega$ .

a. Find  $V_{GSQ}$  and  $V_{DSQ}$ .



$$\begin{aligned} R_D &= 6.2 \text{ k}\Omega & V_{DD} &= 2.5 \text{ V} \\ k'_n &= .4 \text{ mA/V}^2 & V_A &= 10 \text{ V} \\ W/L &= 10 & I_{DQ} &= .2 \text{ mA} \\ V_t &= .4 \text{ V} \end{aligned}$$

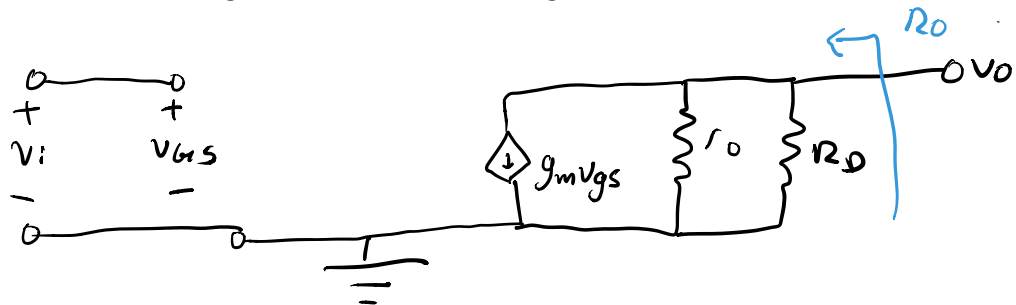
$$I_{DQ} = .2 \text{ mA} = \frac{1}{2} k'_n \frac{W}{L} (V_{GSQ} - V_t)^2$$

$$.2 \text{ mA} = \frac{1}{2} (.4) (10) (V_{GSQ} - .4)^2$$

$$\sqrt{.1} + .4 = V_{GSQ} ; \boxed{V_{GSQ} = 0.716 \text{ V}}$$

$$V_{DSQ} = 2.5 - I_{DQ} R_D \quad \boxed{V_{DSQ} = 1.26 \text{ V}}$$

b. Draw the small signal model and find  $g_m$ ,  $R_{in}$ ,  $A_{vo}$ , and  $R_o$ .



$$R_{in} = \infty$$

$$g_m = k'_n \frac{W}{L} (V_{GSQ} - V_{t}) = (6.4)(10)(6.716 - 4)$$

$$g_m = 1.264 \text{ mA/V}$$

$$r_o = \frac{V_A}{I_{DQ}} = \frac{10}{.2 \text{ F-3}} = 50 \text{ k}\Omega$$

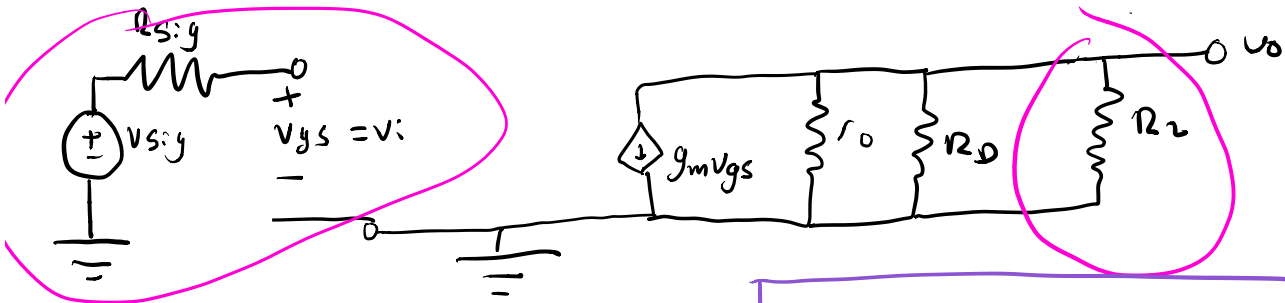
$$R_o = r_o \parallel R_D = 50 \parallel 6.2 = 5.52 \text{ k}\Omega$$

$$A_{v0} = \frac{V_o}{V_i} = -g_m (r_o \parallel R_D)$$

$$= -1.264 (5.52)$$

$$A_{v0} = -6.98 \text{ V/V}$$

- c. If a load resistor is connected to the drain where  $R_L = 15 \text{ k}\Omega$ , what is the gain,  $A_v$ . Update your small signal model.



$$A_v = \frac{V_o}{V_i} = -g_m (r_o \parallel R_D \parallel R_L)$$

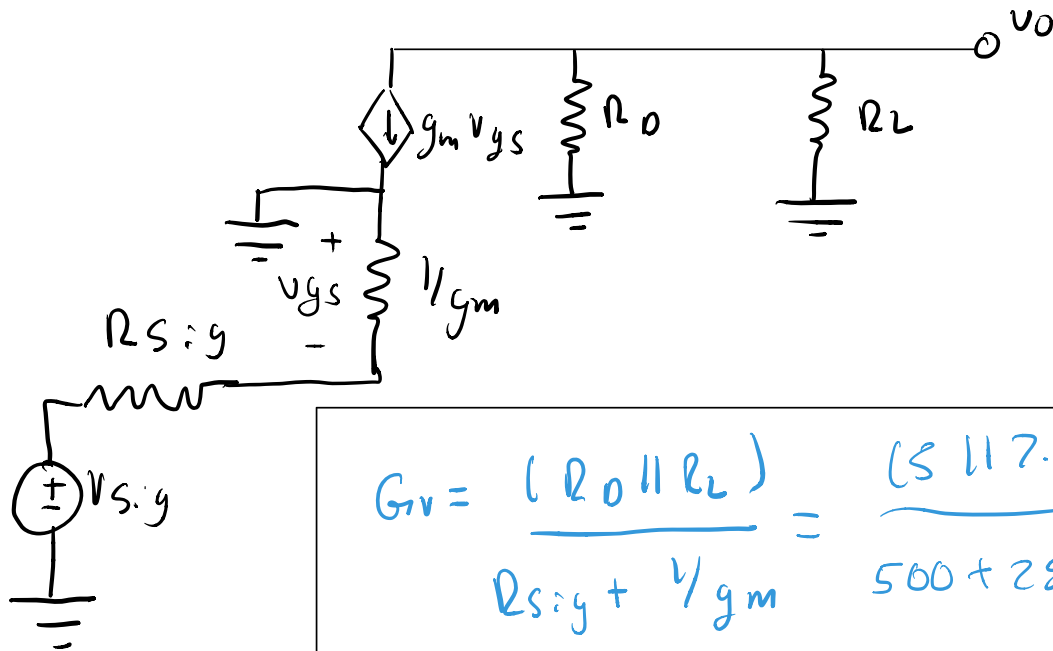
$$= -1.264 (50 \parallel 6.2 \parallel 15)$$

$$A_v = -5.1 \text{ V/V}$$

- d. If a source signal,  $v_{sig}$  in series with a resistance of  $R_{sig} = 300 \text{ k}\Omega$  is connected to the gate, what is the gain,  $G_v$ .

$$G_v = \frac{V_o}{V_{sig}} = -5.1 \text{ V/V}$$

3. A common gate amplifier uses an NMOS transistor with  $g_m = 4 \text{ mA/V}$  and a drain resistor of  $5 \text{ k}\Omega$  and a load resistor of  $7.5 \text{ k}\Omega$ . The amplifier is driven by a source,  $v_{sig}$ , that has  $R_{sig} = 500 \text{ }\Omega$ .
- a. Find the input resistance ( $R_{in}$ ) and the overall voltage gain,  $G_v$ . Draw the small signal model.



$$G_v = \frac{(R_D \parallel R_L)}{R_{sig} + 1/g_m} = \frac{(5 \parallel 7.5)}{500 + 250}$$

$$G_v = 4 \text{ V/V}$$

- b. Suppose we want the input resistance to equal the signal resistance at the Q-point,  $I_{DQ}$ . What would the drain current Q-point need to change to for this to happen?

$$R_{in} = R_{sig}$$

$$R_{sig} = 500$$

$$R_{in} = 250 = \frac{1}{g_m} \quad g_{m1} = 4 \text{ mA/V}$$

$$\frac{1}{g_m} = 500, \quad g_m = 2 \text{ mA/V}$$

$$I_{DQ} = \frac{1}{2} k'_n \frac{W}{L} (V_{GSQ} - V_t)^2$$

$$g_m = k'_n \frac{W}{L} (V_{GSQ} - V_t)$$

$$g_m = \frac{2 I_{DQ}}{(V_{GSQ} - V_t)} \quad (V_{GSQ} - V_t) = \sqrt{\frac{2 I_{DQ}}{k'_n W/L}}$$

$$g_m = \sqrt{2 k'_n \frac{W}{L} I_{DQ}}$$

$g_m$  reduces by  $1/2$

$I_{DQ}$  reduces by  $1/4$

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4. A common drain amplifier has the following characteristics:  $k'_n = 0.1 \text{ mA/V}^2$  and  $V_t = 0.6 \text{ V}$ . The operating point is  $V_{GSQ} = 0.85 \text{ V}$ .
- What is the  $W/L$  ratio for an output resistance of 300 ohms?
  - What is the drain current at the operating point?
  - This amplifier is connected to a 10k ohm potentiometer as the load. What is the range of possible overall voltage gain?

$$k'_n = 0.1 \text{ mA/V}^2$$

$$V_t = 0.6 \text{ V}$$

$$V_{GSQ} = 0.85 \text{ V}$$

a)  $W/L$   $R_o = 300 \Omega$

$$R_o = \frac{1}{g_m} = 300;$$

$$g_m = 3.33 \text{ mA} = k'_n \frac{W}{L} (V_{GSQ} - V_t)$$

$$\frac{W}{L} = \frac{3.33}{(0.1)(0.85 - 0.6)}$$

$$\frac{W}{L} = 133.2$$

b) drain current?

$$I_{DQ} = \frac{1}{2} k'_n \frac{W}{L} (V_{GSQ} - V_t)^2$$

$$I_{DQ} = \frac{1}{2} (0.1)(133.2)(0.85 - 0.6)^2$$

$$I_{DQ} = 0.416 \text{ mA}$$

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c)  $G_v = \frac{R_L}{R_L + 1/g_m} =$

$$G_v = \frac{0}{0 + 3.33}$$

$$R_L = 0 \quad G_v = 0$$

$$G_v = \frac{10}{10 + 3.33}$$

$$R_L = 10 \text{ k}\Omega \quad G_v = 0.97 \text{ V/V}$$