9/21/2020 Quiz: MSP430 ISA

## MSP430 ISA

Started: Sep 21 at 9:21pm

## **Quiz Instructions**

This is a practice quiz that will test your knowledge of MSP430 Instruction Set Architecture.

Question 1	1 pts
Stack Pointer register (SP) points to the last full location on the stack.	
True	
○ False	
Question 2	1 pts
Size of MSP430 address space is:	
○ 256 bytes	
16,384 bytes	
○ 2^8 bytes	
2^16 bytes	
Question 3	1 pts
The following code sequence sums up variables A and B from memory and stores their sum in the variable C. What type of machine is used?	
LOAD A	

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ADD B	
STORE C	
<ul> <li>Accumulator-based machine - one address machine</li> </ul>	
○ Stack machine	
○ Register-memory machine	
○ Star-track machine	

Question 4

Consider the following machine instruction:

MOV.W @R5+, 200(R7);

What is the address of the destination operand if R7=0x0600?

0x06C8

0x0400

0x0800

800

Question 5 1 pts

Consider the following machine instruction:

MOV.W @R5+, 200(R7);

What is the addressing mode of the source operand?

-0	Quiz. Moi 400 fon	
0	Register direct	
0	Register indirect	
0	Indexed	
•	Register indirect with autoincrement	
eve	ry addressing mide except indirect and auto increment add an addition	al word fol
Qu	estion 6 Call register they apply to	1 pts
Wha	at is the size of the following instruction?	
ADI	D.W MyVar1, MyVar2	
•	1 word	
0	2 words	
	3 words	

(III F

O 4 words

Question 7				0006 1000	1 pts
What is the result of execution of the following instruction?  SUBC.B R7, R8	Q8←	R8+	'R7+1	10000101 000100101	
$\label{eq:assumeR} Assume \ R7=0x0908; \ R8=0x0325; \ R2=0x0001 \ (C=1, \ V=N=Z=0).$ $\ SUBC \ (\ .\ B) \qquad \qquad src, dst \qquad \qquad dst + .not.src + C \rightarrow .$	dst			000	
R8=0x001D					
○ R8=0x0013					
○ R8=0xFA1D					

○ R8=0x0012

	1 pts
08 + 25	
3 3	
	08 + 25 3 3

No new data to save. Last checked at 9:22pm

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