State-Machine Configurator

Glossary

Version 1.0

Revision History

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Glossary

# Introduction

[The introduction of the **Glossary** document provides an overview of the entire document.]

# Glossary

[Present the noteworthy terms and their definition, format and validation rules if appropriate.]

|  |  |  |  |
| --- | --- | --- | --- |
| **Term** | **Definition and Information** | **Format** | **Validation Rules** |
| STM | A finite-state machine (FSM) or finite-state automaton (FSA, plural: automata), finite automaton, or simply a state machine, is a mathematical [model of computation](https://en.wikipedia.org/wiki/Model_of_computation). It is an [abstract machine](https://en.wikipedia.org/wiki/Abstract_machine) that can be in exactly one of a finite number of [states](https://en.wikipedia.org/wiki/State_(computer_science)) at any given time. |  |  |