

Timeouts for AVL and BST

CASE 1: The ascending order.

CASE 2: Order such that BST is always filled from left to right.

CASE 3: Random order.

	CASE 1/nanoseconds	CASE 2/ nanoseconds	CASE 3/ nanoseconds
BST	12421	18234	23416
AVL	11952	18916	26382