



allvm - Binary Decompilation

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Possible Approaches

Our Approach



· Research Goal

- Obtain "richer" LLVM IR than native machine code.
- Enable advanced compiler techniques (e.g. pointer analysis, information flow tracking, automatic vectorization)

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- Source code analysis not possible
 - IP-protected software
 - Malicious executables
 - Legacy executables
- Source code analysis not sufficient
 - What-you-see-is-not-what-you-execute
- Platform aware optimizations

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Possible Approaches

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The 3 Possible Approaches

- $\bullet \ \ Decompile \ \texttt{Machine} \ \ \texttt{Code} \to \texttt{LLVM} \ \ \texttt{IR}$
- "Annotated" Machine Code \rightarrow LLVM IR
- Ship LLVM IR

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$Decompile \; \texttt{Machine} \; \; \texttt{Code} \to \texttt{LLVM} \; \; \texttt{IR}$

Benefits	Challenges
Easy to adoptNo compiler support needed	 Reconstructing code and control flow Variable recovery Function & ABI rules recovery

• Tools Available: QEMU, BAP, Dagger, Mcsema, Fracture

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"Annotated" Machine Code \rightarrow LLVM IR

Benefits	Challenges
Effective reconstruction Minimal compiler support needed	 Annotations must be Minimal Compiler & IR independent Adoption

• Tools Available: None

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Benefits	Challenges
• <i>No loss</i> of information	Adoption in Non LLVM based compilersCode size bloat

• Tools Available: Portable Native Client, Renderscript, iOS, watchOS and tvOS apps.

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Long term goal

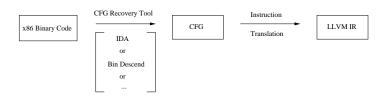
Minimal compiler-independent annotations to reconstruct high-quality IR

Short term goals

- lacktriangle Experiment with Machine Code ightarrow LLVM IR, to **understand** the challenges better
 - To select an existing decompilation framework.
 - Experiment with different variable and type recovery strategies
- Design suitable annotations for what cannot be inferred without them

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- Separation of modules: CFG recovery and CFG \rightarrow LLVM IR
- · Actively supported and open sourced
- · Well docmented
- Functional LLVM IR



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Instruction Translation: Memory Model



Binary Code

RECOVERED_FUNC(RegisterContex):

VAR_EAX = RegisterContext.EAX VAR_ESP = RegisterContext.ESP

 $VAR_EAX = [VAR_ESP - 4]$ $VAR_EAX += 1$

RegisterContext.EAX = VAR_EAX RegisterContect.ESP = VAR_ESP

END

High level view of Recovered Code

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Support & Limitations

- What Works
 - Integer Instructions
 - FPU and SSE registers
 - Callbacks, External Call, Jump tables
- In Progress
 - FPU and SSE Instructions: Not fully supported
 - Exceptions
 - Better Optimizations

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Possible Approaches

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Variable & Function Parameter Recovery

- Benefit
 - Enables many fundamental analysis (Dependence, Pointer analysis)
 - · Functional IR
- State of the art
 - Divine
 - State of the art variable recovery
 - · Second Write
 - · Heuristics for function parameter detection
 - Scalable variable and type recovery
 - TIE
 - Type recovery

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Possible Approaches

Our Approach



What-you-see-is-not-what-you-execute

The following compiler (Microsoft C++ .NET) induced vulnerability was discovered during the Windows security push in 2002

```
memset(password, '\0', len); free(password); free(password);
```

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