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|---------------------------|--|---|-----------------------------------|------------------|----------|
| | | C2_Designator1, C2_Designator2, | | | |
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| | | C3_Designator1, C3_Designator2, C3_Designator3, | | | |
| | | C4_Designator1, C4_Designator2, C4_Designator3, | | | |
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| | | C5_Designator2, C5_Designator3, C6_Designator1, | | | |
| | | C6_Designator2, C6_Designator3, C7_Designator1, | | | |
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| | | C7_Designator3, C8_Designator1, C8_Designator2, | | | |
| | | C8_Designator3, C9 Designator1. | | | |
| | | C9_Designator2, C9_Designator3 | | | |
| | | C10_Designator1, C10_Designator2, C10_Designator3, | | | |
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| | | C11_Designator2, C11_Designator3, C12_Designator1, | | | |
| | | C12_Designator2, C12_Designator3, C13_Designator1, | | | |
| | 0603 (1608 Metric) Chip | C13_Designator1, C13_Designator2, C13_Designator3, | | | |
| CAP0603 | Capacitor | C14_Designator1, | CAPC0603(1608)100_L | CMP-1590-00005-1 | 6 |
| | Standard Tantalum, 100 uF, +/- 10%, 10 V, -55 to 125 degC, 2-Pin SMD (2312), RoHS, Tape and | | | | |
| TAJC107K010RNJ | | C22, C31, C32 | AVX-TAI-C-2_M | CMP-2100-03690-1 | |
| BAS21LT1G | DIODE SWITCH 200MA 250V SOT23 | D1 | ONSC-SOT-23-3-318- 08_V | CMP-1679-00004-3 | |
| MDDANA | Schottky Power Rectifier, 2-Pin SMA, | P.3 | ONSC-SMA-2-403D- | CMD TOTT TOT | |
| MBRA340T3 | Tape and Reel | J1_Designator1, J1_Designator2 | 02_F_V | CMP-1055-00314-1 | |
| | | J1_Designator2, J1_Designator3, J2_Designator1, | | | |
| | | J2_Designator2, J2_Designator3, J3_Designator1, | | | |
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| | | J3_Designator3, J4_Designator1, J4_Designator2, | | | |
| | | J4_Designator2, J4_Designator3, J5_Designator1, J5_Designator2, | | | |
| | | J5_Designator2, J5_Designator3, | | | |
| | | J5_Designator3, J6_Designator1, J6_Designator2, | | | |
| | | J7_Designator1, J7_Designator1 | | | |
| | M12.REAR | | | | |
| M12 PCB Pannel Moun | MOUNTING.MALE.A | J8_Designator1, J8_Designator2, J8_Designator3 | | M12 | 2 |
| | Low Profile, High Current (HI PIR) | | | | |
| IHLP4040DZER101M11 | Inductor, 100 uH, +/- 20%, 2.5 A, -55 to 125 degC, 2-Pin SMD, RoHS, Tape and Reel | | VISH- | | |
| | RoHS, Tape and Reel 150mA linear | L1 | IHLP4040DZER101M11_ V | CMP-2000-06447-1 | |
| | regulators, low dropout with reverse | | | | |
| + 1V8 TPS709 | current protection | PS1 Q1_Designator7, | | TPS709 | |
| | | Q1_Designator8, Q1_Designator9, Q1_Designator10, | | | |
| | Low Noise Transistor. | | | | |
| MPSA18G | NPN Silicon, 3-Pin TO- 92, Pb-Free, Bulk Box | Q2_Designator8, Q2_Designator9, Q2_Designator10 | ONSC-TO-92-3-29-11 | CMP-1048-01396-1 | |
| | | R1_Designator2, R1_Designator3, | | | |
| | | R2_Designator1, R2_Designator2, | | | |
| | | R2_Designator3, R3_Designator1, | | | |
| | | R3_Designator2, R3_Designator3, R4_Designator1, | | | |
| | | R4_Designator2, R4_Designator3, | | | |
| | | R5_Designator1, R5_Designator2 | | | |
| | | R5_Designator3, R6_Designator1, R6_Designator2, | | | |
| | | R6_Designator3, R7_Designator1, | | | |
| | | R7_Designator2, R7_Designator2 | | | |
| | | R8_Designator1, R8_Designator2, R8_Designator3, | | | |
| | | | | | |
| | | R9_Designator2, R9_Designator3, R10_Designator7, | | | |
| | | R10_Designator7, R10_Designator8, R10_Designator9, R10_Designator10, | | | |
| | | | | | |
| | | R11_Designator8, R11_Designator9, | | | |
| | | R11_Designator10, R12_Designator7, R12_Designator8, | | | |
| | 0603 (1608 Metric) Chip | R12_Designator9, R12_Designator10_R13. | | | |
| RES0603 | Resistor | R14, R15, R16 | RESC0603(1608)_L | CMP-1591-00001-1 | 4 |
| | The ADS1278-SP (octal) is a 24-bit, delta-sigma (ΔΣ) analog-to-digital | | | | |
| | | | | | |
| | data rates up to 128k samples per second (SPS), allowing | U1_Designator1, | | | |
| ads1278-sp | simultaneous sampling of eight channels. | U1_Designator2, U1_Designator3 | | ads1278-sp | |
| | 16 MHz Mixed Signal Microcontroller with 2 KB SRAM and 40 GPIOs, | | | | |
| | OFN (RGZ) Green | | | | |
| MSP430FR5969IRGZT | (RoHS & no Sb/Br), Tape and Reel | U2 | RGZ0048B_V | CMP-2000-06762-1 | <u></u> |
| | The DS16F95 Differential Bus Transceiver is a | | | | |
| | Transceiver is a monolithic integrated circuit designed for | | | | |
| | bidirectional data | | | | |
| | balanced multipoint bus transmission lines. | | | | |
| 1 | The transceiver meets EIA standard RS-485 as | | | | |
| | well as RS-422A. High Voltage 500 mA, 200 kHz Step-Down | U3 | | DS16F95QML | |
| DS16F95 | | İ | l | | |
| DS16F95 | Switching Regulator | | l | | |
| DS16F95 | Switching Regulator | | | | |
| D\$16F95 LT3437IFE#PBF | Switching Regulator with 100 uA Quiescent Current, 16-pin SOP (FE 16), -40 to 125 degC, Pb Free Ceramic Surface Mount | U4 | LT-FE-16-EPV-BC_M | CMP-0491-00587-1 | |
| | Switching Regulator with 100 uA Quiescent Current, 16-pin SOP (FE 16), -40 to 125 degC, Pb Free Ceramic Surface Mount Low Profile Quartz | U4 | LT-FE-16-EPV-BC_M | CMP-0491-00587-1 | |
| | Switching Regulator with 100 uA Quiescent Current, 16-pin SOP (FE 16), -40 to 125 degC, Pb Free Ceramic Surface Mount | U4 | LT-FE-16-EPV-BC_M ABRA-ABMM2-4_V | CMP-0491-00587-1 | |