```
 1: ****************************************
 2: Report : timing
 3:  -path_type full
 4:  -delay_type min
 5:  -slack_lesser_than 5.00
 6:  -max_paths 3
 7:  -sort_by slack
 8: Design : cruisecontrol
 9: Version: V-2023.12-SP1
10: Date   : Thu Mar 27 14:01:06 2025
11: ****************************************
12:
13:
14:   Startpoint: reset (input port clocked by clk)
15:   Endpoint: cruisectrl_reg
16:               (rising edge-triggered flip-flop clocked by clk)
17:   Path Group: clk
18:   Path Type: min
19:
20:   Point                                     Incr       Path
21:   -----------------------------------------------------------
22:   clock clk (rise edge)                     0.00       0.00
23:   clock network delay (ideal)               0.00       0.00
24:   input external delay                      0.00       0.00 f
25:   reset (in)                                0.08       0.08 f
26:   U18/Y (NOR2X1)                            0.06       0.14 r
27:   cruisectrl_reg/D (DFFPOSX1)               0.00       0.14 r
28:   data arrival time                                    0.14
29:
30:   clock clk (rise edge)                     0.00       0.00
31:   clock network delay (ideal)               0.00       0.00
32:   clock reconvergence pessimism             0.00       0.00
33:   cruisectrl_reg/CLK (DFFPOSX1)                        0.00 r
34:   library hold time                         0.00       0.00
35:   data required time                                   0.00
36:   -----------------------------------------------------------
37:   data required time                                   0.00
38:   data arrival time                                   -0.14
39:   -----------------------------------------------------------
40:   slack (MET)                                         0.14
41:
42:
43:   Startpoint: reset (input port clocked by clk)
44:   Endpoint: state_reg[0]
45:               (rising edge-triggered flip-flop clocked by clk)
46:   Path Group: clk
47:   Path Type: min
48:
49:   Point                                     Incr       Path
50:   -----------------------------------------------------------
51:   clock clk (rise edge)                     0.00       0.00
52:   clock network delay (ideal)               0.00       0.00
53:   input external delay                      0.00       0.00 f
54:   reset (in)                                0.08       0.08 f
55:   U50/Y (NOR2X1)                            0.06       0.14 r
56:   state_reg[0]/D (DFFPOSX1)                 0.00       0.14 r
57:   data arrival time                                    0.14
58:
59:   clock clk (rise edge)                     0.00       0.00
60:   clock network delay (ideal)               0.00       0.00
```

```
  clock reconvergence pessimism            0.00       0.00
  state_reg[0]/CLK (DFFPOSX1)                         0.00 r
  library hold time                        0.00       0.00
  data required time                                  0.00
  -----------------------------------------------------------
  data required time                                  0.00
  data arrival time                                  -0.14
  -----------------------------------------------------------
  slack (MET)                                         0.14


  Startpoint: speed_reg[7]
              (rising edge-triggered flip-flop clocked by clk)
  Endpoint: cruisespeed_reg[7]
              (rising edge-triggered flip-flop clocked by clk)
  Path Group: clk
  Path Type: min

  Point                                                   Incr       Path
  --------------------------------------------------------------------------
  clock clk (rise edge)                                   0.00       0.00
  clock network delay (ideal)                             0.00       0.00
  speed_reg[7]/CLK (DFFPOSX1)                             0.00       0.00 r
  speed_reg[7]/Q (DFFPOSX1)                               0.12       0.12 r
  U132/Y (AOI22X1)                                        0.07       0.19 f
  U131/Y (NAND2X1)                                        0.05       0.24 r
  cruisespeed_reg[7]/D (DFFPOSX1)                         0.00       0.24 r
  data arrival time                                                  0.24

  clock clk (rise edge)                                   0.00       0.00
  clock network delay (ideal)                             0.00       0.00
  clock reconvergence pessimism                           0.00       0.00
  cruisespeed_reg[7]/CLK (DFFPOSX1)                                  0.00 r
  library hold time                                       0.00       0.00
  data required time                                                 0.00
  --------------------------------------------------------------------------
  data required time                                                 0.00
  data arrival time                                                 -0.24
  --------------------------------------------------------------------------
  slack (MET)                                                        0.24

Warning: report_timing has satisfied the max_paths criteria. There are 17 further
    endpoints which have paths of interest with slack less than     5.00 that were not
    considered when generating this report. (UITE-502)

1
```