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1: *****
2: Report : timing
3: -path_type full
4: -delay_type max
5: -slack_lesser_than 5.00
6: -max_paths 3
7: -sort_by slack
8: Design : cruisecontrol
9: Version: V-2023.12-SP1
10: Date   : Thu Mar 27 14:00:53 2025
11: *****

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12:
13:
14: Startpoint: cruisespeed_reg[6]
15:           (rising edge-triggered flip-flop clocked by clk)
16: Endpoint: cruisespeed[6]
17:           (output port clocked by clk)
18: Path Group: clk
19: Path Type: max
20:

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21: Point	Incr	Path
22: -----		
23: clock clk (rise edge)	0.00	0.00
24: clock network delay (ideal)	0.00	0.00
25: cruisespeed_reg[6]/CLK (DFFPOSX1)	0.00	0.00 r
26: cruisespeed_reg[6]/Q (DFFPOSX1)	0.18	0.18 r
27: U186/Y (INVX1)	0.27	0.44 f
28: U185/Y (INVX8)	0.77	1.21 r
29: cruisespeed[6] (out)	0.00	1.21 r
30: data arrival time		1.21
31:		
32: clock clk (rise edge)	10.00	10.00
33: clock network delay (ideal)	0.00	10.00
34: clock reconvergence pessimism	0.00	10.00
35: output external delay	-5.00	5.00
36: data required time		5.00
37: -----		
38: data required time		5.00
39: data arrival time		-1.21
40: -----		
41: slack (MET)		3.79
42:		

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43:
44: Startpoint: cruisespeed_reg[0]
45:           (rising edge-triggered flip-flop clocked by clk)
46: Endpoint: cruisespeed[0]
47:           (output port clocked by clk)
48: Path Group: clk
49: Path Type: max
50:

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51: Point	Incr	Path
52: -----		
53: clock clk (rise edge)	0.00	0.00
54: clock network delay (ideal)	0.00	0.00
55: cruisespeed_reg[0]/CLK (DFFPOSX1)	0.00	0.00 r
56: cruisespeed_reg[0]/Q (DFFPOSX1)	0.14	0.14 r
57: U184/Y (INVX1)	0.26	0.40 f
58: U183/Y (INVX8)	0.77	1.17 r
59: cruisespeed[0] (out)	0.00	1.17 r
60: data arrival time		1.17

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61:
62: clock clk (rise edge)                10.00      10.00
63: clock network delay (ideal)          0.00      10.00
64: clock reconvergence pessimism        0.00      10.00
65: output external delay                -5.00      5.00
66: data required time                   5.00
67: -----
68: data required time                     5.00
69: data arrival time                     -1.17
70: -----
71: slack (MET)                          3.83
72:
73:
74: Startpoint: cruisespeed_reg[5]
75:      (rising edge-triggered flip-flop clocked by clk)
76: Endpoint: cruisespeed[5]
77:      (output port clocked by clk)
78: Path Group: clk
79: Path Type: max
80:
81: Point                                Incr          Path
82: -----
83: clock clk (rise edge)                0.00          0.00
84: clock network delay (ideal)          0.00          0.00
85: cruisespeed_reg[5]/CLK (DFFPOSX1)    0.00          0.00 r
86: cruisespeed_reg[5]/Q (DFFPOSX1)     0.14          0.14 r
87: U182/Y (INVX1)                       0.23          0.37 f
88: U181/Y (INVX8)                       0.76          1.12 r
89: cruisespeed[5] (out)                 0.00          1.12 r
90: data arrival time                    1.12
91:
92: clock clk (rise edge)                10.00      10.00
93: clock network delay (ideal)          0.00      10.00
94: clock reconvergence pessimism        0.00      10.00
95: output external delay                -5.00      5.00
96: data required time                   5.00
97: -----
98: data required time                     5.00
99: data arrival time                     -1.12
100: -----
101: slack (MET)                          3.88
102:
103: Warning: report_timing has satisfied the max_paths criteria. There are 30 further
    endpoints which have paths of interest with slack less than 5.00 that were not
    considered when generating this report. (UITE-502)
104:
105: 1
106:

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