

```
1:
2: *****
3: Report : area
4: Design : cruisecontrol
5: Version: V-2023.12-SP1
6: Date   : Thu Mar 27 13:33:13 2025
7: *****
8:
9: Library(s) Used:
10:
11:   iit018_stdcells (File:
12:     /home/ugrads/s/sdh02/ecen454_714/synthesis/iit018_stdcells.db)
13: Number of ports:          58
14: Number of nets:          356
15: Number of cells:         305
16: Number of combinational cells: 283
17: Number of sequential cells:  20
18: Number of macros/black boxes:  0
19: Number of buf/inv:         74
20: Number of references:      17
21:
22: Combinational area:      8514.000000
23: Buf/Inv area:           1592.000000
24: Noncombinational area:   1920.000000
25: Macro/Black Box area:    0.000000
26: Net Interconnect area:   undefined (No wire load specified)
27:
28: Total cell area:         10434.000000
29: Total area:              undefined
30:
```