## Owen Shin COMPONENT LAYOUT AND I/O ASSIGNMENT Lab 1 **PROTOBOARD** 16-bit I/O BOARD LEDs B-Side LEDs A-side LB3)(LB4)(LB5)(LB6) LB1 C1 D1 E1 Z-1 Z-2 E2 A2 C2 D2 А3 В3 C3 D3 E3 **Ы**В1 35 35 Right Right 37 Left 37 Left 39 39 **HEX B-side** HEX A-side **A4** B4 C4 D4 E4 C5 **A5** B5 D5 **E**5 A6 B6 C6 D6 E6 В SB1 **SWITCHES A-side SWITCHES B-side** Α7 **B7** D7 **E7**