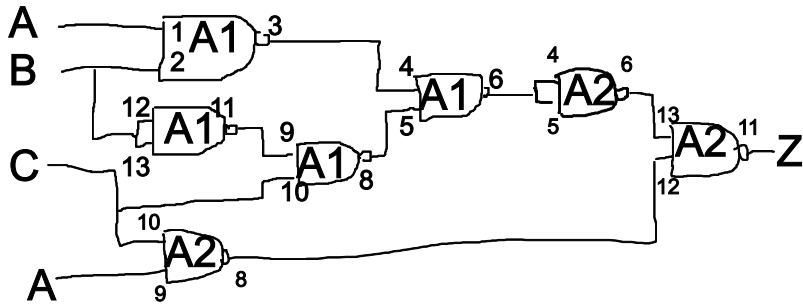
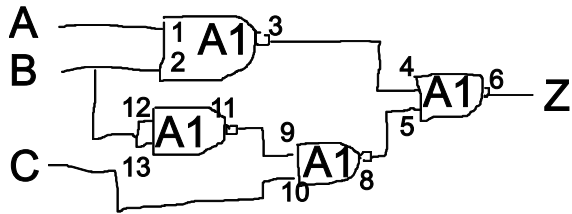


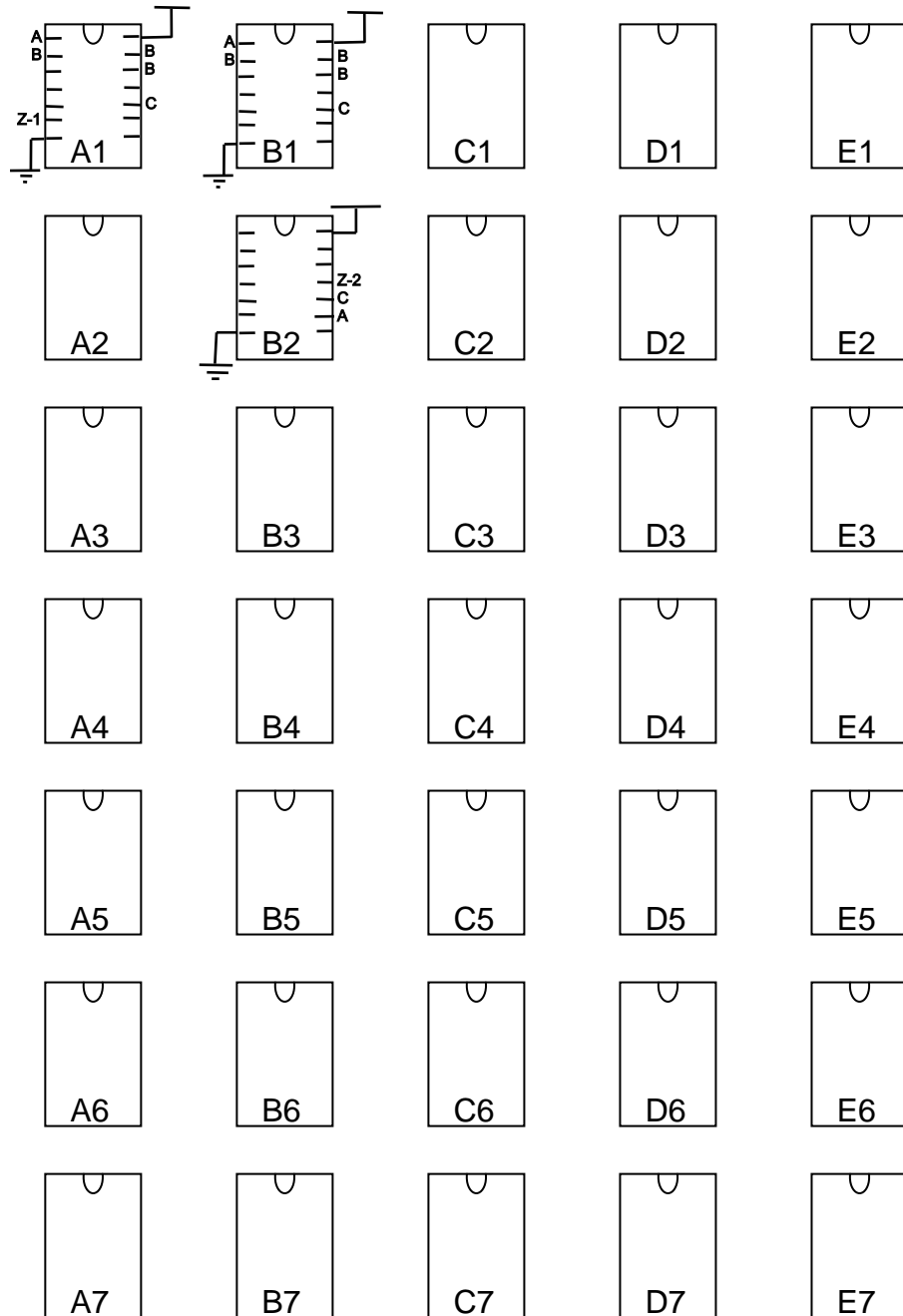
Pre-Lab Part A:

Owen Shin
Lab 1

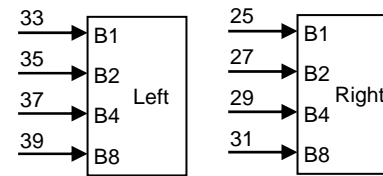
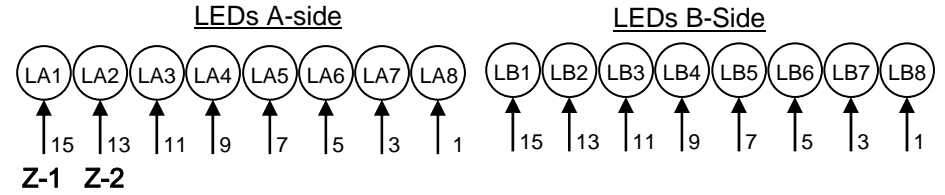


COMPONENT LAYOUT AND I/O ASSIGNMENT

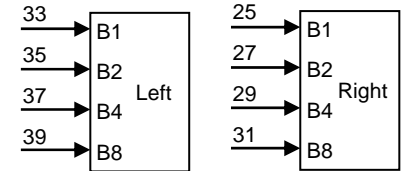
PROTOBOARD



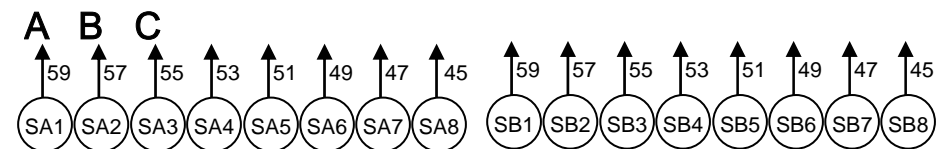
16-bit I/O BOARD



HEX A-side



HEX B-side



SWITCHES A-side

SWITCHES B-side