STEPHEN G. DONCHEZ

Master of Computer Engineering student at Villanova University in possession of a full secret security clearance with a passion for leadership and learning, seeking a career in embedded systems design and systems engineering

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EDUCATION

Villanova University, Villanova, PA   2020-2022
    Master of Science in Computer Engineering,
    GPA: 3.91
Villanova University, Villanova, PA   2016-2020
    Bachelor of Science in Computer Engineering,
    Computer Science Minor
    Graduated Magna Cum Laud
    GPA: 3.85

ENGINEERING COURSE EXPERIENCE

- Embedded Systems Architecture
- Advanced Digital Design Using FPGAs
- Computer Communications Networks
- Network and Computer Security
- Information Security and Data Protection
- Fundamentals of Electrical Engineering I
- Discrete Time Signals and Systems
- Computer Architecture
- Digital Design
- Trusted Computing

PROJECTS

- Secure Remote DPR Architecture for Embedded Systems (Master's thesis, in progress) – designing and implementing a architecture level solution for secure remote dynamic partial reconfiguration of heterogenous embedded systems.
- PrintCheck3D – Developed a 3D printer monitoring and management system that features automatic print failure detection and waste mitigation as well as printer availability information for the Villanova Community.

TECHNICAL SKILLS

- Coding Experience: VHDL, SQL, C++, C, PHP, Git, Assembly (various versions), Bash Shell (Linux), Matlab, HTML, CSS, JavaScript (Vue, jQuery), Python, Arduino, Java, XML, PowerShell
- FPGA and microprocessor control, fundamentals of AC and DC circuit design and analysis, Requirements Management (DOORS), Agile lifecycle

WORK EXPERIENCE

Member of the Engineering Staff/Systems Engineer, *L3Harris Technologies*
- Work to architect, design, implement and integrate embedded systems providing a varied set of features for land, sea, air, and space environments
- Support Software/Firmware/Hardware design efforts

Systems Engineering Intern, *L3Harris Technologies*   2020 - 2022
- Aided in the integration of a next generation Electronic Warfare (EW) system
- Performed in-depth reviews of technical drawings and specifications
- Interfaced with outside vendors to address design discrepancies
- Worked to perform threat simulations in an anechoic environment

Systems Engineering Intern, *Harris Corporation*   2019
- Performed a comprehensive risk management analysis of an electronic warfare (EW) system
- Authored and performed unit and integration testing on EW systems to certify them prior to release
- Performed design documentation work on EW hardware
- Evaluated various software techniques for operating environment configuration and deployment

Software Engineering Intern, *Harris Corporation*   2018
- Aided in the development of software across all levels of design (from OE configuration to application development)
- Performed unit testing on prototype hardware in the EW domain
- Obtained (and maintain) Secret Security Clearance

President Emeritus and Head Technician, *Villanova University Tech Crew*   2016-
- Oversaw the day-to-day operations of a $100,000/year student run organization within Villanova University,   Present
  including client interaction, equipment purchasing and maintenance, financial management and budgeting, and
  personnel management, as well as strategic and long-term business development.
- Provide event production services for a variety of clients within Villanova University, including concert scale sound, video, and lighting systems
- Install and operate small and large-scale event production systems on a weekly basis