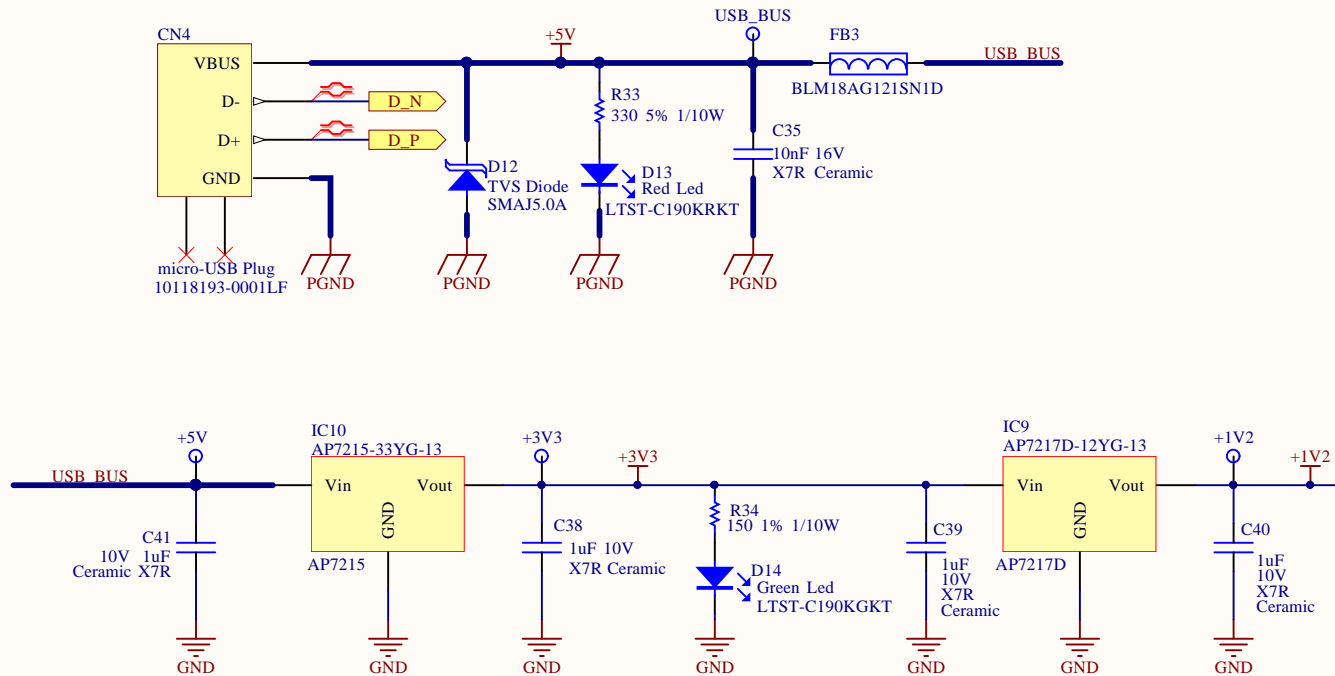


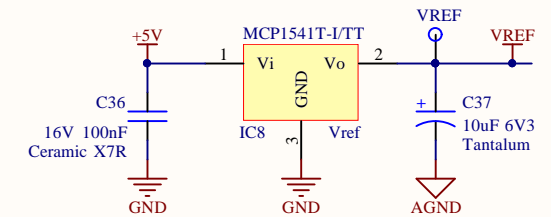
Title		
MercurialSDR - Block Diagram		
Size	Number	Revision
A4		Rev1.0
Date:	17/09/2019	Sheet 1 of 6
File:	C:\Users\...\MercurialSDR_BLK.SchDoc	Drawn By: Andres Hojnadel

# Power Stage

## USB to +3V3 and +1V2

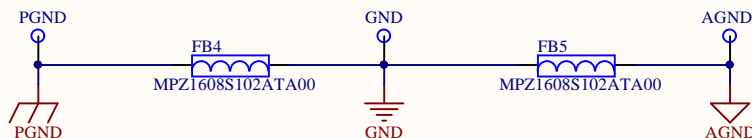


## Reference Voltage



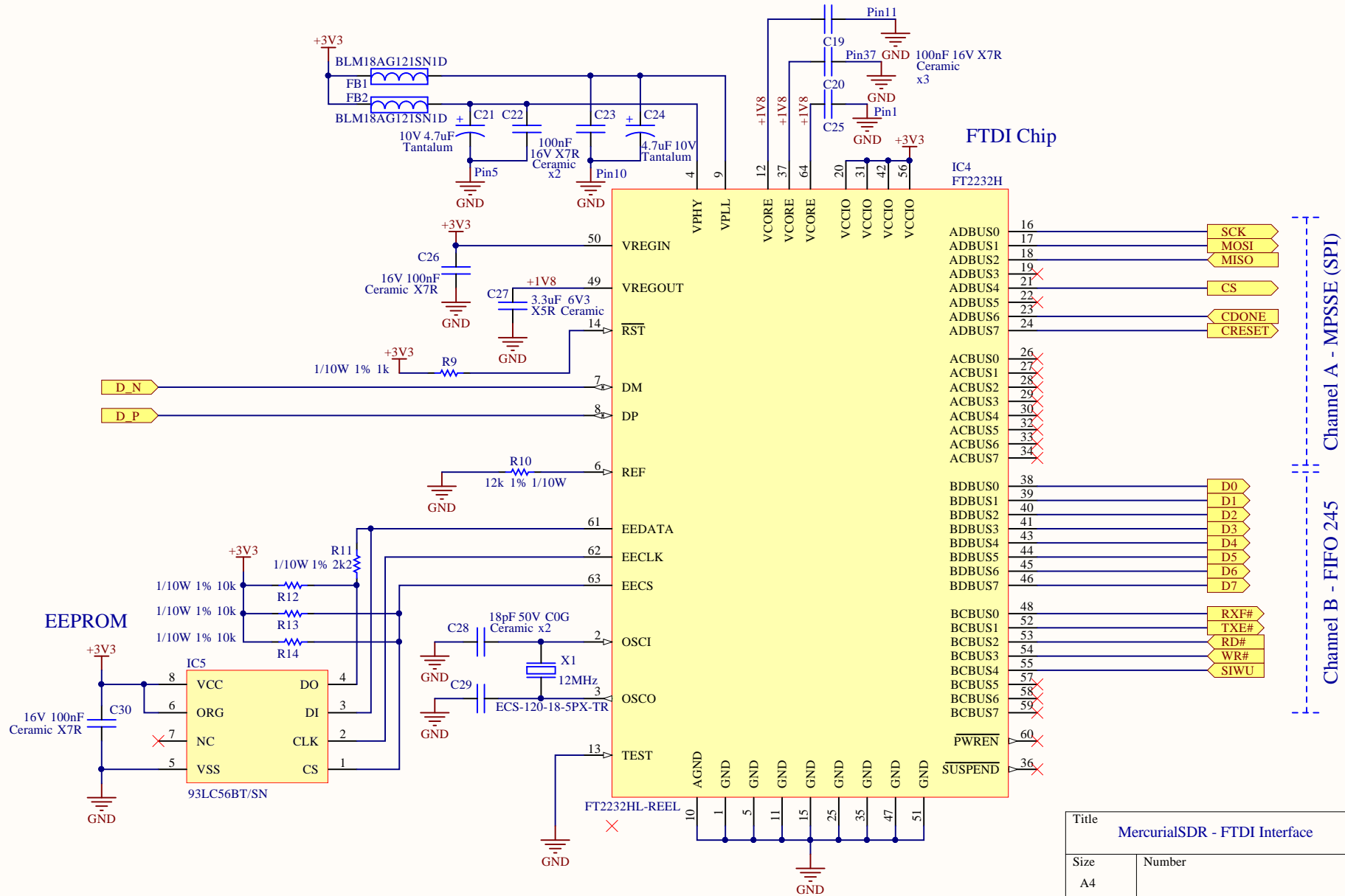
This is a precision voltage reference for DAC.  
Them must be close.

## Ground Connections



Title MercurialSDR - Power Stage		
Size A4	Number	Revision Rev1.0
Date:	17/09/2019	Sheet 2 of 6
File:	C:\Users\...\MercurialSDR_PWR.SchDoc	Drawn By: Andres Hojnadel

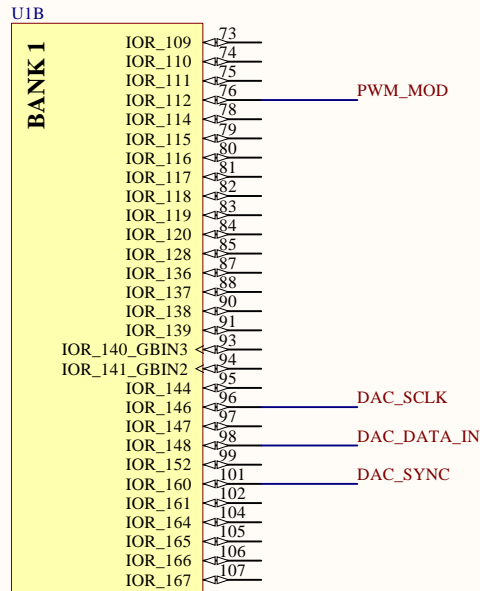
# FTDI Interface



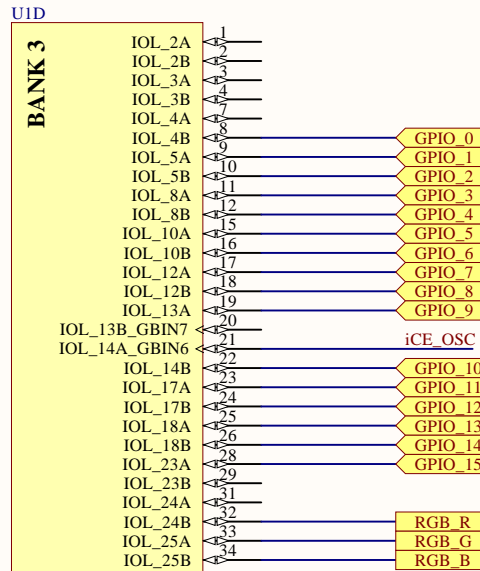
Title		
MercurialSDR - FTDI Interface		
Size	Number	Revision
A4		Rev1.0
Date:	17/09/2019	Sheet 3 of 6
File:	C:\Users\...\MercurialSDR_FTDI.SchDoc	Drawn By: Andres Hojnadel



# FPGA Banks

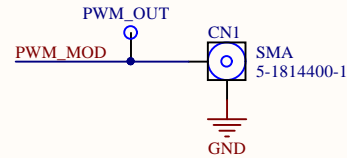


ice40HX4K-TQ144

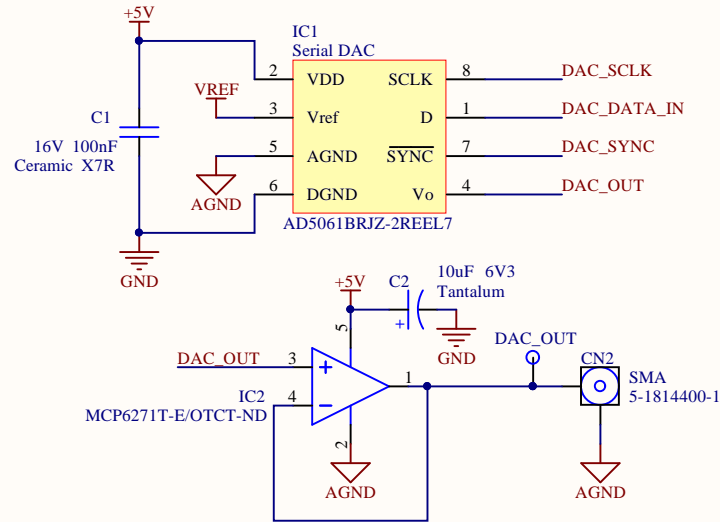


ice40HX4K-TQ144

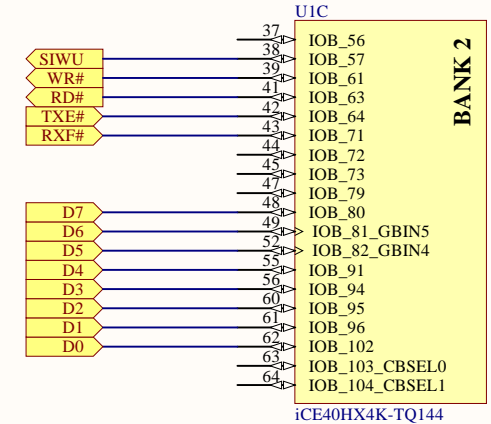
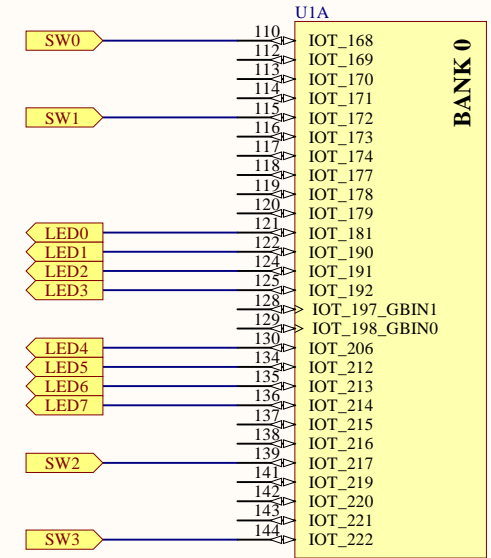
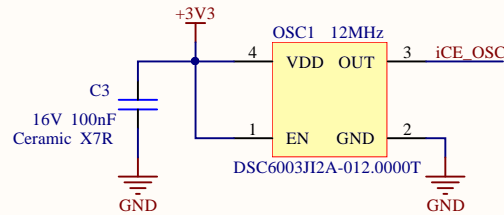
## One-Pin-RF Output



## Analog Output



## iCE Oscillator

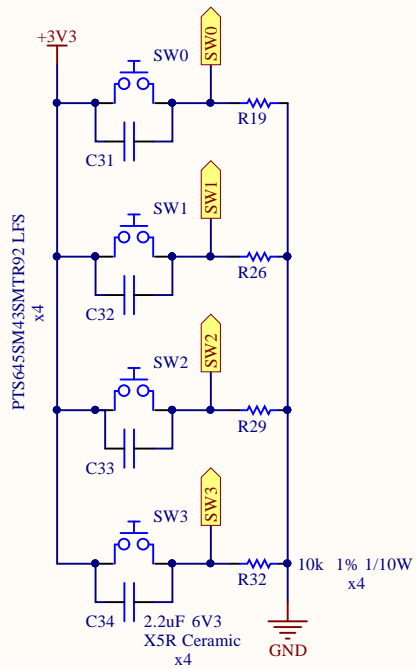


FIFO 245

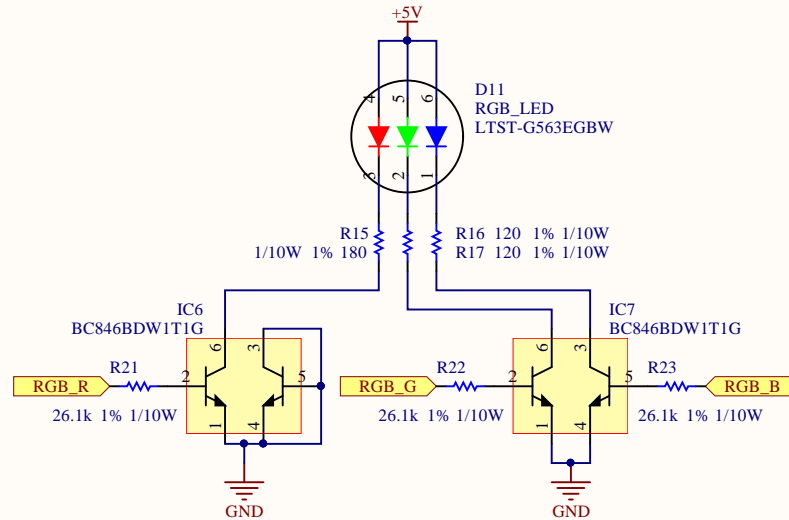
Title		
MercurialSDR - iCE Banks		
Size	Number	Revision
A4		Rev1.0
Date:	17/09/2019	Sheet 5 of 6
File:	C:\Users\...\MercurialSDR_FPGA_BNK_SDR	Drawn By: Andres Hojnadel

# Miscellaneous

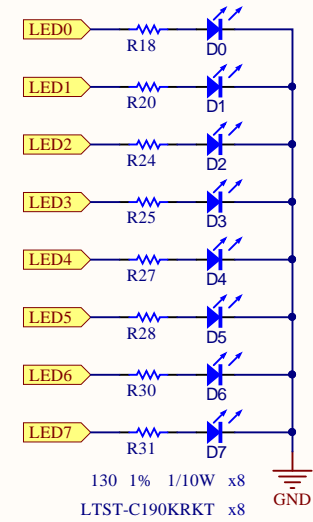
## Buttons



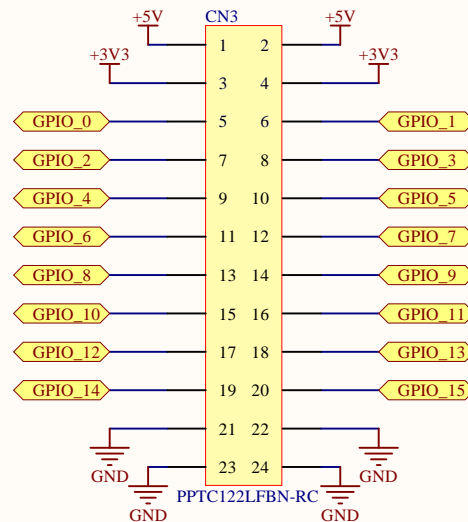
## RGB Led



## 8-Led Array



## GPIO Ports



Title		
MercurialSDR - Miscellaneous		
Size	Number	Revision
A4		Rev1.0
Date:	17/09/2019	Sheet 6 of 6
File:	C:\Users\...\MercurialSDR_MISC.SchDoc Drawn By: Andres Hojnadel	