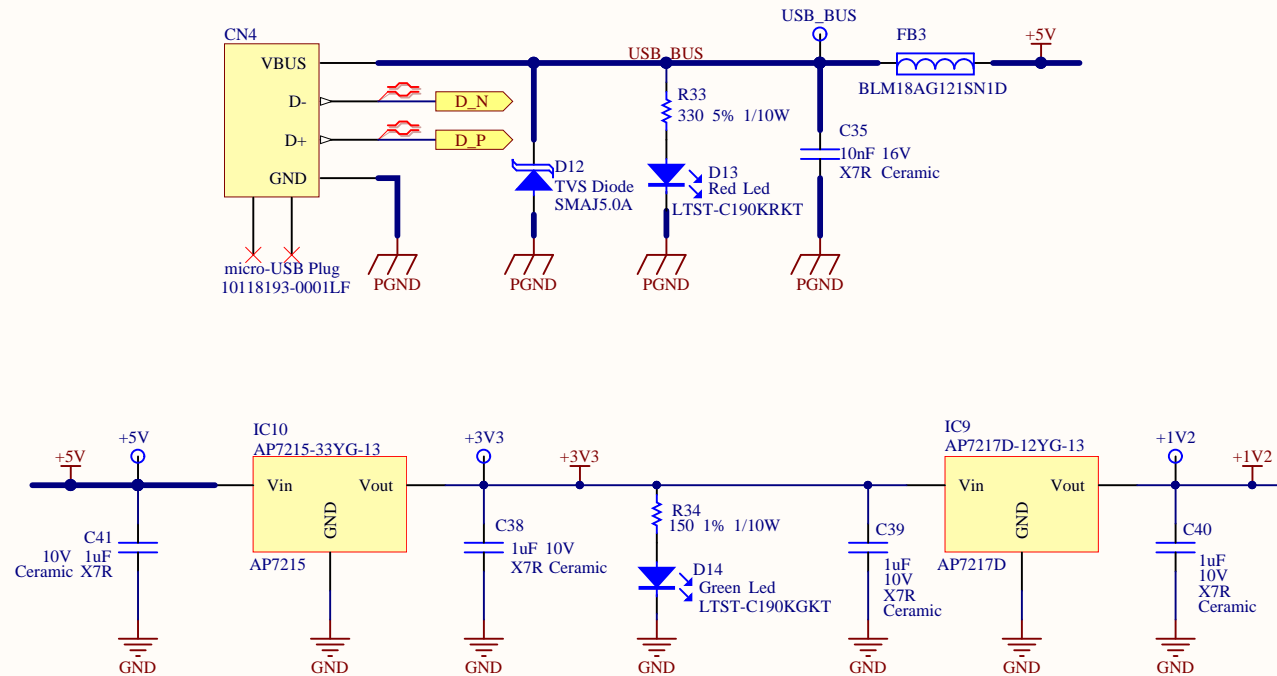


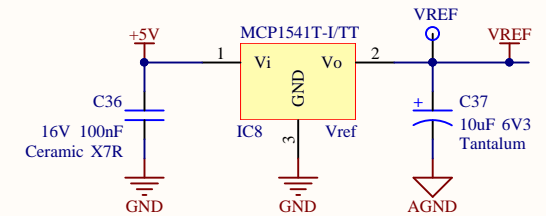
Title		
MercurialSDR - Block Diagram		
Size	Number	Revision
A4		Rev1.0
Date:	21/09/2019	Sheet 1 of 6
File:	C:\Users\...\MercurialSDR_BLK.SchDoc	Drawn By: Andres Højnadel

Power Stage

USB to +3V3 and +1V2

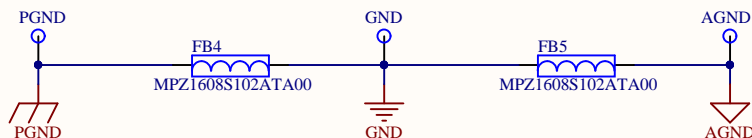


Reference Voltage



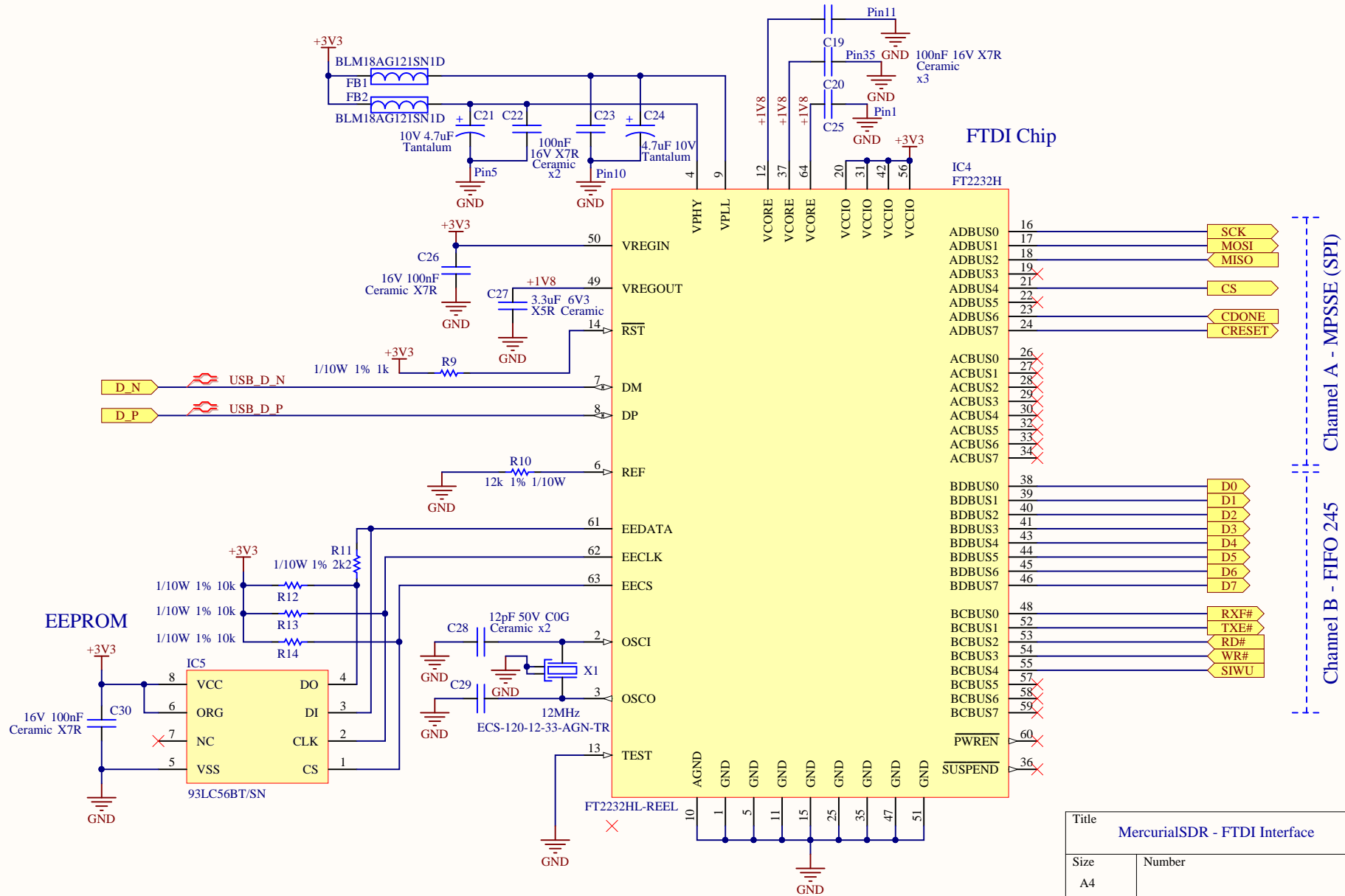
This is a precision voltage reference for DAC (IC1).
Them must be close.

Ground Connections



Title MercurialSDR - Power Stage		
Size A4	Number	Revision Rev1.0
Date:	21/09/2019	Sheet 2 of 6
File:	C:\Users\...\MercurialSDR_PWR.SchDoc	Drawn By: Andres Hojnadel

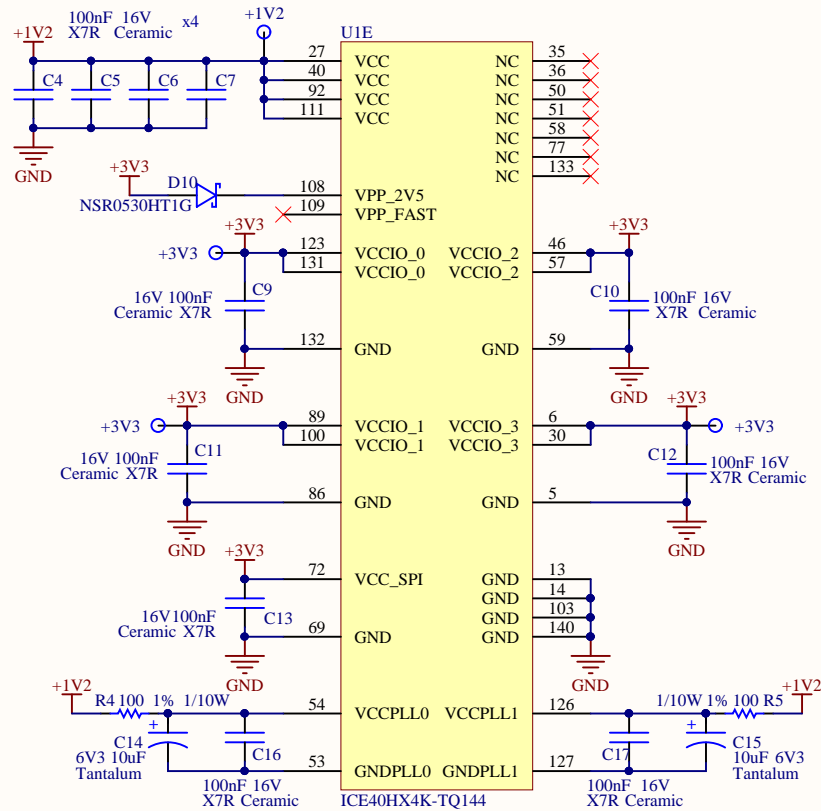
FTDI Interface



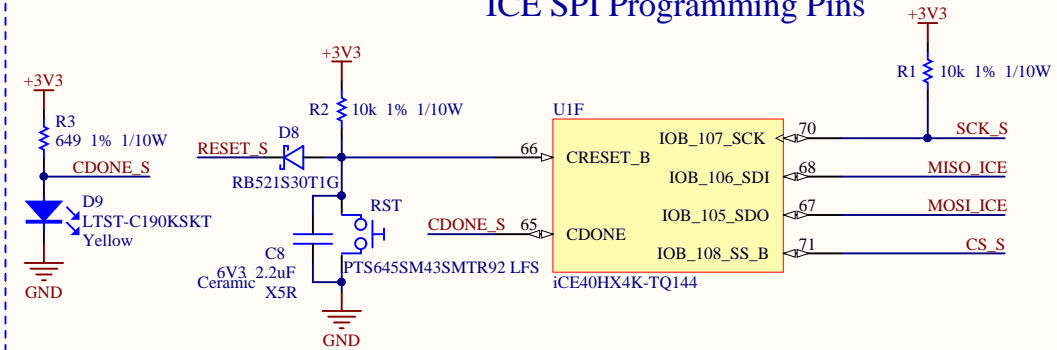
Title		
MercurialSDR - FTDI Interface		
Size	Number	Revision
A4		Rev1.0
Date:	21/09/2019	Sheet 3 of 6
File:	C:\Users\...\MercurialSDR_FTDI.SchDoc	Drawn By: Andres Hojnadel

ICE Programming & Power

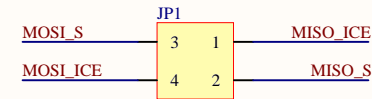
iCE Power



ICE SPI Programming Pins



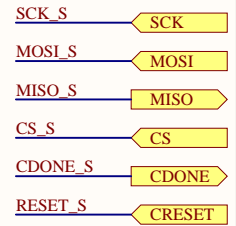
ICE/Flash programming selection



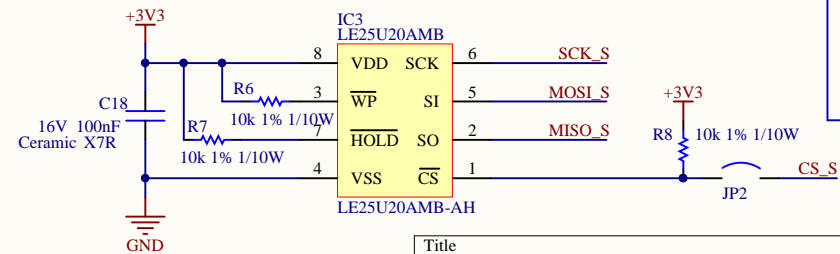
For programming Flash: shunt 1-3 & 2-4

For programming ICE: shunt 1-2 & 3-4

FTDI Ports



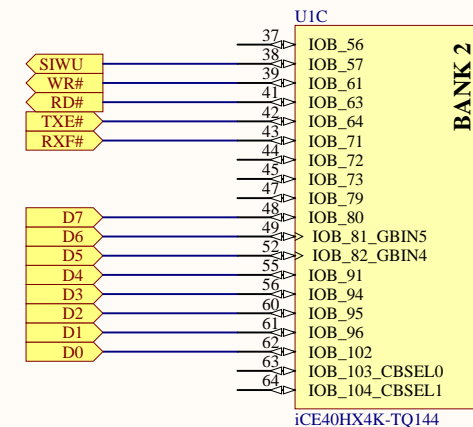
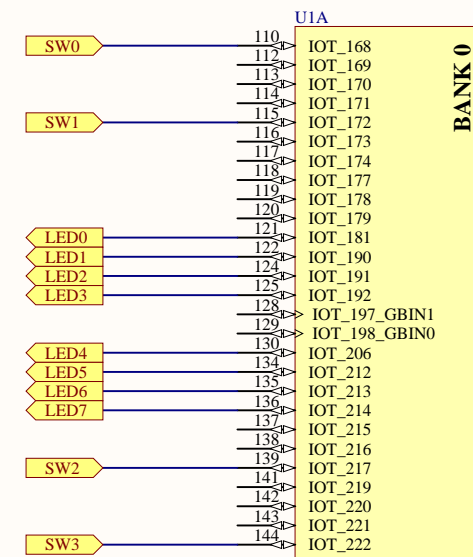
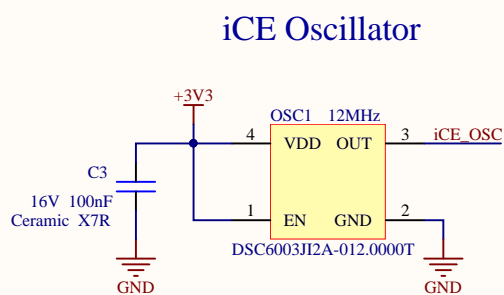
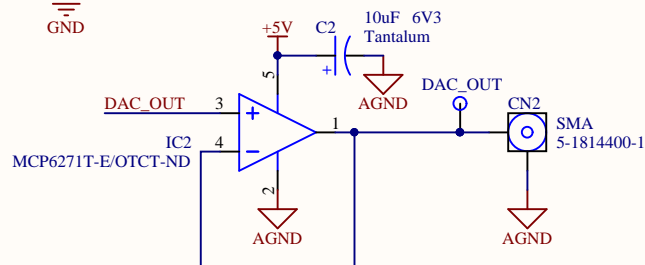
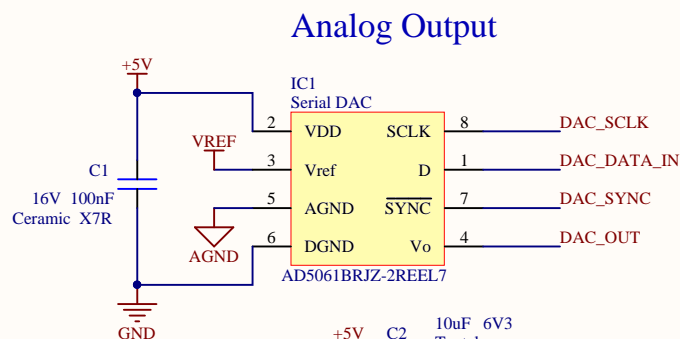
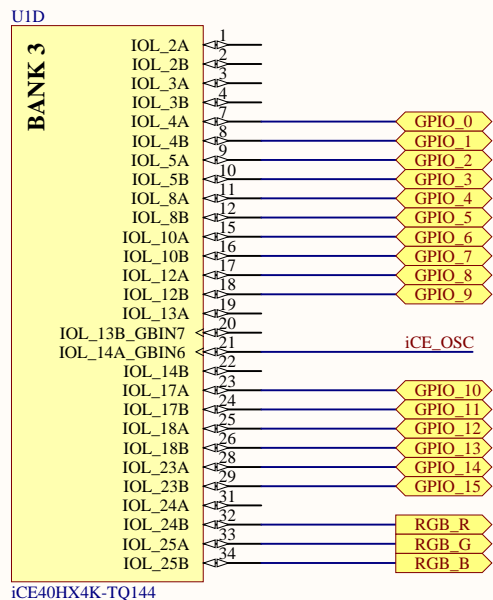
Flash Memory



Remove JP2 only for programming ICE
Shunt JP2 for Flash programming or normal operation

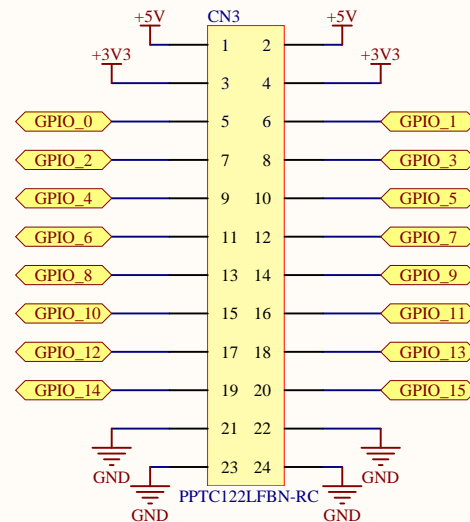
Title MercurialSDR - iCE Power & Program.		
Size A4	Number	Revision Rev1.0
Date:	21/09/2019	Sheet 4 of 6
File:	C:\Users\...\MercurialSDR_FPGA_P&P_Sch	Drawn By: Andres Hojnadel

One-Pin-RF Output



Title MercurialSDR - iCE Banks		
Size A4	Number	Revision Rev1.0
Date: 21/09/2019	Sheet 5 of 6	Drawn By: Andres Hojnadel
File: C:\Users\...\MercurialSDR_FPGA_BNK_Sch		

RGB Led



PTS645SM43SMT R92 LFS

x4

+3V3

SW0

C31

R19

SW1

C32

R26

SW2

C33

R29

SW3

C34

2.2uF 6V3 X5R Ceramic

x4

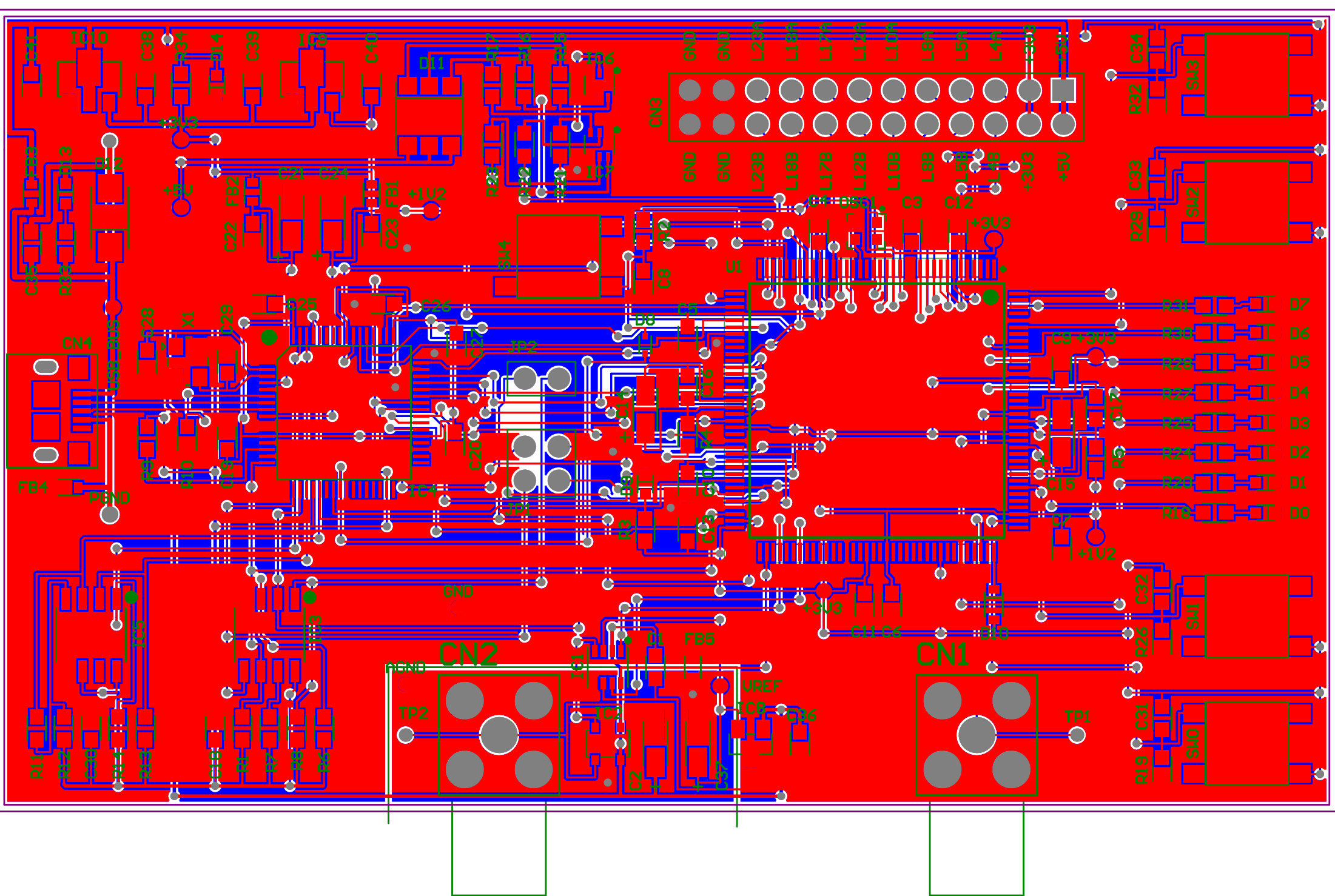
10k

1% 1/10W

x4

GND

Title MercurialSDR - Miscellaneous		
Size A4	Number	Revision Rev1.0
Date: 21/09/2019	Sheet6 of 6	Drawn By: Andres Højnadel
File:	C:\Users\...\MercurialSDR_MISC.SchDoc	



- D7
- D6
- D5
- D4
- D3
- D2
- D1
- D0