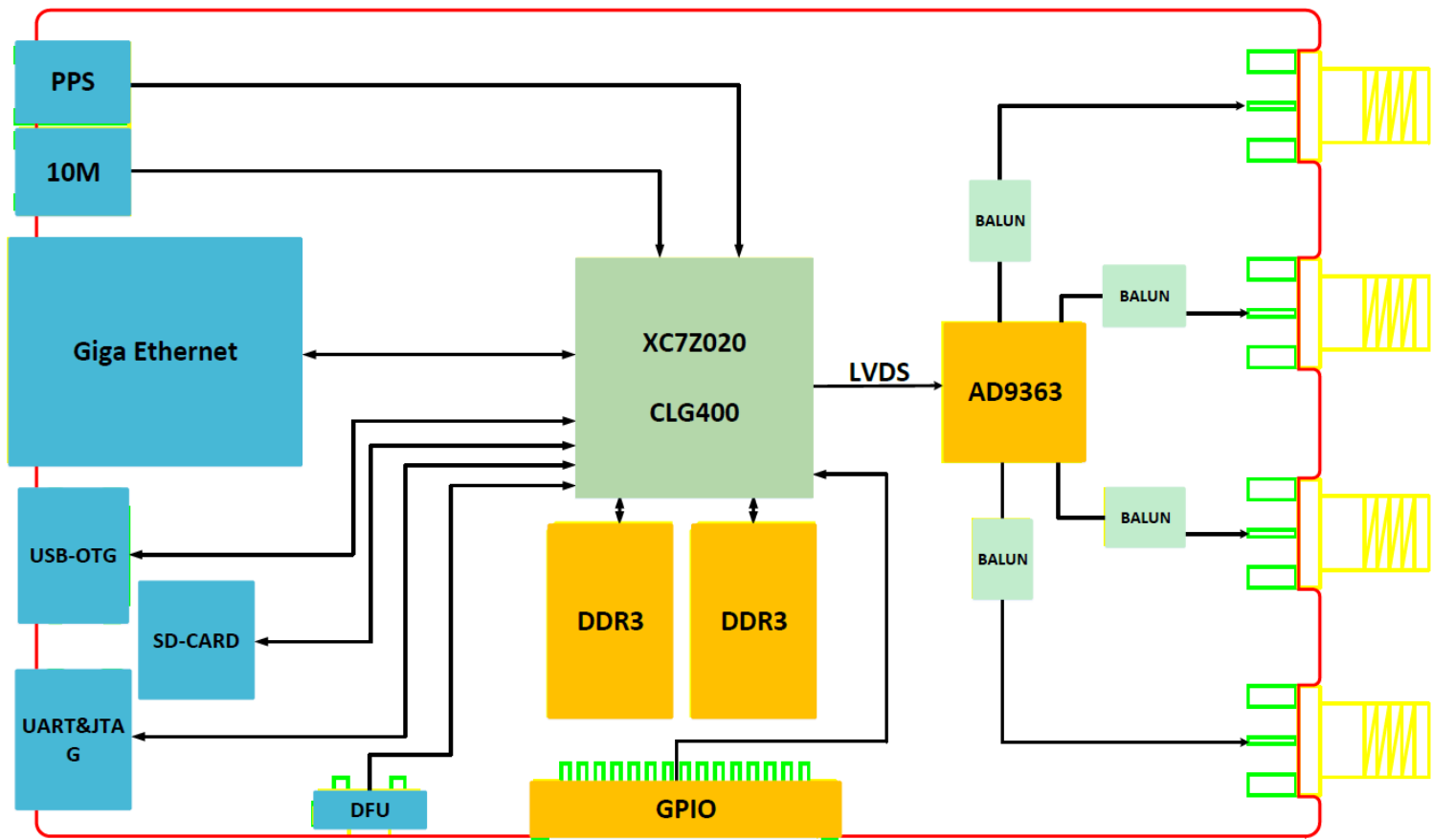
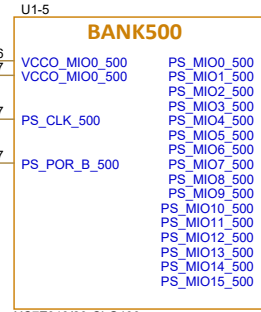
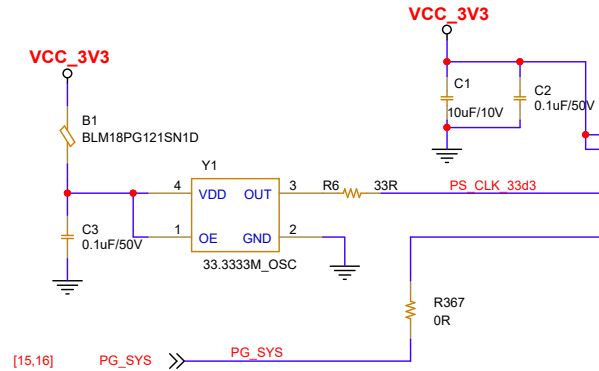


# AIDO SDR Block Diagram

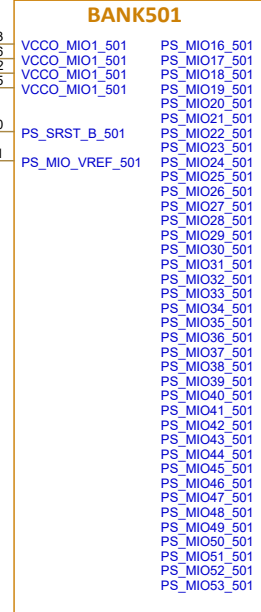


| PAGE | DESCRIPTION                                   |
|------|---|
| 1    | Title, Notes, Block Diagram, Revision History |
| 2    | PS BANK500&501                                |
| 3    | PS BANK502                                    |
| 4    | PL BANK0&13                                   |
| 5    | PL BANK34&35                                  |
| 6    | ZYNQ Power                                    |
| 7    | DDR3 RAM                                      |
| 8    | PS ETH  |
| 9    | USB   |
| 10   | EEPROM  |
| 11   | AD9363  |
| 12   | RF  |
| 13   | CLK   |
| 14   | USB-JTAG                                      |
| 15   | Power   |
| 16   | Power   |
| 17   |   |
| 18   |   |
| 19   |   |
| 20   |   |
| 21   |   |
| 22   |   |
| 23   |   |
| 24   |   |
| 25   |   |
| 26   |   |
| 27   |   |
| 28   |   |
| 29   |   |

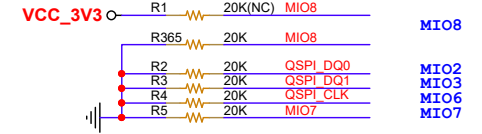
|          |                          |               |
|----------|--------------------------|---------------|
| Title    |                          |               |
| AIDO-SDR |                          |               |
| Size B   | Document Number          | Rev           |
|          | Block Diagram            | 1.0           |
| Date:    | Wednesday, July 23, 2025 | Sheet 1 of 16 |



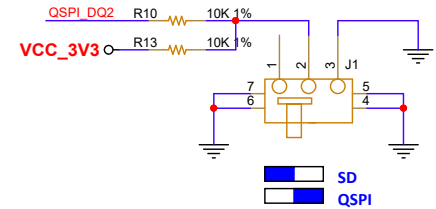
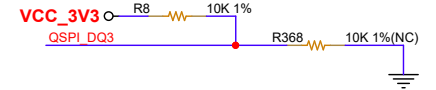
XC7Z010/20-CLG400  
U1-6



XC7Z010/20-CLG400

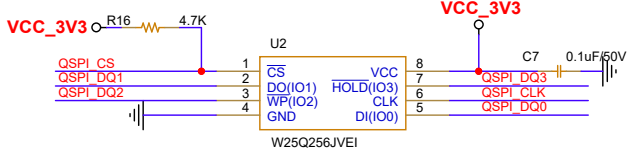


MIO[8] =1 ----MIO bank1 voltage=1.8V  
MIO[2] =0 ----Cascaded JTAG  
MIO[3] =0 ----JTAG/NAND/Quad-SPI/SD  
MIO[6] =0 ----PLL used  
MIO[7] =0 ----MIO bank0 voltage=3.3V

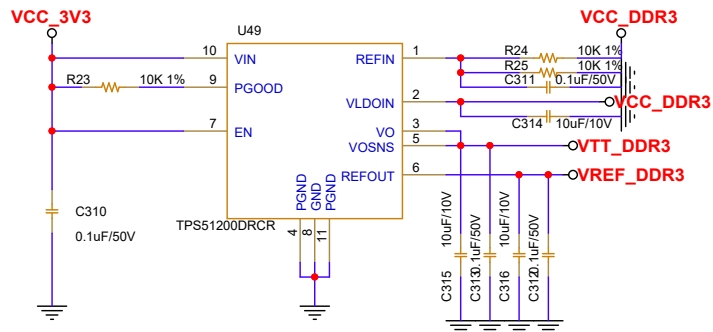
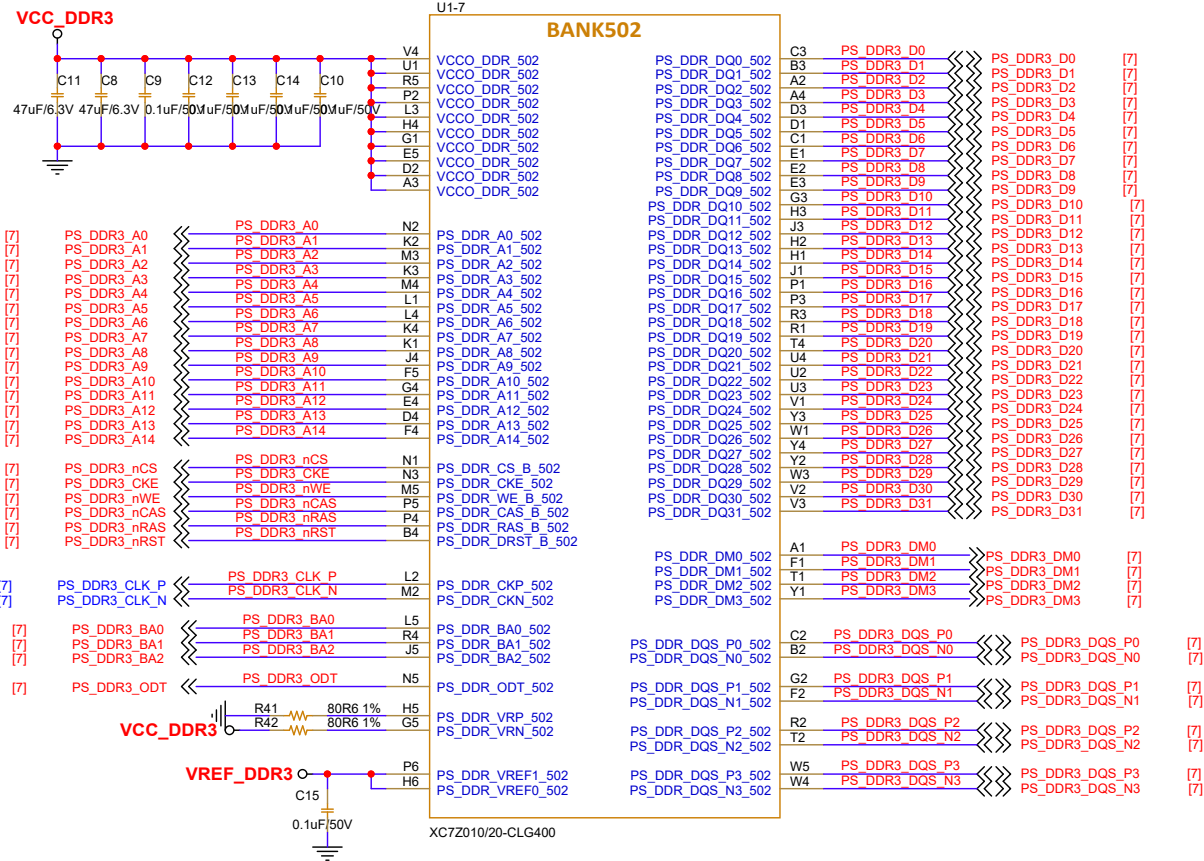


SD  
QSPI

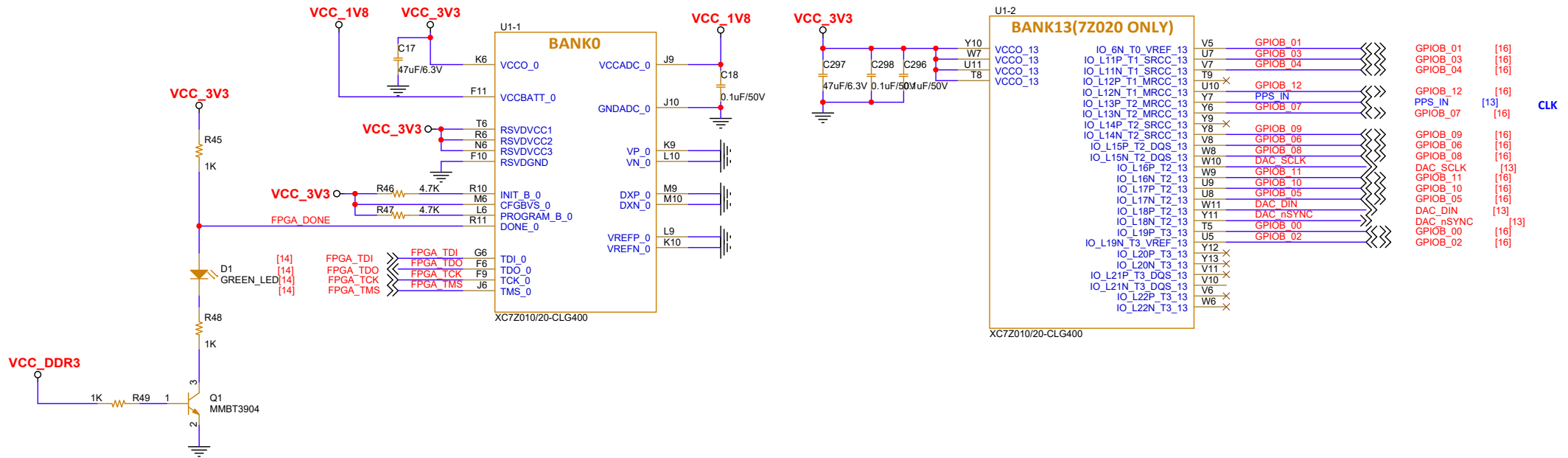
| Boot Mode | MIO[5]<br>(QSPI_DQ3) | MIO[4]<br>(QSPI_DQ2) |
|-----------|----------------------|----------------------|
| JTAG      | 0                    | 0                    |
| NAND      | 0                    | 1                    |
| QSPI      | 1                    | 0                    |
| SD Card   | 1                    | 1                    |



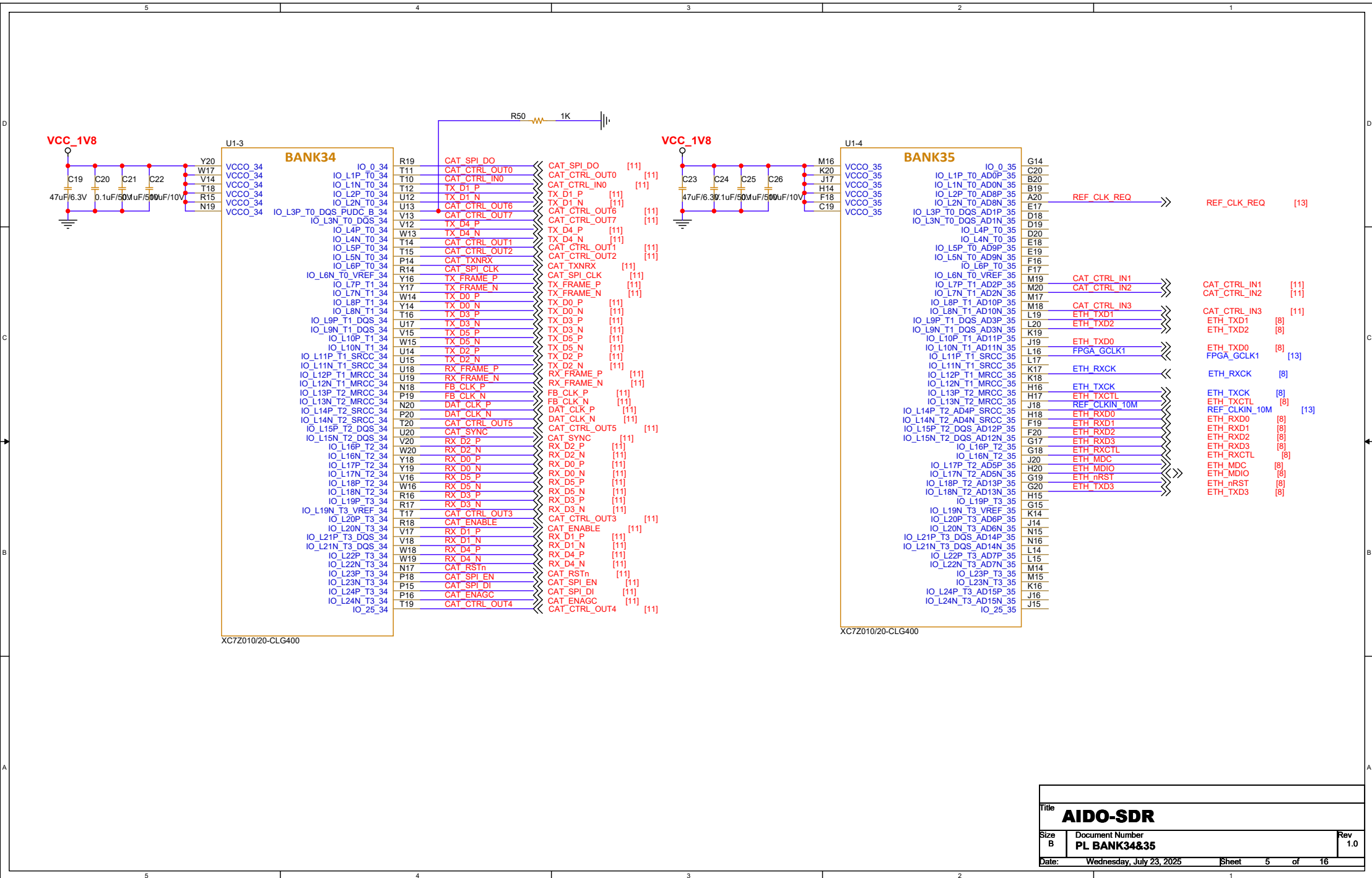
W25Q256JVEI



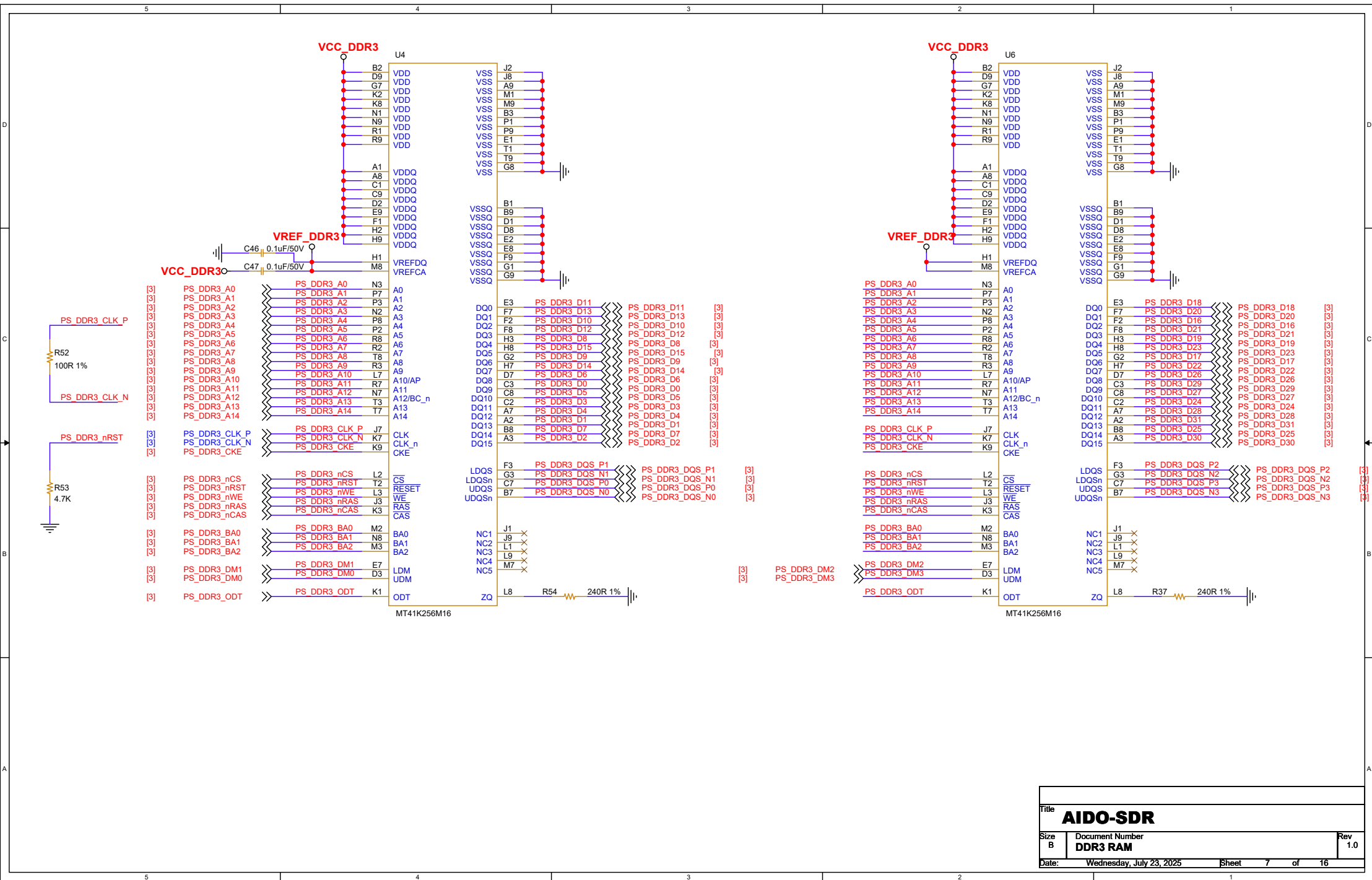
# ZYNQ

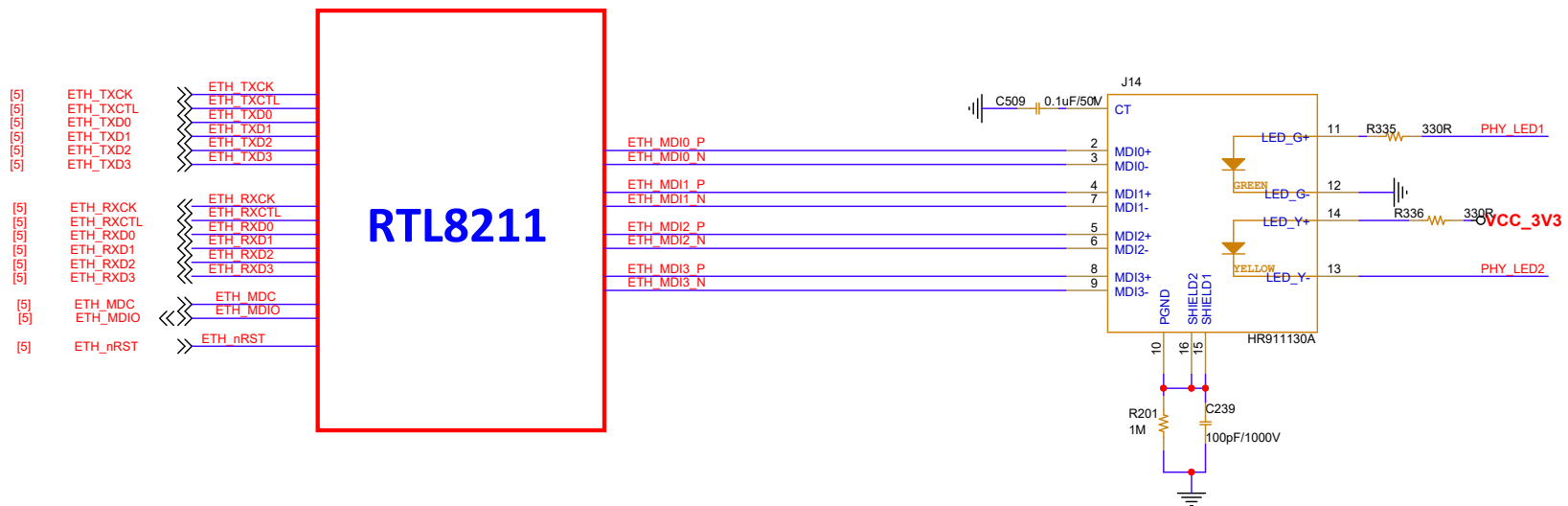


|                 |   |       |   |            |
|-----------------|---|-------|---|------------|
| Title           |   |       |   |            |
| <b>AIDO-SDR</b> |   |       |   |            |
| Size<br>B       | Document Number<br><b>PL BANK0&amp;13</b> |       |   | Rev<br>1.0 |
| Date:           | Wednesday, July 23, 2025                  | Sheet | 4 | of 16      |



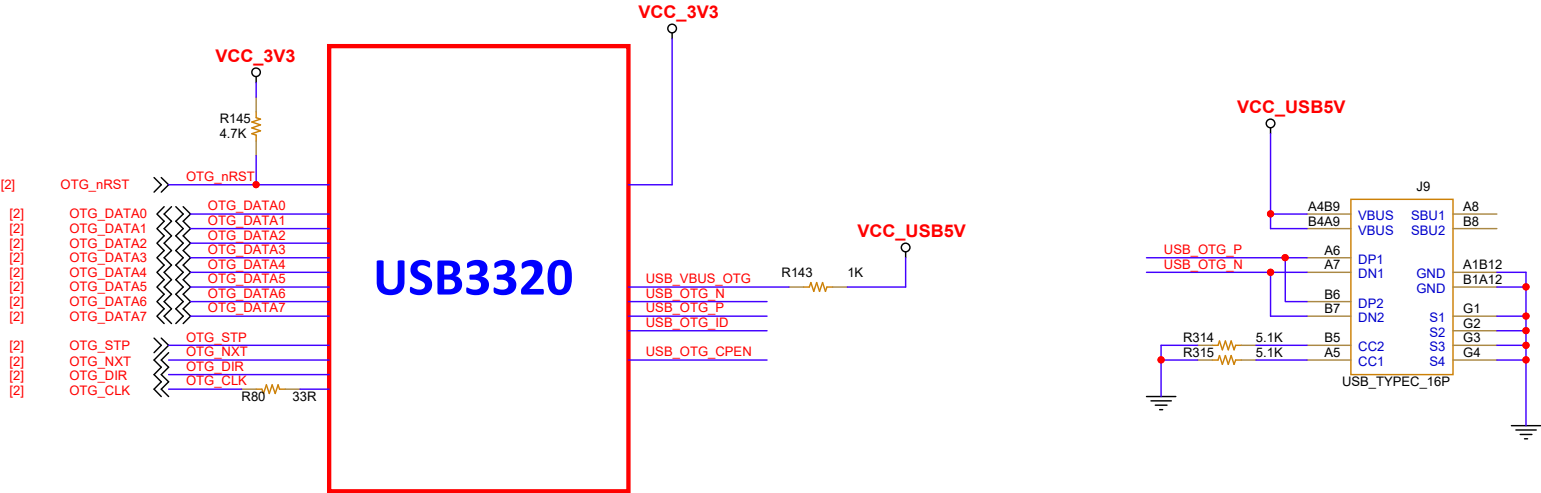




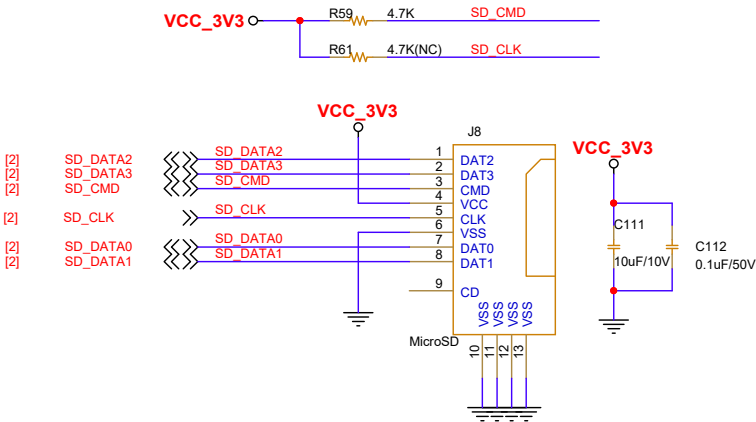




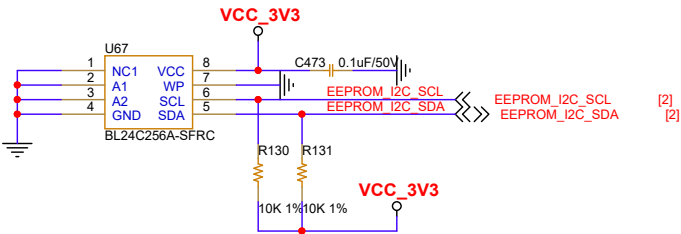
PS USB OTG

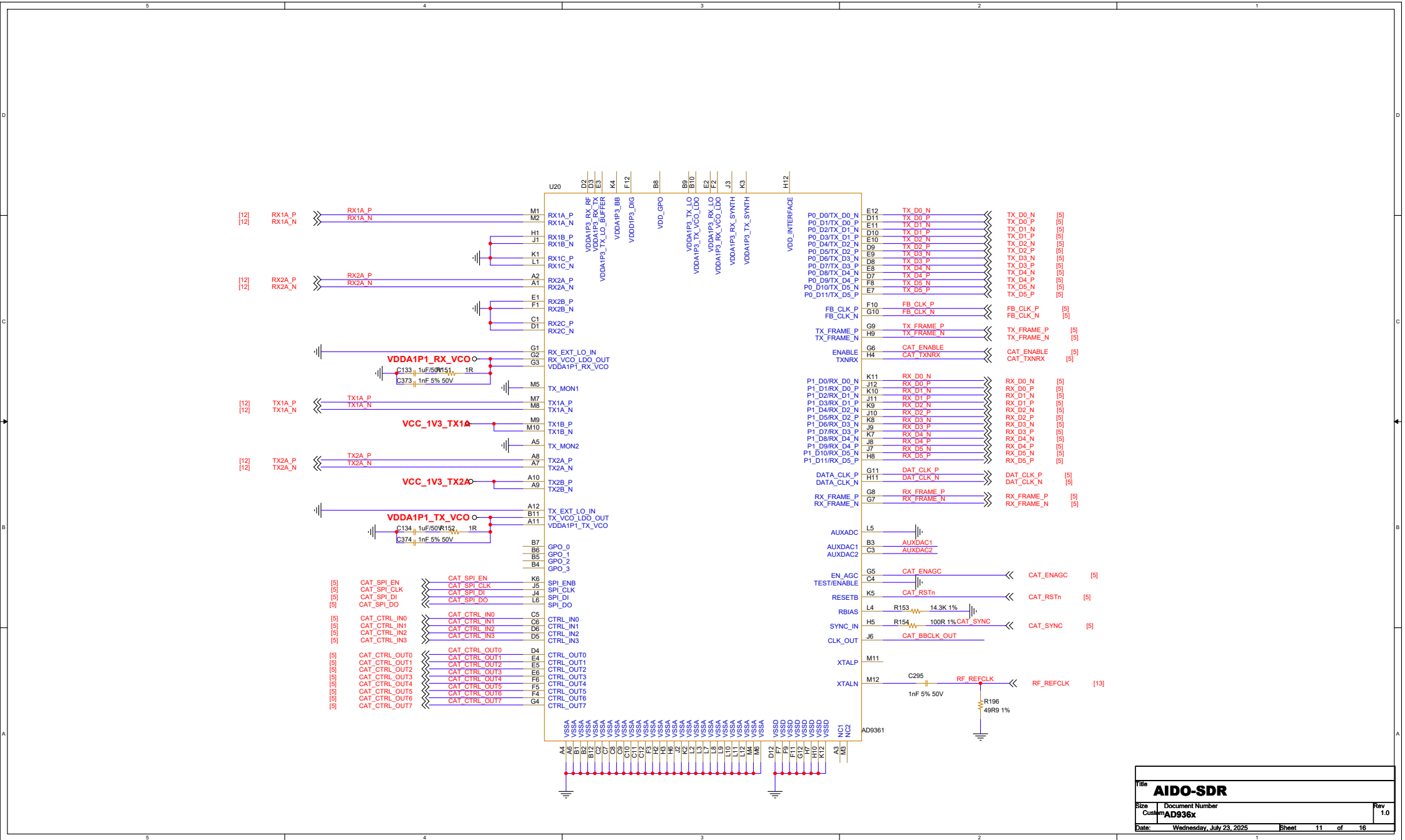


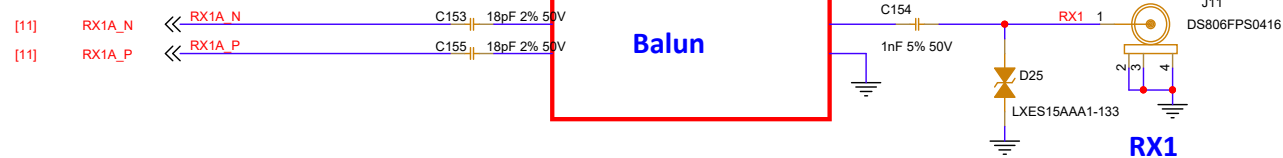
SD CARD



EEPROM

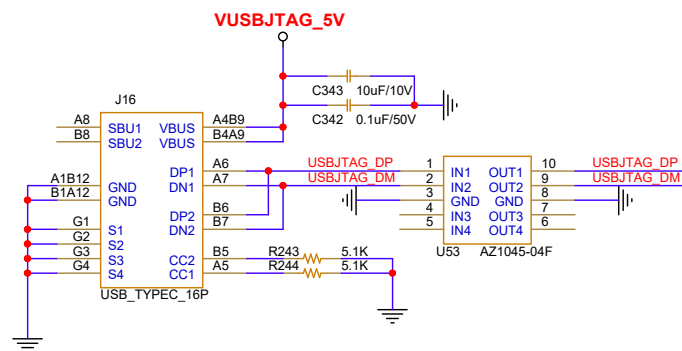




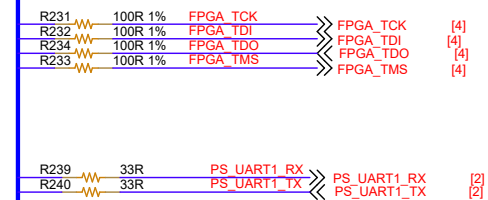


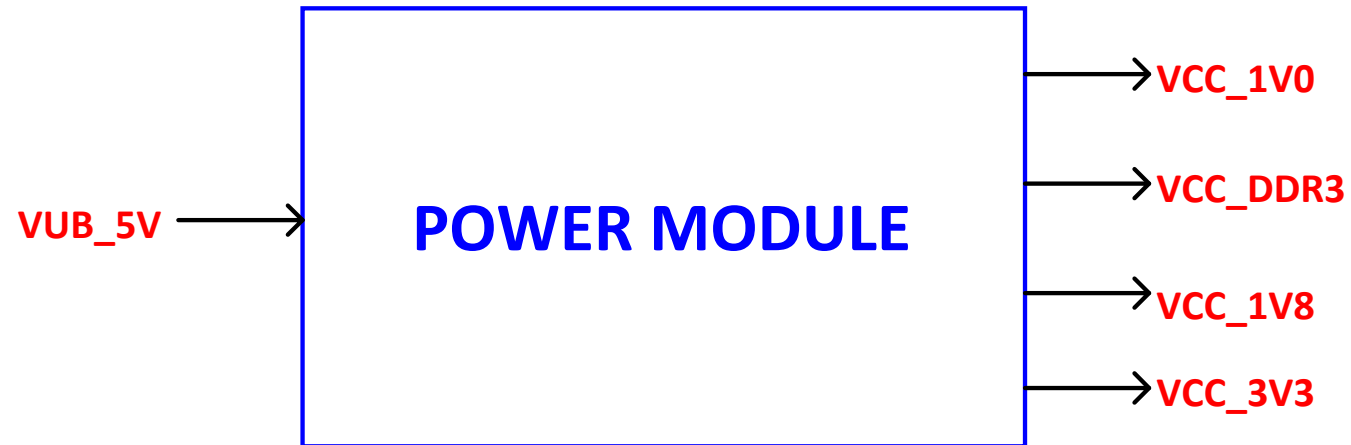
|                 |                          |                |
|-----------------|--------------------------|----------------|
| Title           |                          |                |
| <b>AIDO-SDR</b> |                          |                |
| Size B          | Document Number          | Rev 1.0        |
| <b>RF TX/RX</b> |                          |                |
| Date:           | Wednesday, July 23, 2025 | Sheet 12 of 16 |

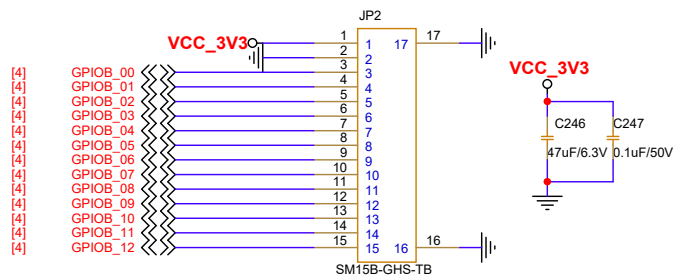




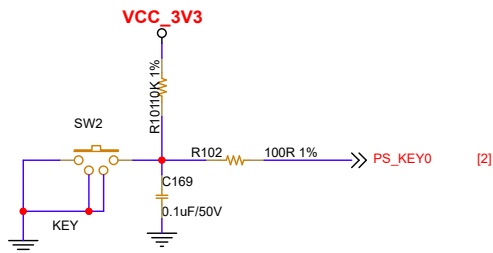
# USB- JTAG&UART



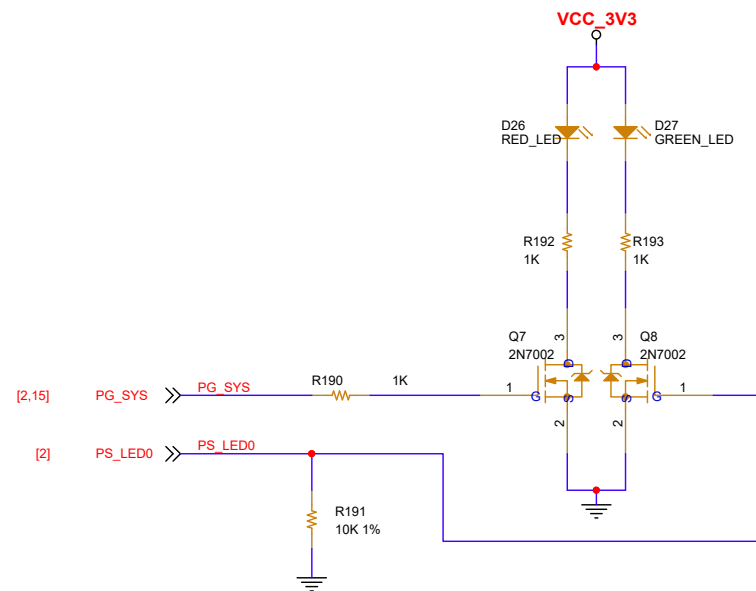




## Panel DFU



## LED



|          |                          |                |
|----------|--------------------------|----------------|
| Title    |                          |                |
| AIDO-SDR |                          |                |
| Size B   | Document Number          | Rev 1.0        |
| GPIO&KEY |                          |                |
| Date:    | Wednesday, July 23, 2025 | Sheet 16 of 16 |