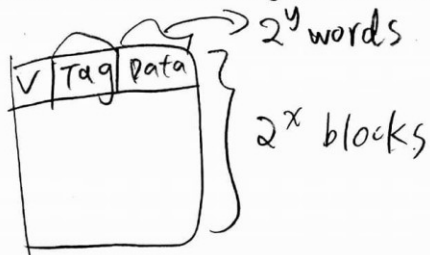


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A direct-mapped cache is designed based on 32-bit "word" address. It contains 2^x blocks, and each block has 2^y words. How large is the cache?



$$\text{Tag} = 32 - x - y$$

$$\text{Ans} = (1 + 32 - x - y + 2^y \cdot 32) \times 2^x \text{ bits}$$