Resume by Sean C.H. Lin

RF Testing S/W Developing Engineer · Algorithm Developing Engineer · Automated Program Developing Engineer · Physiological Signal Processing Engineer · Data analyst · Researcher

English Resume: http://bit.ly/2lWmoMa

Personal Information

Name: Chun-Hao Lin (Sean) Gender: Male

Birthday: 18/June/1984 Nationality: Taiwanese Mail: seanlin0618@outlook.com Phone: +886-918073618

Address: No.114, Yongchun Ln., Yanhe St., Linluo Township, Pingtung County 909, Taiwan (R.O.C.)

Education

National Chiao Tung University, Hsinchu, Taiwan

2013/09 - 2017/09

Institute of Electrical and Control Engineering

Master degree

Feng Chia University, Taichung, Taiwan

2003/09 - 2007/09

Department of Information Engineering and Computer Science

Bachelor degree

Familiar Domains

Wireless Technologies, Automation Verification/Testing, Data Analysis, Bio-ICT, Image Processing, Machine Learning

Professional Skills

Computer Language

Developing Environment

Language

1. C/C++ (10 years)

2. Matlab, OpenCV in physiological info.

domain (4 years)

- 3. Qt framework (4 years)
- 4. Python (2 years)
- 5. Oracle SQL (1 years)

Windows Ubuntu

English Mandarin

Experience



Distinguished Professor Bing-Fe Wu's CSSP Lab, ECE, NCTU \cdot Master Student \cdot September 2013 - September 2017



- 1. Image process in BioICT filed.
- 2. Developing algorithm for using image only to detect vital sign heart rate.

macOS

- 3. Developing algorithm for using image only to estimate vital sign blood pressure.
- 4. Developing program to access and abstract data from open physiological database.

- 5. Establishing private physiological database.
- 6. Apply machine learning in physiological info. field.
- 7. Developing algorithm in Matlab and speed up by rewriting it in C/C++.

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Azurewave Inc. · Deputy Project Manager · March 2014 - now



- 1. Developing 1st version program of verification/testing PLC (Power Line Communication) product.
- 2. Developing program of verification/testing NB-IoT.
- 3. Co-work with IT and consulting firm to phase in factory 4.0.
- 4. Internal source code version control system research/verification/phase in (Git for server and client side both).
- 5. Reviewing source code, release program and documents.
- 6. To hold internal training by H/W and S/W demand.



Azurewave Inc. · Senior Engineer · March 2011 - February 2014



- 1. Developing log parser and statistic tool for monitoring product quality.
- 2. Co-work with test line to build shop floor.
- 3. Developing program of verification/testing Marvell IoT(Internet of Things) platform.
- 4. ATE(Automatic Testing Equipment) developing, maintaining and flow optimizing.
- 5. To join Apple iMac Pro wireless module project. (be responsible for verification and testing part).



Azurewave Inc. · Engineer · October 2008 - February 2011



- 1. Be familiar with DVT-EVT-PVT-MP product develop flow.
- 2. Developing 1st version program of verification/testing FM/RDS technologies product.
- 3. Developing 1st version program of verification/testing GPS technology product.
- 4. Developing program of verification/testing Broadcom WiFi/BT IC.
- 5. SLT Handler(System Level Test Handler) verification, debugging, phasing in and mass deployment.
- 6. To join the project of wireless SiP(System in Package) module manufacturing.

Recent Projects

Internal Training 1

In this internal training, I present WiFi RF signaling test method and flow. The comparison data of signaling and non-signaling are also included. Finally, I share some troubleshooting tips in signaling test. Detail

Internal Training 2

In this internal training, I present a software skill called unit test and apply it to current NB-IoT project with Python language. Detail

Internal Training 3

In this internal training, I show the flow and related tool which design by me for H/W to statistic each test item and draw its graph. By this, it will reduce H/W loading in DVT stage. Detail

Master Thesis

Developing a system by using computer vision and several algorithm to achieve "measuring blood pressure remotely and cufflessly with off the shelf webcam". Detail

Reference

Name: Professor Bing-Fe Wu

Department: Institute of electrical and control engineering, National Chiao Tung University, Hsinchu, Taiwan

Title: Distinguished Professor (IEEE SMC Fellow)

E-mail: bwu@cc.nctu.edu.tw