# **LLVM**

**LLVM** is a set of <u>compiler</u> and <u>toolchain</u> technologies, [4] which can be used to develop a front end for any programming language and a <u>back end</u> for any <u>instruction set architecture</u>. LLVM is designed around a <u>language-independent intermediate</u> representation (IR) that serves as a <u>portable</u>, high-level <u>assembly language</u> that can be <u>optimized</u> with a variety of transformations over multiple passes.[5]

LLVM is written in C++ and is designed for compile-time, linkand "idle-time" optimization. Originally time, run-time, implemented for C and C++, the language-agnostic design of LLVM has since spawned a wide variety of front ends: languages with compilers that use LLVM include ActionScript, Ada, C#, [6][7][8] Common Lisp, PicoLisp, Crystal, CUDA, D, Delphi, Dylan, Forth, [9] Fortran, Graphical G, [10] Halide, Haskell, Java Kotlin, Lua. Objective-C, OpenCL.[11] bytecode, Julia, PostgreSQL's SQL and PLpgSQL, [12] Ruby, [13] Rust, Scala, [14] Swift, XC, [15] Xojo and Zig.

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## History

#### **LLVM**



The LLVM project started in 2000 at the <u>University of Illinois at Urbana–Champaign</u>, under the direction of <u>Vikram Adve</u> and Chris Lattner. The name *LLVM* was originally an initialism for

(https://www.llv m.org)

Low Level Virtual Machine. LLVM was originally developed as a research infrastructure to investigate dynamic compilation techniques for static and dynamic programming languages. LLVM was released under the University of Illinois/NCSA Open Source License, [3] a permissive free software licence. In 2005, Apple Inc. hired Lattner and formed a team to work on the LLVM system for various uses within Apple's development systems. [16] LLVM has been an integral part of Apple's Xcode development tools for  $\underline{\text{macOS}}$  and iOS since Xcode 4.[17]

The LLVM abbreviation has officially been removed to avoid confusion, as LLVM has evolved into an umbrella project that has little relationship to what most current developers think of as (more specifically) process virtual machines. Now, LLVM is a brand that applies to the LLVM umbrella project, the LLVM intermediate representation (IR), the LLVM debugger, the LLVM implementation of the C++ Standard Library (with full support of C++11 and  $C++14^{[19]}$ ), etc. LLVM is administered by the LLVM Foundation. Its president is compiler engineer Tanya Lattner. [20]

"For designing and implementing LLVM", the <u>Association for Computing Machinery</u> presented Vikram Adve, Chris Lattner, and <u>Evan Cheng</u> with the 2012 <u>ACM Software System Award</u>. [21]

Since v9.0.0, it was relicensed to the Apache License 2.0 with LLVM Exceptions. [3]

## **Features**

LLVM can provide the middle layers of a complete compiler system, taking <u>intermediate representation</u> (IR) code from a <u>compiler</u> and emitting an optimized IR. This new IR can then be converted and linked into machine-dependent <u>assembly language</u> code for a target platform. LLVM can accept the IR from the <u>GNU Compiler Collection</u> (GCC) <u>toolchain</u>, allowing it to be used with a wide array of existing compiler front-ends written for that project.

LLVM can also generate <u>relocatable machine code</u> at compile-time or link-time or even binary machine code at run-time.

LLVM supports a language-independent <u>instruction set</u> and <u>type system</u>. Each instruction is in <u>static single assignment form</u> (SSA), meaning that each <u>variable</u> (called a typed register) is assigned once and then frozen. This helps simplify the analysis of dependencies among variables. LLVM allows code to be compiled statically, as it is under the traditional GCC system, or left for late-compiling from the IR to machine code via <u>just-in-time compilation</u> (JIT), similar to <u>Java</u>. The type system consists of basic types such as <u>integer</u> or <u>floating-point</u> numbers and five <u>derived types</u>: <u>pointers</u>, <u>arrays</u>, <u>vectors</u>, <u>structures</u>, and <u>functions</u>. A type construct in a concrete language can be represented by combining these basic types in LLVM. For example, a class in C++ can be represented by a mix of structures, functions and arrays of function pointers.

The LLVM JIT compiler can optimize unneeded static branches out of a program at runtime, and thus is useful for <u>partial evaluation</u> in cases where a program has many options, most of which can easily be determined unneeded in a specific environment. This feature is used in the <u>OpenGL</u> pipeline of <u>Mac OS X</u> Leopard (v10.5) to provide support for missing hardware features. [22]

Graphics code within the OpenGL stack can be left in intermediate representation and then compiled when run on the target machine. On systems with high-end graphics processing units (GPUs), the resulting code remains quite thin, passing the instructions on to the GPU with minimal changes. On systems with low-end GPUs, LLVM will compile optional procedures that run on the local central processing unit (CPU) that

emulate instructions that the GPU cannot run internally. LLVM improved performance on low-end machines using <u>Intel GMA</u> chipsets. A similar system was developed under the <u>Gallium3D</u> LLVMpipe, and incorporated into the GNOME shell to allow it to run without a proper 3D hardware driver loaded. [23]

For run-time performance of the compiled programs, GCC formerly outperformed LLVM by 10% on average in 2011. [24][25] Newer results in 2013 indicate that LLVM has now caught up with GCC in this area, and is now compiling binaries of approximately equal performance. [26]

# **Components**

LLVM has become an umbrella project containing multiple components.

#### **Front ends**

LLVM was originally written to be a replacement for the existing <u>code generator</u> in the GCC stack, [27] and many of the GCC front ends have been modified to work with it, resulting in the now-defunct LLVM-GCC suite. The modifications generally involve a <u>GIMPLE</u>-to-LLVM IR step so that LLVM optimizers and codegen can be used instead of GCC's GIMPLE system. Apple was a significant user of LLVM-GCC through <u>Xcode</u> 4.x (2013). [28][29] This use of the GCC frontend was considered mostly a temporary measure, but with the advent of <u>Clang</u> and advantages of LLVM and Clang's modern and modular codebase (as well as compilation speed), is mostly obsolete.

LLVM currently supports compiling of <u>Ada</u>, <u>C</u>, <u>C++</u>, <u>D</u>, <u>Delphi</u>, <u>Fortran</u>, <u>Haskell</u>, <u>Julia</u>, <u>Objective-C</u>, <u>Rust</u>, and Swift using various front ends.

Widespread interest in LLVM has led to several efforts to develop new front ends for a variety of languages. The one that has received the most attention is Clang, a new compiler supporting C, C++, and Objective-C. Primarily supported by Apple, Clang is aimed at replacing the C/Objective-C compiler in the GCC system with a system that is more easily integrated with <u>integrated development environments</u> (IDEs) and has wider support for <u>multithreading</u>. Support for <u>OpenMP</u> directives has been included in <u>Clang</u> since release 3.8. [30]

The Utrecht <u>Haskell</u> compiler can generate code for LLVM. Though the generator is in the early stages of development, in many cases it has been more efficient than the C code generator. There is a <u>Glasgow Haskell Compiler</u> (GHC) backend using LLVM that achieves a 30% speed-up of the compiled code relative to native code compiling via GHC or C code generation followed by compiling, missing only one of the many optimizing techniques implemented by the GHC.

Many other components are in various stages of development, including, but not limited to, the <u>Rust</u> compiler, a <u>Java bytecode</u> front end, a <u>Common Intermediate Language</u> (CIL) front end, the <u>MacRuby</u> implementation of Ruby 1.9, various front ends for <u>Standard ML</u>, and a new <u>graph coloring</u> register allocator.

## Intermediate representation

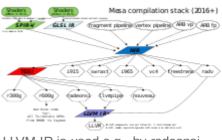
The core of LLVM is the <u>intermediate representation</u> (IR), a low-level programming language similar to assembly. IR is a strongly typed <u>reduced instruction set computing</u> (RISC) instruction set which abstracts away most details of the target. For example, the calling convention is abstracted through *call* and *ret* instructions with explicit arguments. Also, instead of a fixed set of registers, IR uses an infinite set of temporaries of the form %0, %1, etc. LLVM supports three equivalent forms of IR: a human-readable

assembly format, an in-memory format suitable for frontends, and a dense bitcode format for serializing. A simple "Hello, world!" program in the IR format: [33]

```
@.str = internal constant [14 x i8] c"hello,
world\0A\00"

declare i32 @printf(i8*, ...)

define i32 @main(i32 %argc, i8** %argv) nounwind {
  entry:
        %tmp1 = getelementptr [14 x i8], [14 x i8]* @.str,
        i32 0, i32 0
        %tmp2 = call i32 (i8*, ...) @printf( i8* %tmp1 )
        nounwind
        ret i32 0
}
```



LLVM IR is used e.g., by radeonsi and by Ilvmpipe. Both are part of Mesa 3D.

The many different conventions used and features provided by different targets mean that LLVM cannot truly produce a target-independent IR and retarget it without breaking some established rules. Examples of target dependence beyond what is explicitly mentioned in the documentation can be found in a 2011 proposal for "wordcode", a fully target-independent variant of LLVM IR intended for online distribution. [34] A more practical example is PNaCl. [35]

#### **Back ends**

At version 3.4, LLVM supports many instruction sets, including ARM, Qualcomm Hexagon, MIPS, Nvidia Parallel Thread Execution (PTX; called *NVPTX* in LLVM documentation), PowerPC, AMD TeraScale, AMD Graphics Core Next (GCN), SPARC, z/Architecture (called *SystemZ* in LLVM documentation), x86, x86-64, and XCore. Some features are not available on some platforms. Most features are present for x86, x86-64, z/Architecture, ARM, and PowerPC. RISC-V is supported as of version 7. In the past LLVM also supported fully or partially other backends, including C backend, Cell SPU, mblaze (MicroBlaze), AMD R600, DEC/Compaq Alpha (Alpha AXP) and Nios2, but most of this hardware is mostly obsolete, and LLVM developers decided the support and maintenance costs were no longer justified.

LLVM also supports <u>WebAssembly</u> as a target, enabling compiled programs to execute in WebAssembly-enabled environments such as <u>Google Chrome</u> / <u>Chromium</u>, <u>Firefox</u>, <u>Microsoft Edge</u>, <u>Apple Safari</u> or <u>WAVM</u>. LLVM-compliant WebAssembly compilers typically support mostly unmodified source code written in C, C++, D, Rust, Nim, Kotlin and several other languages.

The LLVM machine code (MC) subproject is LLVM's framework for translating machine instructions between textual forms and machine code. Formerly, LLVM relied on the system assembler, or one provided by a toolchain, to translate assembly into machine code. LLVM MC's integrated assembler supports most LLVM targets, including x86, x86-64, ARM, and ARM64. For some targets, including the various MIPS instruction sets, integrated assembly support is usable but still in the beta stage.

#### Linker

The lld subproject is an attempt to develop a built-in, platform-independent <u>linker</u> for LLVM. <u>[41]</u> lld aims to remove dependence on a third-party linker. As of May 2017, lld supports <u>ELF</u>, <u>PE/COFF</u>, <u>Mach-O</u>, and WebAssembly <u>[42]</u> in descending order of completeness. lld is faster than both flavors of GNU ld. <u>[41]</u>

Unlike the GNU linkers, lld has built-in support for <u>link-time optimization</u>. This allows for faster code generation as it bypasses the use of a linker plugin, but on the other hand prohibits interoperability with other flavors of LTO.<sup>[43]</sup>

## C++ Standard Library

The LLVM project includes an implementation of the  $\underline{C++}$  Standard Library called libc++, dual-licensed under the MIT License and the UIUC license. [44]

Since v9.0.0, it was relicensed to the Apache License 2.0 with LLVM Exceptions. [3]

## **Polly**

This implements a suite of cache-locality optimizations as well as auto-parallelism and vectorization using a polyhedral model. [45]

## Debugger

### **Derivatives**

Due to its permissive license, many vendors release their own tuned forks of LLVM. This is officially recognized by LLVM's documentation, which suggests against using version numbers in feature checks for this reason. [46] Some of the vendors include:

- AMD's AMD Optimizing C/C++ Compiler is based on LLVM, Clang, and Flang.
- Apple maintains an open-source fork for Xcode. [47]
- ARM maintains a fork of LLVM 9 as the "Arm Compiler".
- Intel has adopted LLVM for their next generation Intel C++ Compiler. [48]
- The Los Alamos National Laboratory has a parallel-computing fork of LLVM 8 called "Kitsune". [49]
- Since 2013, Sony has been using LLVM's primary front-end Clang compiler in the <u>software</u> development kit (SDK) of its PlayStation 4 console.
- <u>Nvidia</u> uses LLVM in the implementation of its NVVM <u>CUDA</u> Compiler. [51] The NVVM compiler is distinct from the "NVPTX" backend mentioned in the <u>Backends section</u>, although both generate PTX code for Nvidia GPUs.
- IBM is adopting LLVM in its C/C++ and Fortran compilers. [52]

## See also

- HHVM
- C--
- Amsterdam Compiler Kit (ACK)
- LLDB (debugger)
- GNU lightning
- GNU Compiler Collection (GCC)
- Pure

- OpenCL
- Emscripten
- TenDRA Distribution Format
- Architecture Neutral Distribution Format (ANDF)
- Comparison of application virtual machines
- SPIR-V
- University of Illinois at Urbana Champaign discoveries & innovations

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## **External links**

Official website (https://llvm.org)

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