Apple Array Allocation

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Abstract

Here we present statically determined memory allocation for flat, immutable arrays used in the Apple array system, a compiler for an expression-oriented functional language. Array languages like J and APL suffer from a lack of embedability in implementations. Adroit memory management can make embedding easier; one would like to avoid thinking about ownership across two garbage collectors. Arrays are usable as pointers, with no dependence on a garbage collector. Ownership is simple and Apple code does not constrain memory management in the host language. Two embeddings—one in Python and one in R—are exhibited.

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1 Introduction

Array languages such as APL and J suffer from undeserved obscurity [3]. In order to make them more useful to enthusiasts, one might hope to embed them, thus providing access to libraries.

APL implementations typically use reference-counting [4, p. 47] for performance reasons, but this imposes on the calling code—one must increment the reference count and delegate each array to a garbage collector.

1.1 Background

Rather than comparing to competitors such as NumPy [2], we take inspiration from C—we would like an array language that does not impose on its host language [5]. C is almost vulgar in offering pointers as arrays; the typical APL implementation uses reference-counting [4, p. 47] for performance reasons.

By compiling high-level array expressions to a C procedure with all allocations and frees predetermined, we reduce the demands of calling Apple code in other environments.

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1.2 Apple Array System

The Apple JIT compiler is a function

taking a pointer to malloc, a pointer to free, and source code as input and returning machine code; the compiler only generates these two foreign function calls and there is no runtime system.

This simplifies linking in the JIT

1.2.1 Flat Array Types. Apple arrays are completely flat in memory, consisting of rank, dimensions, and data laid out contiguously.

A 2x4 array:

0	1	2	3	4	5	6	7	8	9	10	
2	2	4	a_{00}	a_{01}	a_{02}	a_{03}	a_{10}	a_{11}	a_{12}	a_{13}	

All elements are flat; there are only two primitive types: 64-bit integers and 64-bit floats. Arrays of tuples are supported but not arrays of tuples of arrays; there are no user-defined types. Arrays of arrays are treated as higher-dimensional arrays.

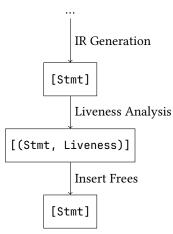
Such constraints, with extra bookkeeping information added during IR generation, are responsible for the surprising fact that memory allocation can be completely determined at compile time. Though such limited types may seem spartan to a functional programmer, this is enough to stand toe-to-toe with NumPy.

1.2.2 Language. The language does not support recursion; all looping is implicit via maps, folds, etc.

2 Method

2.1 Compiler Pipeline

Our work is based on established liveness algorithms; we define uses and defs on statements (in terms of arrays), compute liveness [1, pp. 213-216], and then construct live intervals [6].



data Exp = Const Int

The IR used in the Apple compiler is sequences of statements and expressions, viz.

```
| Reg Temp
         | At ArrayPtr
data Stmt = MovTemp Temp Exp
          | Write ArrayPtr Exp
          | Malloc Lbl Temp Exp -- label, register,
          | CondJump Exp Loc
          | Jump Loc | Label Loc
          | Free Temp
```

Expressions can read from memory via At and Stmts make use of memory access for writes, allocations etc. A function

```
aeval :: Expr -> IRM (Temp, Lbl, [Stmt])
```

translates an array expression, assigning a Lbl for subsequent access and associating the labeled array with a Temp to be used to free it.

All accesses to an array use an ArrayPtr, which specifies the Lbl for tracking within the compiler.

```
-- register, offset, label
data ArrayPtr = ArrayPtr Temp (Maybe Exp) Lbl
 Then we have:
defs :: Stmt -> Set Lbl
```

```
defs (Malloc l _ e) = singleton l
defs _
                    = empty
uses :: Stmt -> Set Lbl
uses (Write (ArrayPtr _ _ l) e) = insert l (defsE e)
uses ...
```

Note that with such labels one can access the array from different Temps; this is necessary for generating efficient

After liveness analysis, we have liveness information for each Stmt, viz.

```
data Liveness =
```

```
Liveness { ins :: Set Lbl, outs :: Set Lbl }
Then we can define done :: Stmt \rightarrow Set Lbl for each
```

array as the last Stmt

We simply insert a free when the live interval for an array ends. Since liveness is tracked per-array, we look up the Temp

2.2 Generation

We must take care when arranging loops; the loop should exit by continuing rather than jumping to an exit location. This ensures that the frees inserted at the end of live intervals are always reachable.

For instance, we do not write:

```
apple 0:
(condjump (< (reg r_2) (int 2)) apple_1)</pre>
(movtemp r_0
  0(ptr r_1+(+ (asl (reg r_2) (int 3)) (int 16))))
(jump apple_0)
apple_1:
  But rather:
(condjump (\geq (reg r_2) (int 2)) apple_1)
size
apple_0:
(movtemp r_0
  0(ptr r_1+(+ (asl (reg r_2) (int 3)) (int 16))))
(condjump (< (reg r_2) (int 2) apple_0)</pre>
apple_1:
  Had we written the former, the free would be unreach-
able, viz.
apple_0:
(condjump (< (reg r_2) (int 2)) apple_1)
```

apple_1:

(movtemp r_0

(free r_2)

(jump apple_0)

This is not the case with the latter: (condjump (\geq (reg r_2) (int 2)) apple_1)

```
apple 0:
(movtemp r_0
  @(ptr r_1+(+ (asl (reg r_2) (int 3)) (int 16))))
(condjump (< (reg r_2) (int 2) apple_0)</pre>
(free r_2)
```

@(ptr r_1+(+ (asl (reg r_2) (int 3)) (int 16))))

apple_1:

Since jumps are only generated by the compiler in only a few cases, we can guarantee that generated code is correct by being careful.

3 Embeddings

Apple has been embedded in Python and R. Observe the following interactions:

```
>>> import apple
>>> area=apple.jit('''
λas.λbs.
    \{ \Sigma \in [(+)/x] \}
    ; 0.5*abs.(\Sigma ((*)`as (1\Thetabs)) - \Sigma ((*)`(1\Thetaas) bs))
''')
>>> import numpy as np
>>> apple.f(area,np.array([0,0,3.]),np.array([0,4,4.
> source("./apple.R")
> shoelace<-jit("
λas.λbs.
    \{ \Sigma \in [(+)/x] \}
")
> run(shoelace,c(0,0,3),c(0,4,4))
[1] 6
```

Thus we can run Apple code on NumPy arrays and R vectors without frustrating the garbage collector.

```
6.0
```

Apple code can be called from C on its own terms, unlike

```
NumPy or J, viz.
#include <stdio.h>
#include <stdlib.h>
#define R return
#define D0(i,n,a) {I i;for(i=0;i<n;i++){a;}}
typedef double F;typedef int64_t I; typedef void* U;
typedef struct Af {I rnk; I* dim; F* xs;} Af;
U poke_af (Af x) {
    I rnk = x.rnk;
    I t = 1;
    DO(i,rnk,t*=x.dim[i]);
    U p = malloc(8+8*x.rnk+8*t);
    I* i_p = p; F* f_p = p;
    *i_p = rnk;
    DO(i,rnk,i_p[i+1]=x.dim[i]);
    DO(i,t,f_p[i+1+rnk]=x.xs[i]);
    R p;
}
extern F shoelace(U, U);
```

```
int main(int argc, char *argv[]) {
    F xs[] = \{0,4,4\};
    F ys[] = \{0,0,3\};
    I d[] = {3};
    Af a = \{1,d,xs\};
    Af b = \{1, d, ys\};
    U x = poke_af(a); U y = poke_af(b);
    printf("%f\n", shoelace(x,y));
    free(x); free(y);
```

4 Coda])) Apple has the potential to be far more efficient; one could consolidate allocations, e.g.

```
irange 0 20 1 ++ irange 0 10 1
```

performs one allocation for each irange but this could be consolidated into one—irange 0 20 1 is inferred to have ; 0.5*abs.(Σ ((*)`as (1 Θ bs)) - Σ ((*)`(1 Θ as) bs))type Vec 20 int in the existing compiler; with liveness and size information one could do something like linear register allocation in which arrays are assigned to memory slots.

> This vindicates flat arrays; while banning arrays of functions &c. may not be obvious to a functional programmer, studying this particular set of constraints has potential; one could use it to implement a compiler for a high-level language that produces performant and portable code.

> $\{\text{shoelace} \leftarrow \lambda \text{as.} \lambda \text{bs.} \{\Sigma \leftarrow [(+)/x]; 0.5*\text{abs.}(\Sigma ((*)`as (1\text{\text{\text{$}}}bs)) - \Sigma ((*)`(1\text{\text{$}}as) bs))\}; shoelace <math>\langle 0,0,3 \rangle \langle 0,4,4 \rangle = 0$

References

- [1] Andrew Appel. 1998. Modern Compiler Implementation in ML. Cambridge University Press.
- [2] Charles R. Harris, K. Jarrod Millman, Stéfan J. van der Walt, Ralf Gommers, Pauli Virtanen, David Cournapeau, Eric Wieser, Julian Taylor, Sebastian Berg, Nathaniel J. Smith, Robert Kern, Matti Picus, Stephan Hoyer, Marten H. van Kerkwijk, Matthew Brett, Allan Haldane, Jaime Fernández del Río, Mark Wiebe, Pearu Peterson, Pierre Gérard-Marchant, Kevin Sheppard, Tyler Reddy, Warren Weckesser, Hameer Abbasi, Christoph Gohlke, and Travis E. Oliphant. 2020. Array programming with NumPy. Nature 585, 7825 (2020), 357-362. https://doi.org/10.1038/s41586-020-2649-2
- [3] Aaron W. Hsu and Rodrigo Girão Serrão. 2023. U-Net CNN in APL: Exploring Zero-Framework, Zero-Library Machine Learning. In Proceedings of the 9th ACM SIGPLAN International Workshop on Libraries, Languages and Compilers for Array Programming (Orlando, FL, USA) (ARRAY 2023). Association for Computing Machinery, New York, NY, USA, 22-35. https://doi.org/10.1145/3589246.3595371
- [4] Roger K. W. Hui and Morten J. Kromberg. 2020. APL since 1978. Proc. ACM Program. Lang. 4, HOPL, Article 69 (jun 2020), 108 pages. https: //doi.org/10.1145/3386319
- [5] Stephen Kell. 2017. Some were meant for C: the endurance of an unmanageable language. In Proceedings of the 2017 ACM SIGPLAN International Symposium on New Ideas, New Paradigms, and Reflections on Programming and Software (Vancouver, BC, Canada) (Onward! 2017). Association for Computing Machinery, New York, NY, USA, 229-245. https://doi.org/10.1145/3133850.3133867
- [6] Massimiliano Poletto and Vivek Sarkar. 1999. Linear scan register allocation. ACM Trans. Program. Lang. Syst. 21, 5 (sep 1999), 895-913. https://doi.org/10.1145/330249.330250