
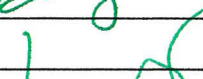




Group Number: 30
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Marks		
2	1. <u>Schematic diagram for the 6-bit comparator</u>	
2	2. <u>VHDL file for the 6-bit comparator</u>	
2	3. <u>Initial partial simulation results for the 6-bit comparator</u>	
2	4. <u>Complete simulation results for the 6-bit comparator</u>	
2	5. <u>VHDL description for the 16:4 encoder circuit</u>	
2	6. <u>Simulation Testbench VHDL for the 16:4 encoder circuit</u>	
7	7. <u>Simulation results for the 16:4 encoder circuit</u>	
		TA Signatures

Each part should be demonstrated to one of the TAs who will then give a grade and sign the grade sheet. Grades for each part will be either 0, 1, or 2. A mark of 2 will be given if everything is done correctly. A grade of 1 will be given if there are significant problems, but an attempt was made. A grade of 0 will be given for parts that were not done at all, or for which there is no TA signature.